

# AX028010 Modbus Address Map

Version 1. For Firmware V1.xx

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Bit/Reg Ad	Modbus Protocol Address		Number Of Register	Name	Format	Default	Range	Units or Resolution	Access	Description
	Dec	Hex								
<b>Input Section</b>										
<b>Universal Inputs</b>										
301025	1024	0x0400	1	Digital Input State	Word	N/A	See output config	See config	RO	
301026	1025	0x0401	2	Universal Input #1	Float	N/A	See output config	See config	RO	
301028	1027	0x0403	2	Universal Input #2	Float	N/A	See output config	See config	RO	
301030	1029	0x0405	2	Universal Input #3	Float	N/A	See output config	See config	RO	
301032	1031	0x0407	2	Universal Input #4	Float	N/A	See output config	See config	RO	
301034	1033	0x0409	2	Universal Input #5	Float	N/A	See output config	See config	RO	
301036	1035	0x040B	2	Universal Input #6	Float	N/A	See output config	See config	RO	
301038	1037	0x040D	2	Universal Input #7	Float	N/A	See output config	See config	RO	
301040	1039	0x040F	2	Universal Input #8	Float	N/A	See output config	See config	RO	
301042	1041	0x0411	2	Universal Input #9	Float	N/A	See output config	See config	RO	
301044	1043	0x0413	2	Universal Input #10	Float	N/A	See output config	See config	RO	
301046	1045	0x0415	2	Universal Input #11	Float	N/A	See output config	See config	RO	
301048	1047	0x0417	2	Universal Input #12	Float	N/A	See output config	See config	RO	
301040	1049	0x0419	5	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0
<b>Proportional Output Target Value</b>										
301055	1054	0x041E	2	Proportional Output Target #1	Float	N/A	See output config	See config	RO	
301057	1056	0x0420	2	Proportional Output Target #2	Float	N/A	See output config	See config	RO	
301059	1058	0x0422	2	Proportional Output Target #3	Float	N/A	See output config	See config	RO	
301061	1060	0x0424	2	Proportional Output Target #4	Float	N/A	See output config	See config	RO	
301063	1062	0x0426	2	Proportional Output Target #5	Float	N/A	See output config	See config	RO	
301065	1064	0x0428	2	Proportional Output Target #6	Float	N/A	See output config	See config	RO	
301067	1066	0x042A	2	Proportional Output Target #7	Float	N/A	See output config	See config	RO	
301069	1068	0x042C	2	Proportional Output Target #8	Float	N/A	See output config	See config	RO	
301071	1070	0x042E	2	Proportional Output Target #9	Float	N/A	See output config	See config	RO	
301073	1072	0x0430	2	Proportional Output Target #10	Float	N/A	See output config	See config	RO	
301075	1074	0x0432	2	Proportional Output Target #11	Float	N/A	See output config	See config	RO	
301077	1076	0x0434	2	Proportional Output Target #12	Float	N/A	See output config	See config	RO	
301079	1078	0x0436	6	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0
<b>Proportional Output Feedback Value</b>										
301085	1084	0x043C	2	Proportional Output Feedback #1	Float	N/A	See output config	See config	RO	
301087	1086	0x043E	2	Proportional Output Feedback #2	Float	N/A	See output config	See config	RO	
301089	1088	0x0440	2	Proportional Output Feedback #3	Float	N/A	See output config	See config	RO	
301091	1090	0x0442	2	Proportional Output Feedback #4	Float	N/A	See output config	See config	RO	
301093	1092	0x0444	2	Proportional Output Feedback #5	Float	N/A	See output config	See config	RO	
301095	1094	0x0446	2	Proportional Output Feedback #6	Float	N/A	See output config	See config	RO	
301097	1096	0x0448	2	Proportional Output Feedback #7	Float	N/A	See output config	See config	RO	
301099	1098	0x044A	2	Proportional Output Feedback #8	Float	N/A	See output config	See config	RO	
301101	1100	0x044C	2	Proportional Output Feedback #9	Float	N/A	See output config	See config	RO	
301103	1102	0x044E	2	Proportional Output Feedback #10	Float	N/A	See output config	See config	RO	
301105	1104	0x0450	2	Proportional Output Feedback #11	Float	N/A	See output config	See config	RO	
301107	1106	0x0452	2	Proportional Output Feedback #12	Float	N/A	See output config	See config	RO	
301109	1108	0x0454	6	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0
<b>PID Function Block</b>										

301115	1114	0x045A	2	PID Output #1	Float	N/A	See output config	See config	RO	
301117	1116	0x045C	2	PID Output #2	Float	N/A	See output config	See config	RO	
301119	1118	0x045E	2	PID Output #3	Float	N/A	See output config	See config	RO	
301121	1120	0x0460	2	PID Output #4	Float	N/A	See output config	See config	RO	
301123	1122	0x0462	2	PID Output #5	Float	N/A	See output config	See config	RO	
301125	1124	0x0464	2	PID Output #6	Float	N/A	See output config	See config	RO	
301127	1126	0x0466	2	PID Output #7	Float	N/A	See output config	See config	RO	
301129	1128	0x0468	2	PID Output #8	Float	N/A	See output config	See config	RO	
301131	1130	0x046A	2	PID Output #9	Float	N/A	See output config	See config	RO	
301133	1132	0x046C	2	PID Output #10	Float	N/A	See output config	See config	RO	
301135	1134	0x046E	2	PID Output #11	Float	N/A	See output config	See config	RO	
301137	1136	0x0470	2	PID Output #12	Float	N/A	See output config	See config	RO	
301139	1138	0x0472	6	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0
<b>Constant Data</b>										
301145	1144	0x0478	2	Constant FALSE	Float	N/A	See output config	See config	RO	
301147	1146	0x047A	2	Constant TRUE	Float	N/A	See output config	See config	RO	
301149	1148	0x047C	2	Constant Data #3	Float	N/A	See output config	See config	RO	
301151	1150	0x047E	2	Constant Data #4	Float	N/A	See output config	See config	RO	
301153	1152	0x0480	2	Constant Data #5	Float	N/A	See output config	See config	RO	
301155	1154	0x0482	2	Constant Data #6	Float	N/A	See output config	See config	RO	
301157	1156	0x0484	2	Constant Data #7	Float	N/A	See output config	See config	RO	
301159	1158	0x0486	2	Constant Data #8	Float	N/A	See output config	See config	RO	
301161	1160	0x0488	2	Constant Data #9	Float	N/A	See output config	See config	RO	
301163	1162	0x048A	2	Constant Data #10	Float	N/A	See output config	See config	RO	
301165	1164	0x048C	2	Constant Data #11	Float	N/A	See output config	See config	RO	
301167	1166	0x048E	2	Constant Data #12	Float	N/A	See output config	See config	RO	
301169	1168	0x0490	2	Constant Data #13	Float	N/A	See output config	See config	RO	
301171	1170	0x0492	2	Constant Data #14	Float	N/A	See output config	See config	RO	
301173	1172	0x0494	2	Constant Data #15	Float	N/A	See output config	See config	RO	
301175	1174	0x0496	5	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0
<b>Lookup Table Output</b>										
301180	1179	0x049B	2	Lookup Table Output #1	Float	N/A	See output config	See config	RO	
301182	1181	0x049D	2	Lookup Table Output #2	Float	N/A	See output config	See config	RO	
301184	1183	0x049F	2	Lookup Table Output #3	Float	N/A	See output config	See config	RO	
301186	1185	0x04A1	2	Lookup Table Output #4	Float	N/A	See output config	See config	RO	
301188	1187	0x04A3	2	Lookup Table Output #5	Float	N/A	See output config	See config	RO	
301190	1189	0x04A5	2	Lookup Table Output #6	Float	N/A	See output config	See config	RO	
301192	1191	0x04A7	2	Lookup Table Output #7	Float	N/A	See output config	See config	RO	
301194	1193	0x04A9	2	Lookup Table Output #8	Float	N/A	See output config	See config	RO	
301196	1195	0x04AB	2	Lookup Table Output #9	Float	N/A	See output config	See config	RO	
301198	1197	0x04AD	2	Lookup Table Output #10	Float	N/A	See output config	See config	RO	
301200	1199	0x04AF	5	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0
<b>Programmable Logic Output</b>										
301205	1204	0x04B4	2	Programmable Logic Output #1	Float	N/A	See output config	See config	RO	
301207	1206	0x04B6	2	Programmable Logic Output #2	Float	N/A	See output config	See config	RO	
301209	1208	0x04B8	2	Programmable Logic Output #3	Float	N/A	See output config	See config	RO	
301211	1210	0x04BA	2	Programmable Logic Output #4	Float	N/A	See output config	See config	RO	
301213	1212	0x04BC	102	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0
<b>Math Output</b>										
301220	1219	0x04C3	2	Math Output #1	Float	N/A	See output config	See config	RO	
301222	1221	0x04C5	2	Math Output #2	Float	N/A	See output config	See config	RO	

301224	1223	0x04C7	2	Math Output #3	Float	N/A	See output config	See config	RO	
301226	1225	0x04C9	2	Math Output #4	Float	N/A	See output config	See config	RO	
301228	1227	0x04CB	2	Math Output #5	Float	N/A	See output config	See config	RO	
301230	1229	0x04CD	5	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0
<b>Conditional Logic Output</b>										
301235	1234	0x04D2	2	Conditional Logic Output #1	Float	N/A	See output config	See config	RO	
301237	1236	0x04D4	2	Conditional Logic Output #2	Float	N/A	See output config	See config	RO	
301239	1238	0x04D6	2	Conditional Logic Output #3	Float	N/A	See output config	See config	RO	
301241	1240	0x04D8	2	Conditional Logic Output #4	Float	N/A	See output config	See config	RO	
301243	1242	0x04DA	2	Conditional Logic Output #5	Float	N/A	See output config	See config	RO	
301245	1244	0x04DC	2	Conditional Logic Output #6	Float	N/A	See output config	See config	RO	
301247	1246	0x04DE	2	Conditional Logic Output #7	Float	N/A	See output config	See config	RO	
301249	1248	0x04E0	2	Conditional Logic Output #8	Float	N/A	See output config	See config	RO	
301251	1250	0x04E2	2	Conditional Logic Output #9	Float	N/A	See output config	See config	RO	
301253	1252	0x04E4	2	Conditional Logic Output #10	Float	N/A	See output config	See config	RO	
301255	1254	0x04E6	5	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0
<b>Set-Reset Latch Output</b>										
301260	1259	0x04EB	2	Set-Reset Latch Output #1	Float	N/A	See output config	See config	RO	
301262	1261	0x04ED	2	Set-Reset Latch Output #2	Float	N/A	See output config	See config	RO	
301264	1263	0x04EF	2	Set-Reset Latch Output #3	Float	N/A	See output config	See config	RO	
301266	1265	0x04F1	2	Set-Reset Latch Output #4	Float	N/A	See output config	See config	RO	
301268	1267	0x04F3	2	Set-Reset Latch Output #5	Float	N/A	See output config	See config	RO	
301270	1269	0x04F5	5	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0
<b>DTC React</b>										
301275	1274	0x04FA	2	DTC React Output #1	Float	N/A	See output config	See config	RO	
301277	1276	0x04FC	2	DTC React Output #2	Float	N/A	See output config	See config	RO	
301279	1278	0x04FE	2	DTC React Output #3	Float	N/A	See output config	See config	RO	
301281	1280	0x0500	2	DTC React Output #4	Float	N/A	See output config	See config	RO	
301283	1282	0x0502	2	DTC React Output #5	Float	N/A	See output config	See config	RO	
301285	1284	0x0504	2	DTC React Output #6	Float	N/A	See output config	See config	RO	
301287	1286	0x0506	2	DTC React Output #7	Float	N/A	See output config	See config	RO	
301289	1288	0x0508	2	DTC React Output #8	Float	N/A	See output config	See config	RO	
301291	1290	0x050A	2	DTC React Output #9	Float	N/A	See output config	See config	RO	
301293	1292	0x050C	2	DTC React Output #10	Float	N/A	See output config	See config	RO	
301295	1294	0x050E	2	DTC React Output #11	Float	N/A	See output config	See config	RO	
301297	1296	0x0510	2	DTC React Output #12	Float	N/A	See output config	See config	RO	
301299	1298	0x0512	2	DTC React Output #13	Float	N/A	See output config	See config	RO	
301301	1300	0x0514	2	DTC React Output #14	Float	N/A	See output config	See config	RO	
301303	1302	0x0516	2	DTC React Output #15	Float	N/A	See output config	See config	RO	
301305	1304	0x0518	2	DTC React Output #16	Float	N/A	See output config	See config	RO	
301307	1306	0x051A	2	DTC React Output #17	Float	N/A	See output config	See config	RO	
301309	1308	0x051C	6	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0
<b>CAN Inputs</b>										
301315	1314	0x0522	2	CAN Receive Signal #1	Float	N/A	See input config	See config	RO	
301317	1316	0x0524	2	CAN Receive Signal #2	Float	N/A	See input config	See config	RO	
301319	1318	0x0526	2	CAN Receive Signal #3	Float	N/A	See input config	See config	RO	
301321	1320	0x0528	2	CAN Receive Signal #4	Float	N/A	See input config	See config	RO	
301323	1322	0x052A	2	CAN Receive Signal #5	Float	N/A	See input config	See config	RO	
301325	1324	0x052C	2	CAN Receive Signal #6	Float	N/A	See input config	See config	RO	
301327	1326	0x052E	2	CAN Receive Signal #7	Float	N/A	See input config	See config	RO	
301329	1328	0x0530	2	CAN Receive Signal #8	Float	N/A	See input config	See config	RO	

301331	1330	0x0532	2	CAN Receive Signal #9	Float	N/A	See input config	See config	RO	
301333	1332	0x0534	2	CAN Receive Signal #10	Float	N/A	See input config	See config	RO	
301335	1334	0x0536	21	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0
<b>Modbus Receive Inputs</b>										
301340	1339	0x053B	2	Modbus Receive Data #1	Float	N/A	See input config	See config	RO	
301342	1341	0x053D	2	Modbus Receive Data #2	Float	N/A	See input config	See config	RO	
301344	1343	0x053F	2	Modbus Receive Data #3	Float	N/A	See input config	See config	RO	
301346	1345	0x0541	2	Modbus Receive Data #4	Float	N/A	See input config	See config	RO	
301348	1347	0x0543	2	Modbus Receive Data #5	Float	N/A	See input config	See config	RO	
301350	1349	0x0545	2	Modbus Receive Data #6	Float	N/A	See input config	See config	RO	
301352	1351	0x0547	2	Modbus Receive Data #7	Float	N/A	See input config	See config	RO	
301354	1353	0x0549	2	Modbus Receive Data #8	Float	N/A	See input config	See config	RO	
301356	1355	0x054B	2	Modbus Receive Data #9	Float	N/A	See input config	See config	RO	
301358	1357	0x054D	2	Modbus Receive Data #10	Float	N/A	See input config	See config	RO	
301360	1359	0x054F	5	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0
<b>Auxiliary Signals</b>										
301365	1364	0x0554	2	Supply Voltage	Float	N/A	Not Rated	V	RO	Covers rated supply voltage range
301367	1366	0x0556	2	Microcontroller Temperature	Float	N/A	Not Rated	Deg.C	RO	Covers rated temperature range
<b>Configuration Section</b>										
<b>Universal Input #1</b>										
401025	1024	0x0400	1	Input Type	Word	1 - Voltage	0 - Disabled1 - Voltage Input2	N/A	R/W	Resistive Input is available for the Universal Inputs
401026	1025	0x0401	1	Voltage Range	Word	0 - 0...5V	See output config	See config	R/W	
401027	1026	0x0402	1	Current Range	Word	0 - 0...20mA	See output config	See config	R/W	
401028	1027	0x0403	2	Input Range Min	Float	0	See output config	See config	R/W	
401030	1029	0x0405	2	Input Range Max	Float	5	See output config	See config	R/W	
401032	1031	0x0407	1	Voltage LoZ Input	Word	0	See output config	See config	R/W	
401033	1032	0x0408	1	Analog Input Filter	Word	1000ms	See output config	See config	R/W	
401034	1033	0x0409	1	Pull-Up/Pull-Down Resistor	Word	1000ms	See output config	See config	R/W	
401035	1034	0x040A	1	Input Polarity	Word	500Hz	See output config	See config	R/W	
401036	1035	0x040B	1	Discrete Input Debounce Time	Byte	1 - High	See output config	See config	R/W	
401037	1036	0x040C	1	Frequency Range	Word	500mA	See output config	See config	R/W	
401038	1037	0x040D	1	Frequency/PWM Debounce Filter	Word	1000mA	See output config	See config	R/W	
401039	1038	0x040E	1	Frequency/PWM Debounce Averaging	Word	1000ms	See output config	See config	R/W	
401040	1039	0x040F	1	Software Filter Type	Byte	0	See output config	See config	R/W	
401041	1040	0x0410	1	Software Filter Constant	Word	10	See output config	See config	R/W	
401042	1041	0x0411	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Universal Input #2</b>										
401050	1049	0x0419	1	Input Type	Word	1 - Voltage	0 - Disabled1 - Voltage Input2	N/A	R/W	Resistive Input is available for the Universal Inputs
401051	1050	0x041A	1	Voltage Range	Word	0 - 0...5V	See output config	See config	R/W	
401052	1051	0x041B	1	Current Range	Word	0 - 0...20mA	See output config	See config	R/W	
401053	1052	0x041C	2	Input Range Min	Float	0	See output config	See config	R/W	
401055	1054	0x041E	2	Input Range Max	Float	5	See output config	See config	R/W	
401057	1056	0x0420	1	Voltage LoZ Input	Word	0	See output config	See config	R/W	
401058	1057	0x0421	1	Analog Input Filter	Word	1000ms	See output config	See config	R/W	
401059	1058	0x0422	1	Pull-Up/Pull-Down Resistor	Word	1000ms	See output config	See config	R/W	
401060	1059	0x0423	1	Input Polarity	Word	500Hz	See output config	See config	R/W	
401061	1060	0x0424	1	Discrete Input Debounce Time	Byte	1 - High	See output config	See config	R/W	
401062	1061	0x0425	1	Frequency Range	Word	500mA	See output config	See config	R/W	
401063	1062	0x0426	1	Frequency/PWM Debounce Filter	Word	1000mA	See output config	See config	R/W	
401064	1063	0x0427	1	Frequency/PWM Debounce Averaging	Word	1000ms	See output config	See config	R/W	
401065	1064	0x0428	1	Software Filter Type	Byte	0	See output config	See config	R/W	

401066	1065	0x0429	1	Software Filter Constant	Word	10	See output config	See config	R/W	
401067	1066	0x042A	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Universal Input #3</b>										
401075	1074	0x0432	1	Input Type	Word	1 - Voltage	0 - Disabled1 - Voltage Input2	N/A	R/W	Resistive Input is available for the Universal Inputs
401076	1075	0x0433	1	Voltage Range	Word	0 - 0...5V	See output config	See config	R/W	
401077	1076	0x0434	1	Current Range	Word	0 - 0...20mA	See output config	See config	R/W	
401078	1077	0x0435	2	Input Range Min	Float	0	See output config	See config	R/W	
401080	1079	0x0437	2	Input Range Max	Float	5	See output config	See config	R/W	
401082	1081	0x0439	1	Voltage LoZ Input	Word	0	See output config	See config	R/W	
401083	1082	0x043A	1	Analog Input Filter	Word	1000ms	See output config	See config	R/W	
401084	1083	0x043B	1	Pull-Up/Pull-Down Resistor	Word	1000ms	See output config	See config	R/W	
401085	1084	0x043C	1	Input Polarity	Word	500Hz	See output config	See config	R/W	
401086	1085	0x043D	1	Discrete Input Debounce Time	Byte	1 - High	See output config	See config	R/W	
401087	1086	0x043E	1	Frequency Range	Word	500mA	See output config	See config	R/W	
401088	1087	0x043F	1	Frequency/PWM Debounce Filter	Word	1000mA	See output config	See config	R/W	
401089	1088	0x0440	1	Frequency/PWM Debounce Averaging	Word	1000ms	See output config	See config	R/W	
401090	1089	0x0441	1	Software Filter Type	Byte	0	See output config	See config	R/W	
401091	1090	0x0442	1	Software Filter Constant	Word	10	See output config	See config	R/W	
401092	1091	0x0443	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Universal Input #4</b>										
401100	1099	0x044B	1	Input Type	Word	1 - Voltage	0 - Disabled1 - Voltage Input2	N/A	R/W	Resistive Input is available for the Universal Inputs
401101	1100	0x044C	1	Voltage Range	Word	0 - 0...5V	See output config	See config	R/W	
401102	1101	0x044D	1	Current Range	Word	0 - 0...20mA	See output config	See config	R/W	
401103	1102	0x044E	2	Input Range Min	Float	0	See output config	See config	R/W	
401105	1104	0x0450	2	Input Range Max	Float	5	See output config	See config	R/W	
401107	1106	0x0452	1	Voltage LoZ Input	Word	0	See output config	See config	R/W	
401108	1107	0x0453	1	Analog Input Filter	Word	1000ms	See output config	See config	R/W	
401109	1108	0x0454	1	Pull-Up/Pull-Down Resistor	Word	1000ms	See output config	See config	R/W	
401110	1109	0x0455	1	Input Polarity	Word	500Hz	See output config	See config	R/W	
401111	1110	0x0456	1	Discrete Input Debounce Time	Byte	1 - High	See output config	See config	R/W	
401112	1111	0x0457	1	Frequency Range	Word	500mA	See output config	See config	R/W	
401113	1112	0x0458	1	Frequency/PWM Debounce Filter	Word	1000mA	See output config	See config	R/W	
401114	1113	0x0459	1	Frequency/PWM Debounce Averaging	Word	1000ms	See output config	See config	R/W	
401115	1114	0x045A	1	Software Filter Type	Byte	0	See output config	See config	R/W	
401116	1115	0x045B	1	Software Filter Constant	Word	10	See output config	See config	R/W	
401117	1116	0x045C	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Universal Input #5</b>										
401125	1124	0x0464	1	Input Type	Word	1 - Voltage	0 - Disabled1 - Voltage Input2	N/A	R/W	Resistive Input is available for the Universal Inputs
401126	1125	0x0465	1	Voltage Range	Word	0 - 0...5V	See output config	See config	R/W	
401127	1126	0x0466	1	Current Range	Word	0 - 0...20mA	See output config	See config	R/W	
401128	1127	0x0467	2	Input Range Min	Float	0	See output config	See config	R/W	
401130	1129	0x0469	2	Input Range Max	Float	5	See output config	See config	R/W	
401132	1131	0x046B	1	Voltage LoZ Input	Word	0	See output config	See config	R/W	
401133	1132	0x046C	1	Analog Input Filter	Word	1000ms	See output config	See config	R/W	
401134	1133	0x046D	1	Pull-Up/Pull-Down Resistor	Word	1000ms	See output config	See config	R/W	
401135	1134	0x046E	1	Input Polarity	Word	500Hz	See output config	See config	R/W	
401136	1135	0x046F	1	Discrete Input Debounce Time	Byte	1 - High	See output config	See config	R/W	
401137	1136	0x0470	1	Frequency Range	Word	500mA	See output config	See config	R/W	
401138	1137	0x0471	1	Frequency/PWM Debounce Filter	Word	1000mA	See output config	See config	R/W	
401139	1138	0x0472	1	Frequency/PWM Debounce Averaging	Word	1000ms	See output config	See config	R/W	
401140	1139	0x0473	1	Software Filter Type	Byte	0	See output config	See config	R/W	

401141	1140	0x0474	1	Software Filter Constant	Word	10	See output config	See config	R/W	
401142	1141	0x0475	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Universal Input #6</b>										
401150	1149	0x047D	1	Input Type	Word	1 - Voltage	0 - Disabled1 - Voltage Input2	N/A	R/W	Resistive Input is available for the Universal Inputs
401151	1150	0x047E	1	Voltage Range	Word	0 - 0...5V	See output config	See config	R/W	
401152	1151	0x047F	1	Current Range	Word	0 - 0...20mA	See output config	See config	R/W	
401153	1152	0x0480	2	Input Range Min	Float	0	See output config	See config	R/W	
401155	1154	0x0482	2	Input Range Max	Float	5	See output config	See config	R/W	
401157	1156	0x0484	1	Voltage LoZ Input	Word	0	See output config	See config	R/W	
401158	1157	0x0485	1	Analog Input Filter	Word	1000ms	See output config	See config	R/W	
401159	1158	0x0486	1	Pull-Up/Pull-Down Resistor	Word	1000ms	See output config	See config	R/W	
401160	1159	0x0487	1	Input Polarity	Word	500Hz	See output config	See config	R/W	
401161	1160	0x0488	1	Discrete Input Debounce Time	Byte	1 - High	See output config	See config	R/W	
401162	1161	0x0489	1	Frequency Range	Word	500mA	See output config	See config	R/W	
401163	1162	0x048A	1	Frequency/PWM Debounce Filter	Word	1000mA	See output config	See config	R/W	
401164	1163	0x048B	1	Frequency/PWM Debounce Averaging	Word	1000ms	See output config	See config	R/W	
401165	1164	0x048C	1	Software Filter Type	Byte	0	See output config	See config	R/W	
401166	1165	0x048D	1	Software Filter Constant	Word	10	See output config	See config	R/W	
401167	1166	0x048E	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Universal Input #7</b>										
401175	1174	0x0496	1	Input Type	Word	1 - Voltage	0 - Disabled1 - Voltage Input2	N/A	R/W	Resistive Input is available for the Universal Inputs
401176	1175	0x0497	1	Voltage Range	Word	0 - 0...5V	See output config	See config	R/W	
401177	1176	0x0498	1	Current Range	Word	0 - 0...20mA	See output config	See config	R/W	
401178	1177	0x0499	2	Input Range Min	Float	0	See output config	See config	R/W	
401180	1179	0x049B	2	Input Range Max	Float	5	See output config	See config	R/W	
401182	1181	0x049D	1	Voltage LoZ Input	Word	0	See output config	See config	R/W	
401183	1182	0x049E	1	Analog Input Filter	Word	1000ms	See output config	See config	R/W	
401184	1183	0x049F	1	Pull-Up/Pull-Down Resistor	Word	1000ms	See output config	See config	R/W	
401185	1184	0x04A0	1	Input Polarity	Word	500Hz	See output config	See config	R/W	
401186	1185	0x04A1	1	Discrete Input Debounce Time	Byte	1 - High	See output config	See config	R/W	
401187	1186	0x04A2	1	Frequency Range	Word	500mA	See output config	See config	R/W	
401188	1187	0x04A3	1	Frequency/PWM Debounce Filter	Word	1000mA	See output config	See config	R/W	
401189	1188	0x04A4	1	Frequency/PWM Debounce Averaging	Word	1000ms	See output config	See config	R/W	
401190	1189	0x04A5	1	Software Filter Type	Byte	0	See output config	See config	R/W	
401191	1190	0x04A6	1	Software Filter Constant	Word	10	See output config	See config	R/W	
401192	1191	0x04A7	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Universal Input #8</b>										
401200	1199	0x04AF	1	Input Type	Word	1 - Voltage	0 - Disabled1 - Voltage Input2	N/A	R/W	Resistive Input is available for the Universal Inputs
401201	1200	0x04B0	1	Voltage Range	Word	0 - 0...5V	See output config	See config	R/W	
401202	1201	0x04B1	1	Current Range	Word	0 - 0...20mA	See output config	See config	R/W	
401203	1202	0x04B2	2	Input Range Min	Float	0	See output config	See config	R/W	
401205	1204	0x04B4	2	Input Range Max	Float	5	See output config	See config	R/W	
401207	1206	0x04B6	1	Voltage LoZ Input	Word	0	See output config	See config	R/W	
401208	1207	0x04B7	1	Analog Input Filter	Word	1000ms	See output config	See config	R/W	
401209	1208	0x04B8	1	Pull-Up/Pull-Down Resistor	Word	1000ms	See output config	See config	R/W	
401210	1209	0x04B9	1	Input Polarity	Word	500Hz	See output config	See config	R/W	
401211	1210	0x04BA	1	Discrete Input Debounce Time	Byte	1 - High	See output config	See config	R/W	
401212	1211	0x04BB	1	Frequency Range	Word	500mA	See output config	See config	R/W	
401213	1212	0x04BC	1	Frequency/PWM Debounce Filter	Word	1000mA	See output config	See config	R/W	
401214	1213	0x04BD	1	Frequency/PWM Debounce Averaging	Word	1000ms	See output config	See config	R/W	
401215	1214	0x04BE	1	Software Filter Type	Byte	0	See output config	See config	R/W	

401216	1215	0x04BF	1	Software Filter Constant	Word	10	See output config	See config	R/W	
401217	1216	0x04C0	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Universal Input #9</b>										
401225	1224	0x04C8	1	Input Type	Word	1 - Voltage	0 - Disabled1 - Voltage Input2	N/A	R/W	Resistive Input is available for the Universal Inputs
401226	1225	0x04C9	1	Voltage Range	Word	0 - 0...5V	See output config	See config	R/W	
401227	1226	0x04CA	1	Current Range	Word	0 - 0...20mA	See output config	See config	R/W	
401228	1227	0x04CB	2	Input Range Min	Float	0	See output config	See config	R/W	
401230	1229	0x04CD	2	Input Range Max	Float	5	See output config	See config	R/W	
401232	1231	0x04CF	1	Voltage LoZ Input	Word	0	See output config	See config	R/W	
401233	1232	0x04D0	1	Analog Input Filter	Word	1000ms	See output config	See config	R/W	
401234	1233	0x04D1	1	Pull-Up/Pull-Down Resistor	Word	1000ms	See output config	See config	R/W	
401235	1234	0x04D2	1	Input Polarity	Word	500Hz	See output config	See config	R/W	
401236	1235	0x04D3	1	Discrete Input Debounce Time	Byte	1 - High	See output config	See config	R/W	
401237	1236	0x04D4	1	Frequency Range	Word	500mA	See output config	See config	R/W	
401238	1237	0x04D5	1	Frequency/PWM Debounce Filter	Word	1000mA	See output config	See config	R/W	
401239	1238	0x04D6	1	Frequency/PWM Debounce Averaging	Word	1000ms	See output config	See config	R/W	
401240	1239	0x04D7	1	Software Filter Type	Byte	0	See output config	See config	R/W	
401241	1240	0x04D8	1	Software Filter Constant	Word	10	See output config	See config	R/W	
401242	1241	0x04D9	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Universal Input #10</b>										
401250	1249	0x04E1	1	Input Type	Word	1 - Voltage	0 - Disabled1 - Voltage Input2	N/A	R/W	Resistive Input is available for the Universal Inputs
401251	1250	0x04E2	1	Voltage Range	Word	0 - 0...5V	See output config	See config	R/W	
401252	1251	0x04E3	1	Current Range	Word	0 - 0...20mA	See output config	See config	R/W	
401253	1252	0x04E4	2	Input Range Min	Float	0	See output config	See config	R/W	
401255	1254	0x04E6	2	Input Range Max	Float	5	See output config	See config	R/W	
401257	1256	0x04E8	1	Voltage LoZ Input	Word	0	See output config	See config	R/W	
401258	1257	0x04E9	1	Analog Input Filter	Word	1000ms	See output config	See config	R/W	
401259	1258	0x04EA	1	Pull-Up/Pull-Down Resistor	Word	1000ms	See output config	See config	R/W	
401260	1259	0x04EB	1	Input Polarity	Word	500Hz	See output config	See config	R/W	
401261	1260	0x04EC	1	Discrete Input Debounce Time	Byte	1 - High	See output config	See config	R/W	
401262	1261	0x04ED	1	Frequency Range	Word	500mA	See output config	See config	R/W	
401263	1262	0x04EE	1	Frequency/PWM Debounce Filter	Word	1000mA	See output config	See config	R/W	
401264	1263	0x04EF	1	Frequency/PWM Debounce Averaging	Word	1000ms	See output config	See config	R/W	
401265	1264	0x04F0	1	Software Filter Type	Byte	0	See output config	See config	R/W	
401266	1265	0x04F1	1	Software Filter Constant	Word	10	See output config	See config	R/W	
401267	1266	0x04F2	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Universal Input #11</b>										
401275	1274	0x04FA	1	Input Type	Word	1 - Voltage	0 - Disabled1 - Voltage Input2	N/A	R/W	Resistive Input is available for the Universal Inputs
401276	1275	0x04FB	1	Voltage Range	Word	0 - 0...5V	See output config	See config	R/W	
401277	1276	0x04FC	1	Current Range	Word	0 - 0...20mA	See output config	See config	R/W	
401278	1277	0x04FD	2	Input Range Min	Float	0	See output config	See config	R/W	
401280	1279	0x04FF	2	Input Range Max	Float	5	See output config	See config	R/W	
401282	1281	0x0501	1	Voltage LoZ Input	Word	0	See output config	See config	R/W	
401283	1282	0x0502	1	Analog Input Filter	Word	1000ms	See output config	See config	R/W	
401284	1283	0x0503	1	Pull-Up/Pull-Down Resistor	Word	1000ms	See output config	See config	R/W	
401285	1284	0x0504	1	Input Polarity	Word	500Hz	See output config	See config	R/W	
401286	1285	0x0505	1	Discrete Input Debounce Time	Byte	1 - High	See output config	See config	R/W	
401287	1286	0x0506	1	Frequency Range	Word	500mA	See output config	See config	R/W	
401288	1287	0x0507	1	Frequency/PWM Debounce Filter	Word	1000mA	See output config	See config	R/W	
401289	1288	0x0508	1	Frequency/PWM Debounce Averaging	Word	1000ms	See output config	See config	R/W	
401290	1289	0x0509	1	Software Filter Type	Byte	0	See output config	See config	R/W	

401291	1290	0x050A	1	Software Filter Constant	Word	10	See output config	See config	R/W	
401292	1291	0x050B	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Universal Input #12</b>										
401300	1299	0x0513	1	Input Type	Word	1 - Voltage	0 - Disabled1 - Voltage Input2	N/A	R/W	Resistive Input is available for the Universal Inputs
401301	1300	0x0514	1	Voltage Range	Word	0 - 0...5V	See output config	See config	R/W	
401302	1301	0x0515	1	Current Range	Word	0 - 0...20mA	See output config	See config	R/W	
401303	1302	0x0516	2	Input Range Min	Float	0	See output config	See config	R/W	
401305	1304	0x0518	2	Input Range Max	Float	5	See output config	See config	R/W	
401307	1306	0x051A	1	Voltage LoZ Input	Word	0	See output config	See config	R/W	
401308	1307	0x051B	1	Analog Input Filter	Word	1000ms	See output config	See config	R/W	
401309	1308	0x051C	1	Pull-Up/Pull-Down Resistor	Word	1000ms	See output config	See config	R/W	
401310	1309	0x051D	1	Input Polarity	Word	500Hz	See output config	See config	R/W	
401311	1310	0x051E	1	Discrete Input Debounce Time	Byte	1 - High	See output config	See config	R/W	
401312	1311	0x051F	1	Frequency Range	Word	500mA	See output config	See config	R/W	
401313	1312	0x0520	1	Frequency/PWM Debounce Filter	Word	1000mA	See output config	See config	R/W	
401314	1313	0x0521	1	Frequency/PWM Debounce Averaging	Word	1000ms	See output config	See config	R/W	
401315	1314	0x0522	1	Software Filter Type	Byte	0	See output config	See config	R/W	
401316	1315	0x0523	1	Software Filter Constant	Word	10	See output config	See config	R/W	
401317	1316	0x0524	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Proportional Output #1</b>										
401325	1324	0x052C	1	Output Type	Byte	Proportional V	0 - Disabled1 - Proportional C	N/A	R/W	
401326	1325	0x052D	2	Output Data Min	Float	0V	See output config	See config	R/W	
401328	1327	0x052F	2	Output Data Max	Float	10V	See output config	See config	R/W	
401330	1329	0x0531	1	Dither Frequency	Word	0	See output config	See config	R/W	
401331	1330	0x0532	1	Dither Amplitude	Word	0	See output config	See config	R/W	
401332	1331	0x0533	1	Ramp Up (Min to Max)	Word	1000ms	See output config	See config	R/W	
401333	1332	0x0534	1	Ramp Down (Max to Min)	Word	1000ms	See output config	See config	R/W	
401334	1333	0x0535	1	PWM Output Frequency	Word	25000Hz	See output config	See config	R/W	
401335	1334	0x0536	1	Digital Response	Byte	0	See output config	See config	R/W	
401336	1335	0x0537	1	Control Source	Byte	Not Used	See output config	See config	R/W	
401337	1336	0x0538	1	Control Number	Byte	1	See output config	See config	R/W	
401338	1337	0x0539	1	Control Response	Byte	0	See output config	See config	R/W	
401339	1338	0x053A	1	Digital Delay polarity	Byte	0	See output config	See config	R/W	
401340	1339	0x053B	1	Enable Source	Byte	Control not use	See output config	See config	R/W	
401341	1340	0x053C	1	Enable Number	Byte	1	See output config	See config	R/W	
401342	1341	0x053D	1	Enable Response	Byte	0	See output config	See config	R/W	
401343	1342	0x053E	1	Override Source	Byte	0	See output config	See config	R/W	
401344	1343	0x053F	1	Override Number	Byte	1	See output config	See config	R/W	
401345	1344	0x0540	1	Override Response	Byte	0	See output config	See config	R/W	
401346	1345	0x0541	2	Override Output Value	Float	0	See output config	See config	R/W	
401348	1347	0x0543	1	Fault Detection is Enabled	Byte	0, False	See output config	See config	R/W	
401349	1348	0x0544	1	Control Fault Response	Byte	1, Apply Fault V	See output config	See config	R/W	
401350	1349	0x0545	2	Override Output Value	Byte	0	See output config	See config	R/W	
401352	1351	0x0547	2	Proportional Gain	Float	0.2	See output config	See config	R/W	
401354	1353	0x0549	2	Integral Gain	Float	0.0015	See output config	See config	R/W	
401356	1355	0x054B	2	Derivative Time	Float	0	See output config	See config	R/W	
401358	1357	0x054D	7	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Proportional Output #2</b>										
401365	1364	0x0554	1	Output Type	Byte	Proportional V	0 - Disabled1 - Proportional C	N/A	R/W	
401366	1365	0x0555	2	Output Data Min	Float	0V	See output config	See config	R/W	
401368	1367	0x0557	2	Output Data Max	Float	10V	See output config	See config	R/W	



401370	1369	0x0559	1	Dither Frequency	Word		0	See output config	See config	R/W	
401371	1370	0x055A	1	Dither Amplitude	Word		0	See output config	See config	R/W	
401372	1371	0x055B	1	Ramp Up (Min to Max)	Word	1000ms		See output config	See config	R/W	
401373	1372	0x055C	1	Ramp Down (Max to Min)	Word	1000ms		See output config	See config	R/W	
401374	1373	0x055D	1	PWM Output Frequency	Word	25000Hz		See output config	See config	R/W	
401375	1374	0x055E	1	Digital Response	Byte		0	See output config	See config	R/W	
401376	1375	0x055F	1	Control Source	Byte	Not Used		See output config	See config	R/W	
401377	1376	0x0560	1	Control Number	Byte		1	See output config	See config	R/W	
401378	1377	0x0561	1	Control Response	Byte		0	See output config	See config	R/W	
401379	1378	0x0562	1	Digital Delay polarity	Byte		0	See output config	See config	R/W	
401380	1379	0x0563	1	Enable Source	Byte	Control not use		See output config	See config	R/W	
401381	1380	0x0564	1	Enable Number	Byte		1	See output config	See config	R/W	
401382	1381	0x0565	1	Enable Response	Byte		0	See output config	See config	R/W	
401383	1382	0x0566	1	Override Source	Byte		0	See output config	See config	R/W	
401384	1383	0x0567	1	Override Number	Byte		1	See output config	See config	R/W	
401385	1384	0x0568	1	Override Response	Byte		0	See output config	See config	R/W	
401386	1385	0x0569	2	Override Output Value	Float		0	See output config	See config	R/W	
401388	1387	0x056B	1	Fault Detection is Enabled	Byte	0, False		See output config	See config	R/W	
401389	1388	0x056C	1	Control Fault Response	Byte	1, Apply Fault V		See output config	See config	R/W	
401390	1389	0x056D	2	Override Output Value	Byte		0	See output config	See config	R/W	
401392	1391	0x056F	2	Proportional Gain	Float		0.2	See output config	See config	R/W	
401394	1393	0x0571	2	Integral Gain	Float		0.0015	See output config	See config	R/W	
401396	1395	0x0573	2	Derivative Time	Float		0	See output config	See config	R/W	
401398	1397	0x0575	7	Reserved	N/A	N/A	N/A	N/A	RO		Reserved for future use. Reading results 0. Writing
<b>Proportional Output #3</b>											
401405	1404	0x057C	1	Output Type	Byte	Proportional V	0 - Disabled1 - Proportional C	N/A		R/W	
401406	1405	0x057D	2	Output Data Min	Float	0V		See output config	See config	R/W	
401408	1407	0x057F	2	Output Data Max	Float	10V		See output config	See config	R/W	
401410	1409	0x0581	1	Dither Frequency	Word		0	See output config	See config	R/W	
401411	1410	0x0582	1	Dither Amplitude	Word		0	See output config	See config	R/W	
401412	1411	0x0583	1	Ramp Up (Min to Max)	Word	1000ms		See output config	See config	R/W	
401413	1412	0x0584	1	Ramp Down (Max to Min)	Word	1000ms		See output config	See config	R/W	
401414	1413	0x0585	1	PWM Output Frequency	Word	25000Hz		See output config	See config	R/W	
401415	1414	0x0586	1	Digital Response	Byte		0	See output config	See config	R/W	
401416	1415	0x0587	1	Control Source	Byte	Not Used		See output config	See config	R/W	
401417	1416	0x0588	1	Control Number	Byte		1	See output config	See config	R/W	
401418	1417	0x0589	1	Control Response	Byte		0	See output config	See config	R/W	
401419	1418	0x058A	1	Digital Delay polarity	Byte		0	See output config	See config	R/W	
401420	1419	0x058B	1	Enable Source	Byte	Control not use		See output config	See config	R/W	
401421	1420	0x058C	1	Enable Number	Byte		1	See output config	See config	R/W	
401422	1421	0x058D	1	Enable Response	Byte		0	See output config	See config	R/W	
401423	1422	0x058E	1	Override Source	Byte		0	See output config	See config	R/W	
401424	1423	0x058F	1	Override Number	Byte		1	See output config	See config	R/W	
401425	1424	0x0590	1	Override Response	Byte		0	See output config	See config	R/W	
401426	1425	0x0591	2	Override Output Value	Float		0	See output config	See config	R/W	
401428	1427	0x0593	1	Fault Detection is Enabled	Byte	0, False		See output config	See config	R/W	
401429	1428	0x0594	1	Control Fault Response	Byte	1, Apply Fault V		See output config	See config	R/W	
401430	1429	0x0595	2	Override Output Value	Byte		0	See output config	See config	R/W	
401432	1431	0x0597	2	Proportional Gain	Float		0.2	See output config	See config	R/W	
401434	1433	0x0599	2	Integral Gain	Float		0.0015	See output config	See config	R/W	
401436	1435	0x059B	2	Derivative Time	Float		0	See output config	See config	R/W	

401438	1437	0x059D	7	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Proportional Output #4</b>										
401445	1444	0x05A4	1	Output Type	Byte	Proportional V	0 - Disabled1 - Proportional C	N/A	R/W	
401446	1445	0x05A5	2	Output Data Min	Float	0V	See output config	See config	R/W	
401448	1447	0x05A7	2	Output Data Max	Float	10V	See output config	See config	R/W	
401450	1449	0x05A9	1	Dither Frequency	Word		0 See output config	See config	R/W	
401451	1450	0x05AA	1	Dither Amplitude	Word		0 See output config	See config	R/W	
401452	1451	0x05AB	1	Ramp Up (Min to Max)	Word	1000ms	See output config	See config	R/W	
401453	1452	0x05AC	1	Ramp Down (Max to Min)	Word	1000ms	See output config	See config	R/W	
401454	1453	0x05AD	1	PWM Output Frequency	Word	25000Hz	See output config	See config	R/W	
401455	1454	0x05AE	1	Digital Response	Byte		0 See output config	See config	R/W	
401456	1455	0x05AF	1	Control Source	Byte	Not Used	See output config	See config	R/W	
401457	1456	0x05B0	1	Control Number	Byte		1 See output config	See config	R/W	
401458	1457	0x05B1	1	Control Response	Byte		0 See output config	See config	R/W	
401459	1458	0x05B2	1	Digital Delay polarity	Byte		0 See output config	See config	R/W	
401460	1459	0x05B3	1	Enable Source	Byte	Control not use	See output config	See config	R/W	
401461	1460	0x05B4	1	Enable Number	Byte		1 See output config	See config	R/W	
401462	1461	0x05B5	1	Enable Response	Byte		0 See output config	See config	R/W	
401463	1462	0x05B6	1	Override Source	Byte		0 See output config	See config	R/W	
401464	1463	0x05B7	1	Override Number	Byte		1 See output config	See config	R/W	
401465	1464	0x05B8	1	Override Response	Byte		0 See output config	See config	R/W	
401466	1465	0x05B9	2	Override Output Value	Float		0 See output config	See config	R/W	
401468	1467	0x05BB	1	Fault Detection is Enabled	Byte	0, False	See output config	See config	R/W	
401469	1468	0x05BC	1	Control Fault Response	Byte	1, Apply Fault V	See output config	See config	R/W	
401470	1469	0x05BD	2	Override Output Value	Byte		0 See output config	See config	R/W	
401472	1471	0x05BF	2	Proportional Gain	Float		0.2 See output config	See config	R/W	
401474	1473	0x05C1	2	Integral Gain	Float		0.0015 See output config	See config	R/W	
401476	1475	0x05C3	2	Derivative Time	Float		0 See output config	See config	R/W	
401478	1477	0x05C5	7	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Proportional Output #5</b>										
401485	1484	0x05CC	1	Output Type	Byte	Proportional V	0 - Disabled1 - Proportional C	N/A	R/W	
401486	1485	0x05CD	2	Output Data Min	Float	0V	See output config	See config	R/W	
401488	1487	0x05CF	2	Output Data Max	Float	10V	See output config	See config	R/W	
401490	1489	0x05D1	1	Dither Frequency	Word		0 See output config	See config	R/W	
401491	1490	0x05D2	1	Dither Amplitude	Word		0 See output config	See config	R/W	
401492	1491	0x05D3	1	Ramp Up (Min to Max)	Word	1000ms	See output config	See config	R/W	
401493	1492	0x05D4	1	Ramp Down (Max to Min)	Word	1000ms	See output config	See config	R/W	
401494	1493	0x05D5	1	PWM Output Frequency	Word	25000Hz	See output config	See config	R/W	
401495	1494	0x05D6	1	Digital Response	Byte		0 See output config	See config	R/W	
401496	1495	0x05D7	1	Control Source	Byte	Not Used	See output config	See config	R/W	
401497	1496	0x05D8	1	Control Number	Byte		1 See output config	See config	R/W	
401498	1497	0x05D9	1	Control Response	Byte		0 See output config	See config	R/W	
401499	1498	0x05DA	1	Digital Delay polarity	Byte		0 See output config	See config	R/W	
401500	1499	0x05DB	1	Enable Source	Byte	Control not use	See output config	See config	R/W	
401501	1500	0x05DC	1	Enable Number	Byte		1 See output config	See config	R/W	
401502	1501	0x05DD	1	Enable Response	Byte		0 See output config	See config	R/W	
401503	1502	0x05DE	1	Override Source	Byte		0 See output config	See config	R/W	
401504	1503	0x05DF	1	Override Number	Byte		1 See output config	See config	R/W	
401505	1504	0x05E0	1	Override Response	Byte		0 See output config	See config	R/W	
401506	1505	0x05E1	2	Override Output Value	Float		0 See output config	See config	R/W	
401508	1507	0x05E3	1	Fault Detection is Enabled	Byte	0, False	See output config	See config	R/W	

401509	1508	0x05E4	1	Control Fault Response	Byte	1, Apply Fault V	See output config	See config	R/W	
401510	1509	0x05E5	2	Override Output Value	Byte	0	See output config	See config	R/W	
401512	1511	0x05E7	2	Proportional Gain	Float	0.2	See output config	See config	R/W	
401514	1513	0x05E9	2	Integral Gain	Float	0.0015	See output config	See config	R/W	
401516	1515	0x05EB	2	Derivative Time	Float	0	See output config	See config	R/W	
401518	1517	0x05ED	7	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Proportional Output #6</b>										
401525	1524	0x05F4	1	Output Type	Byte	Proportional V	0 - Disabled1 - Proportional C	N/A	R/W	
401526	1525	0x05F5	2	Output Data Min	Float	0V	See output config	See config	R/W	
401528	1527	0x05F7	2	Output Data Max	Float	10V	See output config	See config	R/W	
401530	1529	0x05F9	1	Dither Frequency	Word	0	See output config	See config	R/W	
401531	1530	0x05FA	1	Dither Amplitude	Word	0	See output config	See config	R/W	
401532	1531	0x05FB	1	Ramp Up (Min to Max)	Word	1000ms	See output config	See config	R/W	
401533	1532	0x05FC	1	Ramp Down (Max to Min)	Word	1000ms	See output config	See config	R/W	
401534	1533	0x05FD	1	PWM Output Frequency	Word	25000Hz	See output config	See config	R/W	
401535	1534	0x05FE	1	Digital Response	Byte	0	See output config	See config	R/W	
401536	1535	0x05FF	1	Control Source	Byte	Not Used	See output config	See config	R/W	
401537	1536	0x0600	1	Control Number	Byte	1	See output config	See config	R/W	
401538	1537	0x0601	1	Control Response	Byte	0	See output config	See config	R/W	
401539	1538	0x0602	1	Digital Delay polarity	Byte	0	See output config	See config	R/W	
401540	1539	0x0603	1	Enable Source	Byte	Control not use	See output config	See config	R/W	
401541	1540	0x0604	1	Enable Number	Byte	1	See output config	See config	R/W	
401542	1541	0x0605	1	Enable Response	Byte	0	See output config	See config	R/W	
401543	1542	0x0606	1	Override Source	Byte	0	See output config	See config	R/W	
401544	1543	0x0607	1	Override Number	Byte	1	See output config	See config	R/W	
401545	1544	0x0608	1	Override Response	Byte	0	See output config	See config	R/W	
401546	1545	0x0609	2	Override Output Value	Float	0	See output config	See config	R/W	
401548	1547	0x060B	1	Fault Detection is Enabled	Byte	0, False	See output config	See config	R/W	
401549	1548	0x060C	1	Control Fault Response	Byte	1, Apply Fault V	See output config	See config	R/W	
401550	1549	0x060D	2	Override Output Value	Byte	0	See output config	See config	R/W	
401552	1551	0x060F	2	Proportional Gain	Float	0.2	See output config	See config	R/W	
401554	1553	0x0611	2	Integral Gain	Float	0.0015	See output config	See config	R/W	
401556	1555	0x0613	2	Derivative Time	Float	0	See output config	See config	R/W	
401558	1557	0x0615	7	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Proportional Output #7</b>										
401565	1564	0x061C	1	Output Type	Byte	Proportional V	0 - Disabled1 - Proportional C	N/A	R/W	
401566	1565	0x061D	2	Output Data Min	Float	0V	See output config	See config	R/W	
401568	1567	0x061F	2	Output Data Max	Float	10V	See output config	See config	R/W	
401570	1569	0x0621	1	Dither Frequency	Word	0	See output config	See config	R/W	
401571	1570	0x0622	1	Dither Amplitude	Word	0	See output config	See config	R/W	
401572	1571	0x0623	1	Ramp Up (Min to Max)	Word	1000ms	See output config	See config	R/W	
401573	1572	0x0624	1	Ramp Down (Max to Min)	Word	1000ms	See output config	See config	R/W	
401574	1573	0x0625	1	PWM Output Frequency	Word	25000Hz	See output config	See config	R/W	
401575	1574	0x0626	1	Digital Response	Byte	0	See output config	See config	R/W	
401576	1575	0x0627	1	Control Source	Byte	Not Used	See output config	See config	R/W	
401577	1576	0x0628	1	Control Number	Byte	1	See output config	See config	R/W	
401578	1577	0x0629	1	Control Response	Byte	0	See output config	See config	R/W	
401579	1578	0x062A	1	Digital Delay polarity	Byte	0	See output config	See config	R/W	
401580	1579	0x062B	1	Enable Source	Byte	Control not use	See output config	See config	R/W	
401581	1580	0x062C	1	Enable Number	Byte	1	See output config	See config	R/W	
401582	1581	0x062D	1	Enable Response	Byte	0	See output config	See config	R/W	

401583	1582	0x062E	1	Override Source	Byte		0	See output config	See config	R/W	
401584	1583	0x062F	1	Override Number	Byte		1	See output config	See config	R/W	
401585	1584	0x0630	1	Override Response	Byte		0	See output config	See config	R/W	
401586	1585	0x0631	2	Override Output Value	Float		0	See output config	See config	R/W	
401588	1587	0x0633	1	Fault Detection is Enabled	Byte		0, False	See output config	See config	R/W	
401589	1588	0x0634	1	Control Fault Response	Byte		1, Apply Fault V	See output config	See config	R/W	
401590	1589	0x0635	2	Override Output Value	Byte		0	See output config	See config	R/W	
401592	1591	0x0637	2	Proportional Gain	Float		0.2	See output config	See config	R/W	
401594	1593	0x0639	2	Integral Gain	Float		0.0015	See output config	See config	R/W	
401596	1595	0x063B	2	Derivative Time	Float		0	See output config	See config	R/W	
401598	1597	0x063D	7	Reserved	N/A	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Proportional Output #8</b>											
401605	1604	0x0644	1	Output Type	Byte	Proportional V	0 - Disabled1 - Proportional C	N/A		R/W	
401606	1605	0x0645	2	Output Data Min	Float	0V		See output config	See config	R/W	
401608	1607	0x0647	2	Output Data Max	Float	10V		See output config	See config	R/W	
401610	1609	0x0649	1	Dither Frequency	Word		0	See output config	See config	R/W	
401611	1610	0x064A	1	Dither Amplitude	Word		0	See output config	See config	R/W	
401612	1611	0x064B	1	Ramp Up (Min to Max)	Word	1000ms		See output config	See config	R/W	
401613	1612	0x064C	1	Ramp Down (Max to Min)	Word	1000ms		See output config	See config	R/W	
401614	1613	0x064D	1	PWM Output Frequency	Word	25000Hz		See output config	See config	R/W	
401615	1614	0x064E	1	Digital Response	Byte		0	See output config	See config	R/W	
401616	1615	0x064F	1	Control Source	Byte		Not Used	See output config	See config	R/W	
401617	1616	0x0650	1	Control Number	Byte		1	See output config	See config	R/W	
401618	1617	0x0651	1	Control Response	Byte		0	See output config	See config	R/W	
401619	1618	0x0652	1	Digital Delay polarity	Byte		0	See output config	See config	R/W	
401620	1619	0x0653	1	Enable Source	Byte		Control not use	See output config	See config	R/W	
401621	1620	0x0654	1	Enable Number	Byte		1	See output config	See config	R/W	
401622	1621	0x0655	1	Enable Response	Byte		0	See output config	See config	R/W	
401623	1622	0x0656	1	Override Source	Byte		0	See output config	See config	R/W	
401624	1623	0x0657	1	Override Number	Byte		1	See output config	See config	R/W	
401625	1624	0x0658	1	Override Response	Byte		0	See output config	See config	R/W	
401626	1625	0x0659	2	Override Output Value	Float		0	See output config	See config	R/W	
401628	1627	0x065B	1	Fault Detection is Enabled	Byte		0, False	See output config	See config	R/W	
401629	1628	0x065C	1	Control Fault Response	Byte		1, Apply Fault V	See output config	See config	R/W	
401630	1629	0x065D	2	Override Output Value	Byte		0	See output config	See config	R/W	
401632	1631	0x065F	2	Proportional Gain	Float		0.2	See output config	See config	R/W	
401634	1633	0x0661	2	Integral Gain	Float		0.0015	See output config	See config	R/W	
401636	1635	0x0663	2	Derivative Time	Float		0	See output config	See config	R/W	
401638	1637	0x0665	7	Reserved	N/A	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Proportional Output #9</b>											
401645	1644	0x066C	1	Output Type	Byte	Proportional V	0 - Disabled1 - Proportional C	N/A		R/W	
401646	1645	0x066D	2	Output Data Min	Float	0V		See output config	See config	R/W	
401648	1647	0x066F	2	Output Data Max	Float	10V		See output config	See config	R/W	
401650	1649	0x0671	1	Dither Frequency	Word		0	See output config	See config	R/W	
401651	1650	0x0672	1	Dither Amplitude	Word		0	See output config	See config	R/W	
401652	1651	0x0673	1	Ramp Up (Min to Max)	Word	1000ms		See output config	See config	R/W	
401653	1652	0x0674	1	Ramp Down (Max to Min)	Word	1000ms		See output config	See config	R/W	
401654	1653	0x0675	1	PWM Output Frequency	Word	25000Hz		See output config	See config	R/W	
401655	1654	0x0676	1	Digital Response	Byte		0	See output config	See config	R/W	
401656	1655	0x0677	1	Control Source	Byte		Not Used	See output config	See config	R/W	
401657	1656	0x0678	1	Control Number	Byte		1	See output config	See config	R/W	

401658	1657	0x0679	1	Control Response	Byte		0	See output config	See config	R/W	
401659	1658	0x067A	1	Digital Delay polarity	Byte		0	See output config	See config	R/W	
401660	1659	0x067B	1	Enable Source	Byte	Control not use		See output config	See config	R/W	
401661	1660	0x067C	1	Enable Number	Byte		1	See output config	See config	R/W	
401662	1661	0x067D	1	Enable Response	Byte		0	See output config	See config	R/W	
401663	1662	0x067E	1	Override Source	Byte		0	See output config	See config	R/W	
401664	1663	0x067F	1	Override Number	Byte		1	See output config	See config	R/W	
401665	1664	0x0680	1	Override Response	Byte		0	See output config	See config	R/W	
401666	1665	0x0681	2	Override Output Value	Float		0	See output config	See config	R/W	
401668	1667	0x0683	1	Fault Detection is Enabled	Byte		0, False	See output config	See config	R/W	
401669	1668	0x0684	1	Control Fault Response	Byte		1, Apply Fault V	See output config	See config	R/W	
401670	1669	0x0685	2	Override Output Value	Byte		0	See output config	See config	R/W	
401672	1671	0x0687	2	Proportional Gain	Float		0.2	See output config	See config	R/W	
401674	1673	0x0689	2	Integral Gain	Float		0.0015	See output config	See config	R/W	
401676	1675	0x068B	2	Derivative Time	Float		0	See output config	See config	R/W	
401678	1677	0x068D	7	Reserved	N/A	N/A	N/A	N/A	RO		Reserved for future use. Reading results 0. Writing
<b>Proportional Output #10</b>											
401685	1684	0x0694	1	Output Type	Byte	Proportional V	0 - Disabled1 - Proportional C	N/A		R/W	
401686	1685	0x0695	2	Output Data Min	Float		0V	See output config	See config	R/W	
401688	1687	0x0697	2	Output Data Max	Float		10V	See output config	See config	R/W	
401690	1689	0x0699	1	Dither Frequency	Word		0	See output config	See config	R/W	
401691	1690	0x069A	1	Dither Amplitude	Word		0	See output config	See config	R/W	
401692	1691	0x069B	1	Ramp Up (Min to Max)	Word		1000ms	See output config	See config	R/W	
401693	1692	0x069C	1	Ramp Down (Max to Min)	Word		1000ms	See output config	See config	R/W	
401694	1693	0x069D	1	PWM Output Frequency	Word		25000Hz	See output config	See config	R/W	
401695	1694	0x069E	1	Digital Response	Byte		0	See output config	See config	R/W	
401696	1695	0x069F	1	Control Source	Byte		Not Used	See output config	See config	R/W	
401697	1696	0x06A0	1	Control Number	Byte		1	See output config	See config	R/W	
401698	1697	0x06A1	1	Control Response	Byte		0	See output config	See config	R/W	
401699	1698	0x06A2	1	Digital Delay polarity	Byte		0	See output config	See config	R/W	
401700	1699	0x06A3	1	Enable Source	Byte	Control not use		See output config	See config	R/W	
401701	1700	0x06A4	1	Enable Number	Byte		1	See output config	See config	R/W	
401702	1701	0x06A5	1	Enable Response	Byte		0	See output config	See config	R/W	
401703	1702	0x06A6	1	Override Source	Byte		0	See output config	See config	R/W	
401704	1703	0x06A7	1	Override Number	Byte		1	See output config	See config	R/W	
401705	1704	0x06A8	1	Override Response	Byte		0	See output config	See config	R/W	
401706	1705	0x06A9	2	Override Output Value	Float		0	See output config	See config	R/W	
401708	1707	0x06AB	1	Fault Detection is Enabled	Byte		0, False	See output config	See config	R/W	
401709	1708	0x06AC	1	Control Fault Response	Byte		1, Apply Fault V	See output config	See config	R/W	
401710	1709	0x06AD	2	Override Output Value	Byte		0	See output config	See config	R/W	
401712	1711	0x06AF	2	Proportional Gain	Float		0.2	See output config	See config	R/W	
401714	1713	0x06B1	2	Integral Gain	Float		0.0015	See output config	See config	R/W	
401716	1715	0x06B3	2	Derivative Time	Float		0	See output config	See config	R/W	
401718	1717	0x06B5	7	Reserved	N/A	N/A	N/A	N/A	RO		Reserved for future use. Reading results 0. Writing
<b>Proportional Output #11</b>											
401725	1724	0x06BC	1	Output Type	Byte	Proportional V	0 - Disabled1 - Proportional C	N/A		R/W	
401726	1725	0x06BD	2	Output Data Min	Float		0V	See output config	See config	R/W	
401728	1727	0x06BF	2	Output Data Max	Float		10V	See output config	See config	R/W	
401730	1729	0x06C1	1	Dither Frequency	Word		0	See output config	See config	R/W	
401731	1730	0x06C2	1	Dither Amplitude	Word		0	See output config	See config	R/W	
401732	1731	0x06C3	1	Ramp Up (Min to Max)	Word		1000ms	See output config	See config	R/W	

401733	1732	0x06C4	1	Ramp Down (Max to Min)	Word	1000ms	See output config	See config	R/W	
401734	1733	0x06C5	1	PWM Output Frequency	Word	25000Hz	See output config	See config	R/W	
401735	1734	0x06C6	1	Digital Response	Byte	0	See output config	See config	R/W	
401736	1735	0x06C7	1	Control Source	Byte	Not Used	See output config	See config	R/W	
401737	1736	0x06C8	1	Control Number	Byte	1	See output config	See config	R/W	
401738	1737	0x06C9	1	Control Response	Byte	0	See output config	See config	R/W	
401739	1738	0x06CA	1	Digital Delay polarity	Byte	0	See output config	See config	R/W	
401740	1739	0x06CB	1	Enable Source	Byte	Control not use	See output config	See config	R/W	
401741	1740	0x06CC	1	Enable Number	Byte	1	See output config	See config	R/W	
401742	1741	0x06CD	1	Enable Response	Byte	0	See output config	See config	R/W	
401743	1742	0x06CE	1	Override Source	Byte	0	See output config	See config	R/W	
401744	1743	0x06CF	1	Override Number	Byte	1	See output config	See config	R/W	
401745	1744	0x06D0	1	Override Response	Byte	0	See output config	See config	R/W	
401746	1745	0x06D1	2	Override Output Value	Float	0	See output config	See config	R/W	
401748	1747	0x06D3	1	Fault Detection is Enabled	Byte	0, False	See output config	See config	R/W	
401749	1748	0x06D4	1	Control Fault Response	Byte	1, Apply Fault V	See output config	See config	R/W	
401750	1749	0x06D5	2	Override Output Value	Byte	0	See output config	See config	R/W	
401752	1751	0x06D7	2	Proportional Gain	Float	0.2	See output config	See config	R/W	
401754	1753	0x06D9	2	Integral Gain	Float	0.0015	See output config	See config	R/W	
401756	1755	0x06DB	2	Derivative Time	Float	0	See output config	See config	R/W	
401758	1757	0x06DD	7	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Proportional Output #12</b>										
401765	1764	0x06E4	1	Output Type	Byte	Proportional V	0 - Disabled1 - Proportional C	N/A	R/W	
401766	1765	0x06E5	2	Output Data Min	Float	0V	See output config	See config	R/W	
401768	1767	0x06E7	2	Output Data Max	Float	10V	See output config	See config	R/W	
401770	1769	0x06E9	1	Dither Frequency	Word	0	See output config	See config	R/W	
401771	1770	0x06EA	1	Dither Amplitude	Word	0	See output config	See config	R/W	
401772	1771	0x06EB	1	Ramp Up (Min to Max)	Word	1000ms	See output config	See config	R/W	
401773	1772	0x06EC	1	Ramp Down (Max to Min)	Word	1000ms	See output config	See config	R/W	
401774	1773	0x06ED	1	PWM Output Frequency	Word	25000Hz	See output config	See config	R/W	
401775	1774	0x06EE	1	Digital Response	Byte	0	See output config	See config	R/W	
401776	1775	0x06EF	1	Control Source	Byte	Not Used	See output config	See config	R/W	
401777	1776	0x06F0	1	Control Number	Byte	1	See output config	See config	R/W	
401778	1777	0x06F1	1	Control Response	Byte	0	See output config	See config	R/W	
401779	1778	0x06F2	1	Digital Delay polarity	Byte	0	See output config	See config	R/W	
401780	1779	0x06F3	1	Enable Source	Byte	Control not use	See output config	See config	R/W	
401781	1780	0x06F4	1	Enable Number	Byte	1	See output config	See config	R/W	
401782	1781	0x06F5	1	Enable Response	Byte	0	See output config	See config	R/W	
401783	1782	0x06F6	1	Override Source	Byte	0	See output config	See config	R/W	
401784	1783	0x06F7	1	Override Number	Byte	1	See output config	See config	R/W	
401785	1784	0x06F8	1	Override Response	Byte	0	See output config	See config	R/W	
401786	1785	0x06F9	2	Override Output Value	Float	0	See output config	See config	R/W	
401788	1787	0x06FB	1	Fault Detection is Enabled	Byte	0, False	See output config	See config	R/W	
401789	1788	0x06FC	1	Control Fault Response	Byte	1, Apply Fault V	See output config	See config	R/W	
401790	1789	0x06FD	2	Override Output Value	Byte	0	See output config	See config	R/W	
401792	1791	0x06FF	2	Proportional Gain	Float	0.2	See output config	See config	R/W	
401794	1793	0x0701	2	Integral Gain	Float	0.0015	See output config	See config	R/W	
401796	1795	0x0703	2	Derivative Time	Float	0	See output config	See config	R/W	
401798	1797	0x0705	7	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>PID Function Block #1</b>										
401805	1804	0x070C	1	PID Enabled	Byte	FALSE	See output config	N/A	R/W	

401806	1805	0x070D	1	Target Source	Byte	No Source	See output config	See config	R/W	
401807	1806	0x070E	1	Target Number	Byte	1	See output config	See config	R/W	
401808	1807	0x070F	1	Feedback Source	Byte	No Source	See output config	See config	R/W	
401809	1808	0x0710	1	Feedback Number	Byte	1	See output config	See config	R/W	
401810	1809	0x0711	1	Control Response	Byte	0, Single Outpu	See output config	See config	R/W	
401811	1810	0x0712	2	Proportional Band	Float	0.5	See output config	See config	R/W	
401813	1812	0x0714	2	Integral Gain	Float	0.005	See output config	See config	R/W	
401815	1814	0x0716	2	Derivative Time	Float	0.001	See output config	See config	R/W	
401817	1816	0x0718	1	Cycle Time	Word	3 ms	See output config	See config	R/W	
401818	1817	0x0719	1	Time Decimal Digits	Byte	0	See output config	See config	R/W	
401819	1818	0x071A	2	Output Tolerance	Float	0.01	See output config	See config	R/W	
401821	1820	0x071C	2	Integral Gain	Float	1	See output config	See config	R/W	
401823	1822	0x071E	2	Derivative Gain	Float	1	See output config	See config	R/W	
401825	1824	0x0720	5	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>PID Function Block #2</b>										
401830	1829	0x0725	1	PID Enabled	Byte	FALSE	See output config	N/A	R/W	
401831	1830	0x0726	1	Target Source	Byte	No Source	See output config	See config	R/W	
401832	1831	0x0727	1	Target Number	Byte	1	See output config	See config	R/W	
401833	1832	0x0728	1	Feedback Source	Byte	No Source	See output config	See config	R/W	
401834	1833	0x0729	1	Feedback Number	Byte	1	See output config	See config	R/W	
401835	1834	0x072A	1	Control Response	Byte	0, Single Outpu	See output config	See config	R/W	
401836	1835	0x072B	2	Proportional Band	Float	0.5	See output config	See config	R/W	
401838	1837	0x072D	2	Integral Gain	Float	0.005	See output config	See config	R/W	
401840	1839	0x072F	2	Derivative Time	Float	0.001	See output config	See config	R/W	
401842	1841	0x0731	1	Cycle Time	Word	3 ms	See output config	See config	R/W	
401843	1842	0x0732	1	Time Decimal Digits	Byte	0	See output config	See config	R/W	
401844	1843	0x0733	2	Output Tolerance	Float	0.01	See output config	See config	R/W	
401846	1845	0x0735	2	Integral Gain	Float	1	See output config	See config	R/W	
401848	1847	0x0737	2	Derivative Gain	Float	1	See output config	See config	R/W	
401850	1849	0x0739	5	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>PID Function Block #3</b>										
401855	1854	0x073E	1	PID Enabled	Byte	FALSE	See output config	N/A	R/W	
401856	1855	0x073F	1	Target Source	Byte	No Source	See output config	See config	R/W	
401857	1856	0x0740	1	Target Number	Byte	1	See output config	See config	R/W	
401858	1857	0x0741	1	Feedback Source	Byte	No Source	See output config	See config	R/W	
401859	1858	0x0742	1	Feedback Number	Byte	1	See output config	See config	R/W	
401860	1859	0x0743	1	Control Response	Byte	0, Single Outpu	See output config	See config	R/W	
401861	1860	0x0744	2	Proportional Band	Float	0.5	See output config	See config	R/W	
401863	1862	0x0746	2	Integral Gain	Float	0.005	See output config	See config	R/W	
401865	1864	0x0748	2	Derivative Time	Float	0.001	See output config	See config	R/W	
401867	1866	0x074A	1	Cycle Time	Word	3 ms	See output config	See config	R/W	
401868	1867	0x074B	1	Time Decimal Digits	Byte	0	See output config	See config	R/W	
401869	1868	0x074C	2	Output Tolerance	Float	0.01	See output config	See config	R/W	
401871	1870	0x074E	2	Integral Gain	Float	1	See output config	See config	R/W	
401873	1872	0x0750	2	Derivative Gain	Float	1	See output config	See config	R/W	
401875	1874	0x0752	5	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>PID Function Block #4</b>										
401880	1879	0x0757	1	PID Enabled	Byte	FALSE	See output config	N/A	R/W	
401881	1880	0x0758	1	Target Source	Byte	No Source	See output config	See config	R/W	
401882	1881	0x0759	1	Target Number	Byte	1	See output config	See config	R/W	
401883	1882	0x075A	1	Feedback Source	Byte	No Source	See output config	See config	R/W	

401884	1883	0x075B	1	Feedback Number	Byte	1	See output config	See config	R/W	
401885	1884	0x075C	1	Control Response	Byte	0, Single Output	See output config	See config	R/W	
401886	1885	0x075D	2	Proportional Band	Float	0.5	See output config	See config	R/W	
401888	1887	0x075F	2	Integral Gain	Float	0.005	See output config	See config	R/W	
401890	1889	0x0761	2	Derivative Time	Float	0.001	See output config	See config	R/W	
401892	1891	0x0763	1	Cycle Time	Word	3 ms	See output config	See config	R/W	
401893	1892	0x0764	1	Time Decimal Digits	Byte	0	See output config	See config	R/W	
401894	1893	0x0765	2	Output Tolerance	Float	0.01	See output config	See config	R/W	
401896	1895	0x0767	2	Integral Gain	Float	1	See output config	See config	R/W	
401898	1897	0x0769	2	Derivative Gain	Float	1	See output config	See config	R/W	
401900	1899	0x076B	5	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>PID Function Block #5</b>										
401905	1904	0x0770	1	PID Enabled	Byte	FALSE	See output config	N/A	R/W	
401906	1905	0x0771	1	Target Source	Byte	No Source	See output config	See config	R/W	
401907	1906	0x0772	1	Target Number	Byte	1	See output config	See config	R/W	
401908	1907	0x0773	1	Feedback Source	Byte	No Source	See output config	See config	R/W	
401909	1908	0x0774	1	Feedback Number	Byte	1	See output config	See config	R/W	
401910	1909	0x0775	1	Control Response	Byte	0, Single Output	See output config	See config	R/W	
401911	1910	0x0776	2	Proportional Band	Float	0.5	See output config	See config	R/W	
401913	1912	0x0778	2	Integral Gain	Float	0.005	See output config	See config	R/W	
401915	1914	0x077A	2	Derivative Time	Float	0.001	See output config	See config	R/W	
401917	1916	0x077C	1	Cycle Time	Word	3 ms	See output config	See config	R/W	
401918	1917	0x077D	1	Time Decimal Digits	Byte	0	See output config	See config	R/W	
401919	1918	0x077E	2	Output Tolerance	Float	0.01	See output config	See config	R/W	
401921	1920	0x0780	2	Integral Gain	Float	1	See output config	See config	R/W	
401923	1922	0x0782	2	Derivative Gain	Float	1	See output config	See config	R/W	
401925	1924	0x0784	5	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>PID Function Block #6</b>										
401930	1929	0x0789	1	PID Enabled	Byte	FALSE	See output config	N/A	R/W	
401931	1930	0x078A	1	Target Source	Byte	No Source	See output config	See config	R/W	
401932	1931	0x078B	1	Target Number	Byte	1	See output config	See config	R/W	
401933	1932	0x078C	1	Feedback Source	Byte	No Source	See output config	See config	R/W	
401934	1933	0x078D	1	Feedback Number	Byte	1	See output config	See config	R/W	
401935	1934	0x078E	1	Control Response	Byte	0, Single Output	See output config	See config	R/W	
401936	1935	0x078F	2	Proportional Band	Float	0.5	See output config	See config	R/W	
401938	1937	0x0791	2	Integral Gain	Float	0.005	See output config	See config	R/W	
401940	1939	0x0793	2	Derivative Time	Float	0.001	See output config	See config	R/W	
401942	1941	0x0795	1	Cycle Time	Word	3 ms	See output config	See config	R/W	
401943	1942	0x0796	1	Time Decimal Digits	Byte	0	See output config	See config	R/W	
401944	1943	0x0797	2	Output Tolerance	Float	0.01	See output config	See config	R/W	
401946	1945	0x0799	2	Integral Gain	Float	1	See output config	See config	R/W	
401948	1947	0x079B	2	Derivative Gain	Float	1	See output config	See config	R/W	
401950	1949	0x079D	5	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>PID Function Block #7</b>										
401955	1954	0x07A2	1	PID Enabled	Byte	FALSE	See output config	N/A	R/W	
401956	1955	0x07A3	1	Target Source	Byte	No Source	See output config	See config	R/W	
401957	1956	0x07A4	1	Target Number	Byte	1	See output config	See config	R/W	
401958	1957	0x07A5	1	Feedback Source	Byte	No Source	See output config	See config	R/W	
401959	1958	0x07A6	1	Feedback Number	Byte	1	See output config	See config	R/W	
401960	1959	0x07A7	1	Control Response	Byte	0, Single Output	See output config	See config	R/W	
401961	1960	0x07A8	2	Proportional Band	Float	0.5	See output config	See config	R/W	



401963	1962	0x07AA	2	Integral Gain	Float	0.005	See output config	See config	R/W	
401965	1964	0x07AC	2	Derivative Time	Float	0.001	See output config	See config	R/W	
401967	1966	0x07AE	1	Cycle Time	Word	3 ms	See output config	See config	R/W	
401968	1967	0x07AF	1	Time Decimal Digits	Byte	0	See output config	See config	R/W	
401969	1968	0x07B0	2	Output Tolerance	Float	0.01	See output config	See config	R/W	
401971	1970	0x07B2	2	Integral Gain	Float	1	See output config	See config	R/W	
401973	1972	0x07B4	2	Derivative Gain	Float	1	See output config	See config	R/W	
401975	1974	0x07B6	5	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>PID Function Block #8</b>										
401980	1979	0x07BB	1	PID Enabled	Byte	FALSE	See output config	N/A	R/W	
401981	1980	0x07BC	1	Target Source	Byte	No Source	See output config	See config	R/W	
401982	1981	0x07BD	1	Target Number	Byte	1	See output config	See config	R/W	
401983	1982	0x07BE	1	Feedback Source	Byte	No Source	See output config	See config	R/W	
401984	1983	0x07BF	1	Feedback Number	Byte	1	See output config	See config	R/W	
401985	1984	0x07C0	1	Control Response	Byte	0, Single Output	See output config	See config	R/W	
401986	1985	0x07C1	2	Proportional Band	Float	0.5	See output config	See config	R/W	
401988	1987	0x07C3	2	Integral Gain	Float	0.005	See output config	See config	R/W	
401990	1989	0x07C5	2	Derivative Time	Float	0.001	See output config	See config	R/W	
401992	1991	0x07C7	1	Cycle Time	Word	3 ms	See output config	See config	R/W	
401993	1992	0x07C8	1	Time Decimal Digits	Byte	0	See output config	See config	R/W	
401994	1993	0x07C9	2	Output Tolerance	Float	0.01	See output config	See config	R/W	
401996	1995	0x07CB	2	Integral Gain	Float	1	See output config	See config	R/W	
401998	1997	0x07CD	2	Derivative Gain	Float	1	See output config	See config	R/W	
402000	1999	0x07CF	5	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>PID Function Block #9</b>										
402005	2004	0x07D4	1	PID Enabled	Byte	FALSE	See output config	N/A	R/W	
402006	2005	0x07D5	1	Target Source	Byte	No Source	See output config	See config	R/W	
402007	2006	0x07D6	1	Target Number	Byte	1	See output config	See config	R/W	
402008	2007	0x07D7	1	Feedback Source	Byte	No Source	See output config	See config	R/W	
402009	2008	0x07D8	1	Feedback Number	Byte	1	See output config	See config	R/W	
402010	2009	0x07D9	1	Control Response	Byte	0, Single Output	See output config	See config	R/W	
402011	2010	0x07DA	2	Proportional Band	Float	0.5	See output config	See config	R/W	
402013	2012	0x07DC	2	Integral Gain	Float	0.005	See output config	See config	R/W	
402015	2014	0x07DE	2	Derivative Time	Float	0.001	See output config	See config	R/W	
402017	2016	0x07E0	1	Cycle Time	Word	3 ms	See output config	See config	R/W	
402018	2017	0x07E1	1	Time Decimal Digits	Byte	0	See output config	See config	R/W	
402019	2018	0x07E2	2	Output Tolerance	Float	0.01	See output config	See config	R/W	
402021	2020	0x07E4	2	Integral Gain	Float	1	See output config	See config	R/W	
402023	2022	0x07E6	2	Derivative Gain	Float	1	See output config	See config	R/W	
402025	2024	0x07E8	5	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>PID Function Block #10</b>										
402030	2029	0x07ED	1	PID Enabled	Byte	FALSE	See output config	N/A	R/W	
402031	2030	0x07EE	1	Target Source	Byte	No Source	See output config	See config	R/W	
402032	2031	0x07EF	1	Target Number	Byte	1	See output config	See config	R/W	
402033	2032	0x07F0	1	Feedback Source	Byte	No Source	See output config	See config	R/W	
402034	2033	0x07F1	1	Feedback Number	Byte	1	See output config	See config	R/W	
402035	2034	0x07F2	1	Control Response	Byte	0, Single Output	See output config	See config	R/W	
402036	2035	0x07F3	2	Proportional Band	Float	0.5	See output config	See config	R/W	
402038	2037	0x07F5	2	Integral Gain	Float	0.005	See output config	See config	R/W	
402040	2039	0x07F7	2	Derivative Time	Float	0.001	See output config	See config	R/W	
402042	2041	0x07F9	1	Cycle Time	Word	3 ms	See output config	See config	R/W	

402043	2042	0x07FA	1	Time Decimal Digits	Byte		0	See output config	See config	R/W	
402044	2043	0x07FB	2	Output Tolerance	Float		0.01	See output config	See config	R/W	
402046	2045	0x07FD	2	Integral Gain	Float		1	See output config	See config	R/W	
402048	2047	0x07FF	2	Derivative Gain	Float		1	See output config	See config	R/W	
402050	2049	0x0801	5	Reserved	N/A	N/A	N/A		N/A	RO	Reserved for future use. Reading results 0. Writing
<b>PID Function Block #11</b>											
402055	2054	0x0806	1	PID Enabled	Byte		FALSE	See output config	N/A	R/W	
402056	2055	0x0807	1	Target Source	Byte		No Source	See output config	See config	R/W	
402057	2056	0x0808	1	Target Number	Byte		1	See output config	See config	R/W	
402058	2057	0x0809	1	Feedback Source	Byte		No Source	See output config	See config	R/W	
402059	2058	0x080A	1	Feedback Number	Byte		1	See output config	See config	R/W	
402060	2059	0x080B	1	Control Response	Byte		0, Single Output	See output config	See config	R/W	
402061	2060	0x080C	2	Proportional Band	Float		0.5	See output config	See config	R/W	
402063	2062	0x080E	2	Integral Gain	Float		0.005	See output config	See config	R/W	
402065	2064	0x0810	2	Derivative Time	Float		0.001	See output config	See config	R/W	
402067	2066	0x0812	1	Cycle Time	Word		3 ms	See output config	See config	R/W	
402068	2067	0x0813	1	Time Decimal Digits	Byte		0	See output config	See config	R/W	
402069	2068	0x0814	2	Output Tolerance	Float		0.01	See output config	See config	R/W	
402071	2070	0x0816	2	Integral Gain	Float		1	See output config	See config	R/W	
402073	2072	0x0818	2	Derivative Gain	Float		1	See output config	See config	R/W	
402075	2074	0x081A	5	Reserved	N/A	N/A	N/A		N/A	RO	Reserved for future use. Reading results 0. Writing
<b>PID Function Block #12</b>											
402080	2079	0x081F	1	PID Enabled	Byte		FALSE	See output config	N/A	R/W	
402081	2080	0x0820	1	Target Source	Byte		No Source	See output config	See config	R/W	
402082	2081	0x0821	1	Target Number	Byte		1	See output config	See config	R/W	
402083	2082	0x0822	1	Feedback Source	Byte		No Source	See output config	See config	R/W	
402084	2083	0x0823	1	Feedback Number	Byte		1	See output config	See config	R/W	
402085	2084	0x0824	1	Control Response	Byte		0, Single Output	See output config	See config	R/W	
402086	2085	0x0825	2	Proportional Band	Float		0.5	See output config	See config	R/W	
402088	2087	0x0827	2	Integral Gain	Float		0.005	See output config	See config	R/W	
402090	2089	0x0829	2	Derivative Time	Float		0.001	See output config	See config	R/W	
402092	2091	0x082B	2	Cycle Time	Word		3 ms	See output config	See config	R/W	
402094	2093	0x082D	2	Time Decimal Digits	Byte		0	See output config	See config	R/W	
402096	2095	0x082F	2	Output Tolerance	Float		0.01	See output config	See config	R/W	
402098	2097	0x0831	2	Integral Gain	Float		1	See output config	See config	R/W	
402100	2099	0x0833	2	Derivative Gain	Float		1	See output config	See config	R/W	
402102	2101	0x0835	59	Reserved	N/A	N/A	N/A		N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Control Constant Data</b>											
402105	2104	0x0838	2	Constant FALSE	Float		0	See output config	See config	R/W	
402107	2106	0x083A	2	Reserved	N/A	N/A					
402109	2108	0x083C	2	Constant TRUE	Float		1	See output config	See config	R/W	
402111	2110	0x083E	2	Reserved	N/A	N/A					
402113	2112	0x0840	2	Constant Data #3	Float		0	See output config	See config	R/W	
402115	2114	0x0842	2	Reserved	N/A	N/A					
402117	2116	0x0844	2	Constant Data #4	Float		0	See output config	See config	R/W	
402119	2118	0x0846	2	Reserved	N/A	N/A					
402121	2120	0x0848	2	Constant Data #5	Float		0	See output config	See config	R/W	
402123	2122	0x084A	2	Reserved	N/A	N/A					
402125	2124	0x084C	2	Constant Data #6	Float		0	See output config	See config	R/W	
402127	2126	0x084E	2	Reserved	N/A	N/A					
402129	2128	0x0850	2	Constant Data #7	Float		0	See output config	See config	R/W	

402131	2130	0x0852	2	Reserved	N/A	N/A				
402133	2132	0x0854	2	Constant Data #8	Float	0	See output config	See config	R/W	
402135	2134	0x0856	2	Reserved	N/A	N/A				
402137	2136	0x0858	2	Constant Data #9	Float	0	See output config	See config	R/W	
402139	2138	0x085A	2	Reserved	N/A	N/A				
402141	2140	0x085C	2	Constant Data #10	Float	0	See output config	See config	R/W	
402143	2142	0x085E	2	Reserved	N/A	N/A				
402145	2144	0x0860	2	Constant Data #11	Float	0	See output config	See config	R/W	
402147	2146	0x0862	2	Reserved	N/A	N/A				
402149	2148	0x0864	2	Constant Data #12	Float	0	See output config	See config	R/W	
402151	2150	0x0866	2	Reserved	N/A	N/A				
402153	2152	0x0868	2	Constant Data #13	Float	0	See output config	See config	R/W	
402155	2154	0x086A	2	Reserved	N/A	N/A				
402157	2156	0x086C	2	Constant Data #14	Float	0	See output config	See config	R/W	
402159	2158	0x086E	2	Reserved	N/A	N/A				
402161	2160	0x0870	2	Constant Data #15	Float	0	See output config	See config	R/W	
402163	2162	0x0872	2	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Lookup Table 1</b>										
402165	2164	0x0874	1	X-Axis Source	Byte	0 - Control Not Used		N/A	R/W	
402166	2165	0x0875	1	X-Axis Number	Byte	1	Depends on control source	N/A	R/W	
402167	2166	0x0876	1	X-Axis Type	Byte			N/A	R/W	
402168	2167	0x0877	1	Auto Repeat	Byte	0		N/A	R/W	
402169	2168	0x0878	1	Response 1	Byte	No Response		N/A	R/W	
402170	2169	0x0879	1	Response 2	Byte	No Response		N/A	R/W	
402171	2170	0x087A	1	Response 3	Byte	No Response		N/A	R/W	
402172	2171	0x087B	1	Response 4	Byte	No Response		N/A	R/W	
402173	2172	0x087C	1	Response 5	Byte	No Response		N/A	R/W	
402174	2173	0x087D	1	Response 6	Byte	No Response		N/A	R/W	
402175	2174	0x087E	1	Response 7	Byte	No Response		N/A	R/W	
402176	2175	0x087F	1	Response 8	Byte	No Response		N/A	R/W	
402177	2176	0x0880	1	Response 9	Byte	No Response		N/A	R/W	
402178	2177	0x0881	1	Response 10	Byte	No Response		N/A	R/W	
402179	2178	0x0882	2	Point X0	Float	0		N/A	R/W	
402181	2180	0x0884	2	Point X1	Float	5		N/A	R/W	
402183	2182	0x0886	2	Point X2	Float	10		N/A	R/W	
402185	2184	0x0888	2	Point X3	Float	15		N/A	R/W	
402187	2186	0x088A	2	Point X4	Float	20		N/A	R/W	
402189	2188	0x088C	2	Point X5	Float	25		N/A	R/W	
402191	2190	0x088E	2	Point X6	Float	30		N/A	R/W	
402193	2192	0x0890	2	Point X7	Float	35		N/A	R/W	
402195	2194	0x0892	2	Point X8	Float	40		N/A	R/W	
402197	2196	0x0894	2	Point X9	Float	45		N/A	R/W	
402199	2198	0x0896	2	Point X10	Float	50		N/A	R/W	
402201	2200	0x0898	2	Point Y0	Float	0	-10 <sup>6</sup> to 10 <sup>5</sup>	N/A	R/W	
402203	2202	0x089A	2	Point Y1	Float	10	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402205	2204	0x089C	2	Point Y2	Float	20	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402207	2206	0x089E	2	Point Y3	Float	30	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402209	2208	0x08A0	2	Point Y4	Float	40	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402211	2210	0x08A2	2	Point Y5	Float	50	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402213	2212	0x08A4	2	Point Y6	Float	60	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402215	2214	0x08A6	2	Point Y7	Float	70	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	

402217	2216	0x08A8		2	Point Y8	Float	80	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402219	2218	0x08AA		2	Point Y9	Float	90	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402221	2220	0x08AC		2	Point Y10	Float	100	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402223	2222	0x08AE	#REF!		Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing

**Lookup Table 2**

402230	2229	0x08B5		1	X-Axis Source	Byte	0 - Control Not Used		N/A	R/W	
402231	2230	0x08B6		1	X-Axis Number	Byte	1	Depends on control source	N/A	R/W	
402232	2231	0x08B7		1	X-Axis Type	Byte			N/A	R/W	
402233	2232	0x08B8		1	Auto Repeat	Byte	0		N/A	R/W	
402234	2233	0x08B9		1	Response 1	Byte	No Response		N/A	R/W	
402235	2234	0x08BA		1	Response 2	Byte	No Response		N/A	R/W	
402236	2235	0x08BB		1	Response 3	Byte	No Response		N/A	R/W	
402237	2236	0x08BC		1	Response 4	Byte	No Response		N/A	R/W	
402238	2237	0x08BD		1	Response 5	Byte	No Response		N/A	R/W	
402239	2238	0x08BE		1	Response 6	Byte	No Response		N/A	R/W	
402240	2239	0x08BF		1	Response 7	Byte	No Response		N/A	R/W	
402241	2240	0x08C0		1	Response 8	Byte	No Response		N/A	R/W	
402242	2241	0x08C1		1	Response 9	Byte	No Response		N/A	R/W	
402243	2242	0x08C2		1	Response 10	Byte	No Response		N/A	R/W	
402244	2243	0x08C3		2	Point X0	Float	0		N/A	R/W	
402246	2245	0x08C5		2	Point X1	Float	5		N/A	R/W	
402248	2247	0x08C7		2	Point X2	Float	10		N/A	R/W	
402250	2249	0x08C9		2	Point X3	Float	15		N/A	R/W	
402252	2251	0x08CB		2	Point X4	Float	20		N/A	R/W	
402254	2253	0x08CD		2	Point X5	Float	25		N/A	R/W	
402256	2255	0x08CF		2	Point X6	Float	30		N/A	R/W	
402258	2257	0x08D1		2	Point X7	Float	35		N/A	R/W	
402260	2259	0x08D3		2	Point X8	Float	40		N/A	R/W	
402262	2261	0x08D5		2	Point X9	Float	45		N/A	R/W	
402264	2263	0x08D7		2	Point X10	Float	50		N/A	R/W	
402266	2265	0x08D9		2	Point Y0	Float	0	-10 <sup>6</sup> to 10 <sup>5</sup>	N/A	R/W	
402268	2267	0x08DB		2	Point Y1	Float	10	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402270	2269	0x08DD		2	Point Y2	Float	20	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402272	2271	0x08DF		2	Point Y3	Float	30	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402274	2273	0x08E1		2	Point Y4	Float	40	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402276	2275	0x08E3		2	Point Y5	Float	50	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402278	2277	0x08E5		2	Point Y6	Float	60	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402280	2279	0x08E7		2	Point Y7	Float	70	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402282	2281	0x08E9		2	Point Y8	Float	80	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402284	2283	0x08EB		2	Point Y9	Float	90	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402286	2285	0x08ED		2	Point Y10	Float	100	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402288	2287	0x08EF		7	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing

**Lookup Table 3**

402295	2294	0x08F6		1	X-Axis Source	Byte	0 - Control Not Used		N/A	R/W	
402296	2295	0x08F7		1	X-Axis Number	Byte	1	Depends on control source	N/A	R/W	
402297	2296	0x08F8		1	X-Axis Type	Byte			N/A	R/W	
402298	2297	0x08F9		1	Auto Repeat	Byte	0		N/A	R/W	
402299	2298	0x08FA		1	Response 1	Byte	No Response		N/A	R/W	
402300	2299	0x08FB		1	Response 2	Byte	No Response		N/A	R/W	
402301	2300	0x08FC		1	Response 3	Byte	No Response		N/A	R/W	

402302	2301	0x08FD	1	Response 4	Byte	No Response		N/A	R/W	
402303	2302	0x08FE	1	Response 5	Byte	No Response		N/A	R/W	
402304	2303	0x08FF	1	Response 6	Byte	No Response		N/A	R/W	
402305	2304	0x0900	1	Response 7	Byte	No Response		N/A	R/W	
402306	2305	0x0901	1	Response 8	Byte	No Response		N/A	R/W	
402307	2306	0x0902	1	Response 9	Byte	No Response		N/A	R/W	
402308	2307	0x0903	1	Response 10	Byte	No Response		N/A	R/W	
402309	2308	0x0904	2	Point X0	Float	0		N/A	R/W	
402311	2310	0x0906	2	Point X1	Float	5		N/A	R/W	
402313	2312	0x0908	2	Point X2	Float	10		N/A	R/W	
402315	2314	0x090A	2	Point X3	Float	15		N/A	R/W	
402317	2316	0x090C	2	Point X4	Float	20		N/A	R/W	
402319	2318	0x090E	2	Point X5	Float	25		N/A	R/W	
402321	2320	0x0910	2	Point X6	Float	30		N/A	R/W	
402323	2322	0x0912	2	Point X7	Float	35		N/A	R/W	
402325	2324	0x0914	2	Point X8	Float	40		N/A	R/W	
402327	2326	0x0916	2	Point X9	Float	45		N/A	R/W	
402329	2328	0x0918	2	Point X10	Float	50		N/A	R/W	
402331	2330	0x091A	2	Point Y0	Float	0	$-10^6$ to $10^6$	N/A	R/W	
402333	2332	0x091C	2	Point Y1	Float	10	$-10^6$ to $10^6$	N/A	R/W	
402335	2334	0x091E	2	Point Y2	Float	20	$-10^6$ to $10^6$	N/A	R/W	
402337	2336	0x0920	2	Point Y3	Float	30	$-10^6$ to $10^6$	N/A	R/W	
402339	2338	0x0922	2	Point Y4	Float	40	$-10^6$ to $10^6$	N/A	R/W	
402341	2340	0x0924	2	Point Y5	Float	50	$-10^6$ to $10^6$	N/A	R/W	
402343	2342	0x0926	2	Point Y6	Float	60	$-10^6$ to $10^6$	N/A	R/W	
402345	2344	0x0928	2	Point Y7	Float	70	$-10^6$ to $10^6$	N/A	R/W	
402347	2346	0x092A	2	Point Y8	Float	80	$-10^6$ to $10^6$	N/A	R/W	
402349	2348	0x092C	2	Point Y9	Float	90	$-10^6$ to $10^6$	N/A	R/W	
402351	2350	0x092E	2	Point Y10	Float	100	$-10^6$ to $10^6$	N/A	R/W	
402353	2352	0x0930	7	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Lookup Table 4</b>										
402360	2359	0x0937	1	X-Axis Source	Byte	0 - Control Not Used		N/A	R/W	
402361	2360	0x0938	1	X-Axis Number	Byte	1	Depends on control source	N/A	R/W	
402362	2361	0x0939	1	X-Axis Type	Byte			N/A	R/W	
402363	2362	0x093A	1	Auto Repeat	Byte	0		N/A	R/W	
402364	2363	0x093B	1	Response 1	Byte	No Response		N/A	R/W	
402365	2364	0x093C	1	Response 2	Byte	No Response		N/A	R/W	
402366	2365	0x093D	1	Response 3	Byte	No Response		N/A	R/W	
402367	2366	0x093E	1	Response 4	Byte	No Response		N/A	R/W	
402368	2367	0x093F	1	Response 5	Byte	No Response		N/A	R/W	
402369	2368	0x0940	1	Response 6	Byte	No Response		N/A	R/W	
402370	2369	0x0941	1	Response 7	Byte	No Response		N/A	R/W	
402371	2370	0x0942	1	Response 8	Byte	No Response		N/A	R/W	
402372	2371	0x0943	1	Response 9	Byte	No Response		N/A	R/W	
402373	2372	0x0944	1	Response 10	Byte	No Response		N/A	R/W	
402374	2373	0x0945	2	Point X0	Float	0		N/A	R/W	
402376	2375	0x0947	2	Point X1	Float	5		N/A	R/W	
402378	2377	0x0949	2	Point X2	Float	10		N/A	R/W	
402380	2379	0x094B	2	Point X3	Float	15		N/A	R/W	
402382	2381	0x094D	2	Point X4	Float	20		N/A	R/W	

402384	2383	0x094F	2	Point X5	Float	25		N/A	R/W	
402386	2385	0x0951	2	Point X6	Float	30		N/A	R/W	
402388	2387	0x0953	2	Point X7	Float	35		N/A	R/W	
402390	2389	0x0955	2	Point X8	Float	40		N/A	R/W	
402392	2391	0x0957	2	Point X9	Float	45		N/A	R/W	
402394	2393	0x0959	2	Point X10	Float	50		N/A	R/W	
402396	2395	0x095B	2	Point Y0	Float	0	-10 <sup>6</sup> to 10 <sup>5</sup>	N/A	R/W	
402398	2397	0x095D	2	Point Y1	Float	10	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402400	2399	0x095F	2	Point Y2	Float	20	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402402	2401	0x0961	2	Point Y3	Float	30	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402404	2403	0x0963	2	Point Y4	Float	40	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402406	2405	0x0965	2	Point Y5	Float	50	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402408	2407	0x0967	2	Point Y6	Float	60	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402410	2409	0x0969	2	Point Y7	Float	70	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402412	2411	0x096B	2	Point Y8	Float	80	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402414	2413	0x096D	2	Point Y9	Float	90	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402416	2415	0x096F	2	Point Y10	Float	100	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402418	2417	0x0971	7	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Lookup Table 5</b>										
402425	2424	0x0978	1	X-Axis Source	Byte	0 - Control Not Used		N/A	R/W	
402426	2425	0x0979	1	X-Axis Number	Byte	1	Depends on control source	N/A	R/W	
402427	2426	0x097A	1	X-Axis Type	Byte			N/A	R/W	
402428	2427	0x097B	1	Auto Repeat	Byte	0		N/A	R/W	
402429	2428	0x097C	1	Response 1	Byte	No Response		N/A	R/W	
402430	2429	0x097D	1	Response 2	Byte	No Response		N/A	R/W	
402431	2430	0x097E	1	Response 3	Byte	No Response		N/A	R/W	
402432	2431	0x097F	1	Response 4	Byte	No Response		N/A	R/W	
402433	2432	0x0980	1	Response 5	Byte	No Response		N/A	R/W	
402434	2433	0x0981	1	Response 6	Byte	No Response		N/A	R/W	
402435	2434	0x0982	1	Response 7	Byte	No Response		N/A	R/W	
402436	2435	0x0983	1	Response 8	Byte	No Response		N/A	R/W	
402437	2436	0x0984	1	Response 9	Byte	No Response		N/A	R/W	
402438	2437	0x0985	1	Response 10	Byte	No Response		N/A	R/W	
402439	2438	0x0986	2	Point X0	Float	0		N/A	R/W	
402441	2440	0x0988	2	Point X1	Float	5		N/A	R/W	
402443	2442	0x098A	2	Point X2	Float	10		N/A	R/W	
402445	2444	0x098C	2	Point X3	Float	15		N/A	R/W	
402447	2446	0x098E	2	Point X4	Float	20		N/A	R/W	
402449	2448	0x0990	2	Point X5	Float	25		N/A	R/W	
402451	2450	0x0992	2	Point X6	Float	30		N/A	R/W	
402453	2452	0x0994	2	Point X7	Float	35		N/A	R/W	
402455	2454	0x0996	2	Point X8	Float	40		N/A	R/W	
402457	2456	0x0998	2	Point X9	Float	45		N/A	R/W	
402459	2458	0x099A	2	Point X10	Float	50		N/A	R/W	
402461	2460	0x099C	2	Point Y0	Float	0	-10 <sup>6</sup> to 10 <sup>5</sup>	N/A	R/W	
402463	2462	0x099E	2	Point Y1	Float	10	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402465	2464	0x09A0	2	Point Y2	Float	20	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402467	2466	0x09A2	2	Point Y3	Float	30	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402469	2468	0x09A4	2	Point Y4	Float	40	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402471	2470	0x09A6	2	Point Y5	Float	50	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	

402473	2472	0x09A8	2	Point Y6	Float	60	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402475	2474	0x09AA	2	Point Y7	Float	70	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402477	2476	0x09AC	2	Point Y8	Float	80	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402479	2478	0x09AE	2	Point Y9	Float	90	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402481	2480	0x09B0	2	Point Y10	Float	100	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402483	2482	0x09B2	7	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Lookup Table 6</b>										
402490	2489	0x09B9	1	X-Axis Source	Byte	0 - Control Not Used		N/A	R/W	
402491	2490	0x09BA	1	X-Axis Number	Byte	1	Depends on control source	N/A	R/W	
402492	2491	0x09BB	1	X-Axis Type	Byte			N/A	R/W	
402493	2492	0x09BC	1	Auto Repeat	Byte	0		N/A	R/W	
402494	2493	0x09BD	1	Response 1	Byte	No Response		N/A	R/W	
402495	2494	0x09BE	1	Response 2	Byte	No Response		N/A	R/W	
402496	2495	0x09BF	1	Response 3	Byte	No Response		N/A	R/W	
402497	2496	0x09C0	1	Response 4	Byte	No Response		N/A	R/W	
402498	2497	0x09C1	1	Response 5	Byte	No Response		N/A	R/W	
402499	2498	0x09C2	1	Response 6	Byte	No Response		N/A	R/W	
402500	2499	0x09C3	1	Response 7	Byte	No Response		N/A	R/W	
402501	2500	0x09C4	1	Response 8	Byte	No Response		N/A	R/W	
402502	2501	0x09C5	1	Response 9	Byte	No Response		N/A	R/W	
402503	2502	0x09C6	1	Response 10	Byte	No Response		N/A	R/W	
402504	2503	0x09C7	2	Point X0	Float	0		N/A	R/W	
402506	2505	0x09C9	2	Point X1	Float	5		N/A	R/W	
402508	2507	0x09CB	2	Point X2	Float	10		N/A	R/W	
402510	2509	0x09CD	2	Point X3	Float	15		N/A	R/W	
402512	2511	0x09CF	2	Point X4	Float	20		N/A	R/W	
402514	2513	0x09D1	2	Point X5	Float	25		N/A	R/W	
402516	2515	0x09D3	2	Point X6	Float	30		N/A	R/W	
402518	2517	0x09D5	2	Point X7	Float	35		N/A	R/W	
402520	2519	0x09D7	2	Point X8	Float	40		N/A	R/W	
402522	2521	0x09D9	2	Point X9	Float	45		N/A	R/W	
402524	2523	0x09DB	2	Point X10	Float	50		N/A	R/W	
402526	2525	0x09DD	2	Point Y0	Float	0	-10 <sup>6</sup> to 10 <sup>5</sup>	N/A	R/W	
402528	2527	0x09DF	2	Point Y1	Float	10	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402530	2529	0x09E1	2	Point Y2	Float	20	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402532	2531	0x09E3	2	Point Y3	Float	30	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402534	2533	0x09E5	2	Point Y4	Float	40	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402536	2535	0x09E7	2	Point Y5	Float	50	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402538	2537	0x09E9	2	Point Y6	Float	60	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402540	2539	0x09EB	2	Point Y7	Float	70	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402542	2541	0x09ED	2	Point Y8	Float	80	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402544	2543	0x09EF	2	Point Y9	Float	90	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402546	2545	0x09F1	2	Point Y10	Float	100	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402548	2547	0x09F3	7	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Lookup Table 7</b>										
402555	2554	0x09FA	1	X-Axis Source	Byte	0 - Control Not Used		N/A	R/W	
402556	2555	0x09FB	1	X-Axis Number	Byte	1	Depends on control source	N/A	R/W	
402557	2556	0x09FC	1	X-Axis Type	Byte			N/A	R/W	
402558	2557	0x09FD	1	Auto Repeat	Byte	0		N/A	R/W	
402559	2558	0x09FE	1	Response 1	Byte	No Response		N/A	R/W	

402560	2559	0x09FF	1	Response 2	Byte	No Response		N/A	R/W	
402561	2560	0x0A00	1	Response 3	Byte	No Response		N/A	R/W	
402562	2561	0x0A01	1	Response 4	Byte	No Response		N/A	R/W	
402563	2562	0x0A02	1	Response 5	Byte	No Response		N/A	R/W	
402564	2563	0x0A03	1	Response 6	Byte	No Response		N/A	R/W	
402565	2564	0x0A04	1	Response 7	Byte	No Response		N/A	R/W	
402566	2565	0x0A05	1	Response 8	Byte	No Response		N/A	R/W	
402567	2566	0x0A06	1	Response 9	Byte	No Response		N/A	R/W	
402568	2567	0x0A07	1	Response 10	Byte	No Response		N/A	R/W	
402569	2568	0x0A08	2	Point X0	Float	0		N/A	R/W	
402571	2570	0x0A0A	2	Point X1	Float	5		N/A	R/W	
402573	2572	0x0A0C	2	Point X2	Float	10		N/A	R/W	
402575	2574	0x0A0E	2	Point X3	Float	15		N/A	R/W	
402577	2576	0x0A10	2	Point X4	Float	20		N/A	R/W	
402579	2578	0x0A12	2	Point X5	Float	25		N/A	R/W	
402581	2580	0x0A14	2	Point X6	Float	30		N/A	R/W	
402583	2582	0x0A16	2	Point X7	Float	35		N/A	R/W	
402585	2584	0x0A18	2	Point X8	Float	40		N/A	R/W	
402587	2586	0x0A1A	2	Point X9	Float	45		N/A	R/W	
402589	2588	0x0A1C	2	Point X10	Float	50		N/A	R/W	
402591	2590	0x0A1E	2	Point Y0	Float	0	-10 <sup>6</sup> to 10 <sup>5</sup>	N/A	R/W	
402593	2592	0x0A20	2	Point Y1	Float	10	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402595	2594	0x0A22	2	Point Y2	Float	20	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402597	2596	0x0A24	2	Point Y3	Float	30	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402599	2598	0x0A26	2	Point Y4	Float	40	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402601	2600	0x0A28	2	Point Y5	Float	50	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402603	2602	0x0A2A	2	Point Y6	Float	60	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402605	2604	0x0A2C	2	Point Y7	Float	70	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402607	2606	0x0A2E	2	Point Y8	Float	80	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402609	2608	0x0A30	2	Point Y9	Float	90	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402611	2610	0x0A32	2	Point Y10	Float	100	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402613	2612	0x0A34	7	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Lookup Table 8</b>										
402620	2619	0x0A3B	1	X-Axis Source	Byte	0 - Control Not Used		N/A	R/W	
402621	2620	0x0A3C	1	X-Axis Number	Byte	1	Depends on control source	N/A	R/W	
402622	2621	0x0A3D	1	X-Axis Type	Byte			N/A	R/W	
402623	2622	0x0A3E	1	Auto Repeat	Byte	0		N/A	R/W	
402624	2623	0x0A3F	1	Response 1	Byte	No Response		N/A	R/W	
402625	2624	0x0A40	1	Response 2	Byte	No Response		N/A	R/W	
402626	2625	0x0A41	1	Response 3	Byte	No Response		N/A	R/W	
402627	2626	0x0A42	1	Response 4	Byte	No Response		N/A	R/W	
402628	2627	0x0A43	1	Response 5	Byte	No Response		N/A	R/W	
402629	2628	0x0A44	1	Response 6	Byte	No Response		N/A	R/W	
402630	2629	0x0A45	1	Response 7	Byte	No Response		N/A	R/W	
402631	2630	0x0A46	1	Response 8	Byte	No Response		N/A	R/W	
402632	2631	0x0A47	1	Response 9	Byte	No Response		N/A	R/W	
402633	2632	0x0A48	1	Response 10	Byte	No Response		N/A	R/W	
402634	2633	0x0A49	2	Point X0	Float	0		N/A	R/W	
402636	2635	0x0A4B	2	Point X1	Float	5		N/A	R/W	
402638	2637	0x0A4D	2	Point X2	Float	10		N/A	R/W	



402640	2639	0x0A4F	2	Point X3	Float	15		N/A	R/W	
402642	2641	0x0A51	2	Point X4	Float	20		N/A	R/W	
402644	2643	0x0A53	2	Point X5	Float	25		N/A	R/W	
402646	2645	0x0A55	2	Point X6	Float	30		N/A	R/W	
402648	2647	0x0A57	2	Point X7	Float	35		N/A	R/W	
402650	2649	0x0A59	2	Point X8	Float	40		N/A	R/W	
402652	2651	0x0A5B	2	Point X9	Float	45		N/A	R/W	
402654	2653	0x0A5D	2	Point X10	Float	50		N/A	R/W	
402656	2655	0x0A5F	2	Point Y0	Float	0	$-10^6$ to $10^5$	N/A	R/W	
402658	2657	0x0A61	2	Point Y1	Float	10	$-10^6$ to $10^6$	N/A	R/W	
402660	2659	0x0A63	2	Point Y2	Float	20	$-10^6$ to $10^6$	N/A	R/W	
402662	2661	0x0A65	2	Point Y3	Float	30	$-10^6$ to $10^6$	N/A	R/W	
402664	2663	0x0A67	2	Point Y4	Float	40	$-10^6$ to $10^6$	N/A	R/W	
402666	2665	0x0A69	2	Point Y5	Float	50	$-10^6$ to $10^6$	N/A	R/W	
402668	2667	0x0A6B	2	Point Y6	Float	60	$-10^6$ to $10^6$	N/A	R/W	
402670	2669	0x0A6D	2	Point Y7	Float	70	$-10^6$ to $10^6$	N/A	R/W	
402672	2671	0x0A6F	2	Point Y8	Float	80	$-10^6$ to $10^6$	N/A	R/W	
402674	2673	0x0A71	2	Point Y9	Float	90	$-10^6$ to $10^6$	N/A	R/W	
402676	2675	0x0A73	2	Point Y10	Float	100	$-10^6$ to $10^6$	N/A	R/W	
402678	2677	0x0A75	7	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing

**Lookup Table 9**

402685	2684	0x0A7C	1	X-Axis Source	Byte	0 - Control Not Used		N/A	R/W	
402686	2685	0x0A7D	1	X-Axis Number	Byte	1	Depends on control source	N/A	R/W	
402687	2686	0x0A7E	1	X-Axis Type	Byte			N/A	R/W	
402688	2687	0x0A7F	1	Auto Repeat	Byte	0		N/A	R/W	
402689	2688	0x0A80	1	Response 1	Byte	No Response		N/A	R/W	
402690	2689	0x0A81	1	Response 2	Byte	No Response		N/A	R/W	
402691	2690	0x0A82	1	Response 3	Byte	No Response		N/A	R/W	
402692	2691	0x0A83	1	Response 4	Byte	No Response		N/A	R/W	
402693	2692	0x0A84	1	Response 5	Byte	No Response		N/A	R/W	
402694	2693	0x0A85	1	Response 6	Byte	No Response		N/A	R/W	
402695	2694	0x0A86	1	Response 7	Byte	No Response		N/A	R/W	
402696	2695	0x0A87	1	Response 8	Byte	No Response		N/A	R/W	
402697	2696	0x0A88	1	Response 9	Byte	No Response		N/A	R/W	
402698	2697	0x0A89	1	Response 10	Byte	No Response		N/A	R/W	
402699	2698	0x0A8A	2	Point X0	Float	0		N/A	R/W	
402701	2700	0x0A8C	2	Point X1	Float	5		N/A	R/W	
402703	2702	0x0A8E	2	Point X2	Float	10		N/A	R/W	
402705	2704	0x0A90	2	Point X3	Float	15		N/A	R/W	
402707	2706	0x0A92	2	Point X4	Float	20		N/A	R/W	
402709	2708	0x0A94	2	Point X5	Float	25		N/A	R/W	
402711	2710	0x0A96	2	Point X6	Float	30		N/A	R/W	
402713	2712	0x0A98	2	Point X7	Float	35		N/A	R/W	
402715	2714	0x0A9A	2	Point X8	Float	40		N/A	R/W	
402717	2716	0x0A9C	2	Point X9	Float	45		N/A	R/W	
402719	2718	0x0A9E	2	Point X10	Float	50		N/A	R/W	
402721	2720	0x0AA0	2	Point Y0	Float	0	$-10^6$ to $10^5$	N/A	R/W	
402723	2722	0x0AA2	2	Point Y1	Float	10	$-10^6$ to $10^6$	N/A	R/W	
402725	2724	0x0AA4	2	Point Y2	Float	20	$-10^6$ to $10^6$	N/A	R/W	
402727	2726	0x0AA6	2	Point Y3	Float	30	$-10^6$ to $10^6$	N/A	R/W	

402729	2728	0x0AA8	2	Point Y4	Float	40	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402731	2730	0x0AAA	2	Point Y5	Float	50	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402733	2732	0x0AAC	2	Point Y6	Float	60	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402735	2734	0x0AAE	2	Point Y7	Float	70	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402737	2736	0x0AB0	2	Point Y8	Float	80	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402739	2738	0x0AB2	2	Point Y9	Float	90	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402741	2740	0x0AB4	2	Point Y10	Float	100	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402743	2742	0x0AB6	7	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing

**Lookup Table 10**

402750	2749	0x0ABD	1	X-Axis Source	Byte	0 - Control Not Used		N/A	R/W	
402751	2750	0x0ABE	1	X-Axis Number	Byte	1	Depends on control source	N/A	R/W	
402752	2751	0x0ABF	1	X-Axis Type	Byte			N/A	R/W	
402753	2752	0x0AC0	1	Auto Repeat	Byte	0		N/A	R/W	
402754	2753	0x0AC1	1	Response 1	Byte	No Response		N/A	R/W	
402755	2754	0x0AC2	1	Response 2	Byte	No Response		N/A	R/W	
402756	2755	0x0AC3	1	Response 3	Byte	No Response		N/A	R/W	
402757	2756	0x0AC4	1	Response 4	Byte	No Response		N/A	R/W	
402758	2757	0x0AC5	1	Response 5	Byte	No Response		N/A	R/W	
402759	2758	0x0AC6	1	Response 6	Byte	No Response		N/A	R/W	
402760	2759	0x0AC7	1	Response 7	Byte	No Response		N/A	R/W	
402761	2760	0x0AC8	1	Response 8	Byte	No Response		N/A	R/W	
402762	2761	0x0AC9	1	Response 9	Byte	No Response		N/A	R/W	
402763	2762	0x0ACA	1	Response 10	Byte	No Response		N/A	R/W	
402764	2763	0x0ACB	2	Point X0	Float	0		N/A	R/W	
402766	2765	0x0ACD	2	Point X1	Float	5		N/A	R/W	
402768	2767	0x0ACF	2	Point X2	Float	10		N/A	R/W	
402770	2769	0x0AD1	2	Point X3	Float	15		N/A	R/W	
402772	2771	0x0AD3	2	Point X4	Float	20		N/A	R/W	
402774	2773	0x0AD5	2	Point X5	Float	25		N/A	R/W	
402776	2775	0x0AD7	2	Point X6	Float	30		N/A	R/W	
402778	2777	0x0AD9	2	Point X7	Float	35		N/A	R/W	
402780	2779	0x0ADB	2	Point X8	Float	40		N/A	R/W	
402782	2781	0x0ADD	2	Point X9	Float	45		N/A	R/W	
402784	2783	0x0ADF	2	Point X10	Float	50		N/A	R/W	
402786	2785	0x0AE1	2	Point Y0	Float	0	-10 <sup>6</sup> to 10 <sup>5</sup>	N/A	R/W	
402788	2787	0x0AE3	2	Point Y1	Float	10	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402790	2789	0x0AE5	2	Point Y2	Float	20	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402792	2791	0x0AE7	2	Point Y3	Float	30	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402794	2793	0x0AE9	2	Point Y4	Float	40	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402796	2795	0x0AEB	2	Point Y5	Float	50	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402798	2797	0x0AED	2	Point Y6	Float	60	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402800	2799	0x0AEF	2	Point Y7	Float	70	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402802	2801	0x0AF1	2	Point Y8	Float	80	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402804	2803	0x0AF3	2	Point Y9	Float	90	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402806	2805	0x0AF5	2	Point Y10	Float	100	-10 <sup>6</sup> to 10 <sup>6</sup>	N/A	R/W	
402808	2807	0x0AF7	7	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing

**Programmable Logic 1**

402815	2814	0x0AFE	1	Logic Enabled	Byte	0 - No	No / Yes	N/A	R/W	
402816	2815	0x0AFF	1	Table Number 1	Byte	1 - Lookup Tab	Drop List	N/A	R/W	
402817	2816	0x0B00	1	Logical Operator 1	Byte	0	Drop List	N/A	R/W	

402818	2817	0x0B01	1	Table 1 - Condition 1 Argument 1 Sour	Byte	0	Drop List	N/A	R/W	
402819	2818	0x0B02	1	Table 1 - Condition 1 Argument 1 Num	Byte	1		N/A	R/W	
402820	2819	0x0B03	1	Table 1 - Condition 1 Argument 2 Sour	Byte	0	Drop List	N/A	R/W	
402821	2820	0x0B04	1	Table 1 - Condition 1 Argument 2 Num	Byte	1		N/A	R/W	
402822	2821	0x0B05	1	Table 1 - Condition 1 Operator	Byte	0 =, Equal	Drop List	N/A	R/W	
402823	2822	0x0B06	1	Table 1 - Condition 2 Argument 1 Sour	Byte	0	Drop List	N/A	R/W	
402824	2823	0x0B07	1	Table 1 - Condition 2 Argument 1 Num	Byte	1		N/A	R/W	
402825	2824	0x0B08	1	Table 1 - Condition 2 Argument 2 Sour	Byte	0	Drop List	N/A	R/W	
402826	2825	0x0B09	1	Table 1 - Condition 2 Argument 2 Num	Byte	1		N/A	R/W	
402827	2826	0x0B0A	1	Table 1 - Condition 2 Operator	Byte	0 =, Equal	Drop List	N/A	R/W	
402828	2827	0x0B0B	1	Table 1 - Condition 3 Argument 1 Sour	Byte	0	Drop List	N/A	R/W	
402829	2828	0x0B0C	1	Table 1 - Condition 3 Argument 1 Num	Byte	1		N/A	R/W	
402830	2829	0x0B0D	1	Table 1 - Condition 3 Argument 2 Sour	Byte	0	Drop List	N/A	R/W	
402831	2830	0x0B0E	1	Table 1 - Condition 3 Argument 2 Num	Byte	1		N/A	R/W	
402832	2831	0x0B0F	1	Table 1 - Condition 3 Operator	Byte	0 =, Equal	Drop List	N/A	R/W	
402833	2832	0x0B10	1	Table Number 2	Byte	1 - Lookup Tab	Drop List	N/A	R/W	
402834	2833	0x0B11	1	Logical Operator 2	Byte	1 - Cnd1 & Cnd	Drop List	N/A	R/W	
402835	2834	0x0B12	1	Table 2 - Condition 1 Argument 1 Sour	Byte	1 - CAN Receive	Drop List	N/A	R/W	
402836	2835	0x0B13	1	Table 2 - Condition 1 Argument 1 Num	Byte	1		N/A	R/W	
402837	2836	0x0B14	1	Table 2 - Condition 1 Argument 2 Sour	Byte	3 - Constant Co	Drop List	N/A	R/W	
402838	2837	0x0B15	1	Table 2 - Condition 1 Argument 2 Num	Byte	1		N/A	R/W	
402839	2838	0x0B16	1	Table 2 - Condition 1 Operator	Byte	0 =, Equal	Drop List	N/A	R/W	
402840	2839	0x0B17	1	Table 2 - Condition 2 Argument 1 Sour	Byte	0	Drop List	N/A	R/W	
402841	2840	0x0B18	1	Table 2 - Condition 2 Argument 1 Num	Byte	1		N/A	R/W	
402842	2841	0x0B19	1	Table 2 - Condition 2 Argument 2 Sour	Byte	0	Drop List	N/A	R/W	
402843	2842	0x0B1A	1	Table 2 - Condition 2 Argument 2 Num	Byte	1		N/A	R/W	
402844	2843	0x0B1B	1	Table 2 - Condition 2 Operator	Byte	0 =, Equal	Drop List	N/A	R/W	
402845	2844	0x0B1C	1	Table 2 - Condition 3 Argument 1 Sour	Byte	0	Drop List	N/A	R/W	
402846	2845	0x0B1D	1	Table 2 - Condition 3 Argument 1 Num	Byte	1		N/A	R/W	
402847	2846	0x0B1E	1	Table 2 - Condition 3 Argument 2 Sour	Byte	0	Drop List	N/A	R/W	
402848	2847	0x0B1F	1	Table 2 - Condition 3 Argument 2 Num	Byte	1		N/A	R/W	
402849	2848	0x0B20	1	Table 2 - Condition 3 Operator	Byte	0 =, Equal	Drop List	N/A	R/W	
402850	2849	0x0B21	1	Table Number 3	Byte	0	Drop List	N/A	R/W	
402851	2850	0x0B22	1	Logical Operator 3	Byte	0	Drop List	N/A	R/W	
402852	2851	0x0B23	1	Table 3 - Condition 1 Argument 1 Sour	Byte	0	Drop List	N/A	R/W	
402853	2852	0x0B24	1	Table 3 - Condition 1 Argument 1 Num	Byte	1		N/A	R/W	
402854	2853	0x0B25	1	Table 3 - Condition 1 Argument 2 Sour	Byte	0	Drop List	N/A	R/W	
402855	2854	0x0B26	1	Table 3 - Condition 1 Argument 2 Num	Byte	1		N/A	R/W	
402856	2855	0x0B27	1	Table 3 - Condition 1 Operator	Byte	0 =, Equal	Drop List	N/A	R/W	
402857	2856	0x0B28	1	Table 3 - Condition 2 Argument 1 Sour	Byte	0	Drop List	N/A	R/W	
402858	2857	0x0B29	1	Table 3 - Condition 2 Argument 1 Num	Byte	1		N/A	R/W	
402859	2858	0x0B2A	1	Table 3 - Condition 2 Argument 2 Sour	Byte	0	Drop List	N/A	R/W	
402860	2859	0x0B2B	1	Table 3 - Condition 2 Argument 2 Num	Byte	1		N/A	R/W	
402861	2860	0x0B2C	1	Table 3 - Condition 2 Operator	Byte	0 =, Equal	Drop List	N/A	R/W	
402862	2861	0x0B2D	1	Table 3 - Condition 3 Argument 1 Sour	Byte	0	Drop List	N/A	R/W	
402863	2862	0x0B2E	1	Table 3 - Condition 3 Argument 1 Num	Byte	1		N/A	R/W	
402864	2863	0x0B2F	1	Table 3 - Condition 3 Argument 2 Sour	Byte	0	Drop List	N/A	R/W	
402865	2864	0x0B30	1	Table 3 - Condition 3 Argument 2 Num	Byte	1		N/A	R/W	
402866	2865	0x0B31	1	Table 3 - Condition 3 Operator	Byte	0 =, Equal	Drop List	N/A	R/W	
402867	2866	0x0B32	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing

**Programmable Logic 2**

402875	2874	0x0B3A	1	Logic Enabled	Byte	0 - No	No / Yes	N/A	R/W	
402876	2875	0x0B3B	1	Table Number 1	Byte	1 - Lookup Tab	Drop List	N/A	R/W	
402877	2876	0x0B3C	1	Logical Operator 1	Byte	0	Drop List	N/A	R/W	
402878	2877	0x0B3D	1	Table 1 - Condition 1 Argument 1 Sour	Byte	0	Drop List	N/A	R/W	
402879	2878	0x0B3E	1	Table 1 - Condition 1 Argument 1 Num	Byte	1		N/A	R/W	
402880	2879	0x0B3F	1	Table 1 - Condition 1 Argument 2 Sour	Byte	0	Drop List	N/A	R/W	
402881	2880	0x0B40	1	Table 1 - Condition 1 Argument 2 Num	Byte	1		N/A	R/W	
402882	2881	0x0B41	1	Table 1 - Condition 1 Operator	Byte	0 - =, Equal	Drop List	N/A	R/W	
402883	2882	0x0B42	1	Table 1 - Condition 2 Argument 1 Sour	Byte	0	Drop List	N/A	R/W	
402884	2883	0x0B43	1	Table 1 - Condition 2 Argument 1 Num	Byte	1		N/A	R/W	
402885	2884	0x0B44	1	Table 1 - Condition 2 Argument 2 Sour	Byte	0	Drop List	N/A	R/W	
402886	2885	0x0B45	1	Table 1 - Condition 2 Argument 2 Num	Byte	1		N/A	R/W	
402887	2886	0x0B46	1	Table 1 - Condition 2 Operator	Byte	0 - =, Equal	Drop List	N/A	R/W	
402888	2887	0x0B47	1	Table 1 - Condition 3 Argument 1 Sour	Byte	0	Drop List	N/A	R/W	
402889	2888	0x0B48	1	Table 1 - Condition 3 Argument 1 Num	Byte	1		N/A	R/W	
402890	2889	0x0B49	1	Table 1 - Condition 3 Argument 2 Sour	Byte	0	Drop List	N/A	R/W	
402891	2890	0x0B4A	1	Table 1 - Condition 3 Argument 2 Num	Byte	1		N/A	R/W	
402892	2891	0x0B4B	1	Table 1 - Condition 3 Operator	Byte	0 - =, Equal	Drop List	N/A	R/W	
402893	2892	0x0B4C	1	Table Number 2	Byte	1 - Lookup Tab	Drop List	N/A	R/W	
402894	2893	0x0B4D	1	Logical Operator 2	Byte	1 - Cnd1 & Cnd	Drop List	N/A	R/W	
402895	2894	0x0B4E	1	Table 2 - Condition 1 Argument 1 Sour	Byte	1 - CAN Receive	Drop List	N/A	R/W	
402896	2895	0x0B4F	1	Table 2 - Condition 1 Argument 1 Num	Byte	1		N/A	R/W	
402897	2896	0x0B50	1	Table 2 - Condition 1 Argument 2 Sour	Byte	3 - Constant Co	Drop List	N/A	R/W	
402898	2897	0x0B51	1	Table 2 - Condition 1 Argument 2 Num	Byte	1		N/A	R/W	
402899	2898	0x0B52	1	Table 2 - Condition 1 Operator	Byte	0 - =, Equal	Drop List	N/A	R/W	
402900	2899	0x0B53	1	Table 2 - Condition 2 Argument 1 Sour	Byte	0	Drop List	N/A	R/W	
402901	2900	0x0B54	1	Table 2 - Condition 2 Argument 1 Num	Byte	1		N/A	R/W	
402902	2901	0x0B55	1	Table 2 - Condition 2 Argument 2 Sour	Byte	0	Drop List	N/A	R/W	
402903	2902	0x0B56	1	Table 2 - Condition 2 Argument 2 Num	Byte	1		N/A	R/W	
402904	2903	0x0B57	1	Table 2 - Condition 2 Operator	Byte	0 - =, Equal	Drop List	N/A	R/W	
402905	2904	0x0B58	1	Table 2 - Condition 3 Argument 1 Sour	Byte	0	Drop List	N/A	R/W	
402906	2905	0x0B59	1	Table 2 - Condition 3 Argument 1 Num	Byte	1		N/A	R/W	
402907	2906	0x0B5A	1	Table 2 - Condition 3 Argument 2 Sour	Byte	0	Drop List	N/A	R/W	
402908	2907	0x0B5B	1	Table 2 - Condition 3 Argument 2 Num	Byte	1		N/A	R/W	
402909	2908	0x0B5C	1	Table 2 - Condition 3 Operator	Byte	0 - =, Equal	Drop List	N/A	R/W	
402910	2909	0x0B5D	1	Table Number 3	Byte	0	Drop List	N/A	R/W	
402911	2910	0x0B5E	1	Logical Operator 3	Byte	0	Drop List	N/A	R/W	
402912	2911	0x0B5F	1	Table 3 - Condition 1 Argument 1 Sour	Byte	0	Drop List	N/A	R/W	
402913	2912	0x0B60	1	Table 3 - Condition 1 Argument 1 Num	Byte	1		N/A	R/W	
402914	2913	0x0B61	1	Table 3 - Condition 1 Argument 2 Sour	Byte	0	Drop List	N/A	R/W	
402915	2914	0x0B62	1	Table 3 - Condition 1 Argument 2 Num	Byte	1		N/A	R/W	
402916	2915	0x0B63	1	Table 3 - Condition 1 Operator	Byte	0 - =, Equal	Drop List	N/A	R/W	
402917	2916	0x0B64	1	Table 3 - Condition 2 Argument 1 Sour	Byte	0	Drop List	N/A	R/W	
402918	2917	0x0B65	1	Table 3 - Condition 2 Argument 1 Num	Byte	1		N/A	R/W	
402919	2918	0x0B66	1	Table 3 - Condition 2 Argument 2 Sour	Byte	0	Drop List	N/A	R/W	
402920	2919	0x0B67	1	Table 3 - Condition 2 Argument 2 Num	Byte	1		N/A	R/W	
402921	2920	0x0B68	1	Table 3 - Condition 2 Operator	Byte	0 - =, Equal	Drop List	N/A	R/W	
402922	2921	0x0B69	1	Table 3 - Condition 3 Argument 1 Sour	Byte	0	Drop List	N/A	R/W	
402923	2922	0x0B6A	1	Table 3 - Condition 3 Argument 1 Num	Byte	1		N/A	R/W	
402924	2923	0x0B6B	1	Table 3 - Condition 3 Argument 2 Sour	Byte	0	Drop List	N/A	R/W	
402925	2924	0x0B6C	1	Table 3 - Condition 3 Argument 2 Num	Byte	1		N/A	R/W	

402926	2925	0x0B6D	1	Table 3 - Condition 3 Operator	Byte	0 =, Equal	Drop List	N/A	R/W	
402927	2926	0x0B6E	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Programmable Logic 3</b>										
402935	2934	0x0B76	1	Logic Enabled	Byte	0 - No	No / Yes	N/A	R/W	
402936	2935	0x0B77	1	Table Number 1	Byte	1 - Lookup Tab	Drop List	N/A	R/W	
402937	2936	0x0B78	1	Logical Operator 1	Byte	0	Drop List	N/A	R/W	
402938	2937	0x0B79	1	Table 1 - Condition 1 Argument 1 Sour	Byte	0	Drop List	N/A	R/W	
402939	2938	0x0B7A	1	Table 1 - Condition 1 Argument 1 Num	Byte	1		N/A	R/W	
402940	2939	0x0B7B	1	Table 1 - Condition 1 Argument 2 Sour	Byte	0	Drop List	N/A	R/W	
402941	2940	0x0B7C	1	Table 1 - Condition 1 Argument 2 Num	Byte	1		N/A	R/W	
402942	2941	0x0B7D	1	Table 1 - Condition 1 Operator	Byte	0 =, Equal	Drop List	N/A	R/W	
402943	2942	0x0B7E	1	Table 1 - Condition 2 Argument 1 Sour	Byte	0	Drop List	N/A	R/W	
402944	2943	0x0B7F	1	Table 1 - Condition 2 Argument 1 Num	Byte	1		N/A	R/W	
402945	2944	0x0B80	1	Table 1 - Condition 2 Argument 2 Sour	Byte	0	Drop List	N/A	R/W	
402946	2945	0x0B81	1	Table 1 - Condition 2 Argument 2 Num	Byte	1		N/A	R/W	
402947	2946	0x0B82	1	Table 1 - Condition 2 Operator	Byte	0 =, Equal	Drop List	N/A	R/W	
402948	2947	0x0B83	1	Table 1 - Condition 3 Argument 1 Sour	Byte	0	Drop List	N/A	R/W	
402949	2948	0x0B84	1	Table 1 - Condition 3 Argument 1 Num	Byte	1		N/A	R/W	
402950	2949	0x0B85	1	Table 1 - Condition 3 Argument 2 Sour	Byte	0	Drop List	N/A	R/W	
402951	2950	0x0B86	1	Table 1 - Condition 3 Argument 2 Num	Byte	1		N/A	R/W	
402952	2951	0x0B87	1	Table 1 - Condition 3 Operator	Byte	0 =, Equal	Drop List	N/A	R/W	
402953	2952	0x0B88	1	Table Number 2	Byte	1 - Lookup Tab	Drop List	N/A	R/W	
402954	2953	0x0B89	1	Logical Operator 2	Byte	1 - Cnd1 & Cnd	Drop List	N/A	R/W	
402955	2954	0x0B8A	1	Table 2 - Condition 1 Argument 1 Sour	Byte	1 - CAN Receve	Drop List	N/A	R/W	
402956	2955	0x0B8B	1	Table 2 - Condition 1 Argument 1 Num	Byte	1		N/A	R/W	
402957	2956	0x0B8C	1	Table 2 - Condition 1 Argument 2 Sour	Byte	3 - Constant Co	Drop List	N/A	R/W	
402958	2957	0x0B8D	1	Table 2 - Condition 1 Argument 2 Num	Byte	1		N/A	R/W	
402959	2958	0x0B8E	1	Table 2 - Condition 1 Operator	Byte	0 =, Equal	Drop List	N/A	R/W	
402960	2959	0x0B8F	1	Table 2 - Condition 2 Argument 1 Sour	Byte	0	Drop List	N/A	R/W	
402961	2960	0x0B90	1	Table 2 - Condition 2 Argument 1 Num	Byte	1		N/A	R/W	
402962	2961	0x0B91	1	Table 2 - Condition 2 Argument 2 Sour	Byte	0	Drop List	N/A	R/W	
402963	2962	0x0B92	1	Table 2 - Condition 2 Argument 2 Num	Byte	1		N/A	R/W	
402964	2963	0x0B93	1	Table 2 - Condition 2 Operator	Byte	0 =, Equal	Drop List	N/A	R/W	
402965	2964	0x0B94	1	Table 2 - Condition 3 Argument 1 Sour	Byte	0	Drop List	N/A	R/W	
402966	2965	0x0B95	1	Table 2 - Condition 3 Argument 1 Num	Byte	1		N/A	R/W	
402967	2966	0x0B96	1	Table 2 - Condition 3 Argument 2 Sour	Byte	0	Drop List	N/A	R/W	
402968	2967	0x0B97	1	Table 2 - Condition 3 Argument 2 Num	Byte	1		N/A	R/W	
402969	2968	0x0B98	1	Table 2 - Condition 3 Operator	Byte	0 =, Equal	Drop List	N/A	R/W	
402970	2969	0x0B99	1	Table Number 3	Byte	0	Drop List	N/A	R/W	
402971	2970	0x0B9A	1	Logical Operator 3	Byte	0	Drop List	N/A	R/W	
402972	2971	0x0B9B	1	Table 3 - Condition 1 Argument 1 Sour	Byte	0	Drop List	N/A	R/W	
402973	2972	0x0B9C	1	Table 3 - Condition 1 Argument 1 Num	Byte	1		N/A	R/W	
402974	2973	0x0B9D	1	Table 3 - Condition 1 Argument 2 Sour	Byte	0	Drop List	N/A	R/W	
402975	2974	0x0B9E	1	Table 3 - Condition 1 Argument 2 Num	Byte	1		N/A	R/W	
402976	2975	0x0B9F	1	Table 3 - Condition 1 Operator	Byte	0 =, Equal	Drop List	N/A	R/W	
402977	2976	0x0BA0	1	Table 3 - Condition 2 Argument 1 Sour	Byte	0	Drop List	N/A	R/W	
402978	2977	0x0BA1	1	Table 3 - Condition 2 Argument 1 Num	Byte	1		N/A	R/W	
402979	2978	0x0BA2	1	Table 3 - Condition 2 Argument 2 Sour	Byte	0	Drop List	N/A	R/W	
402980	2979	0x0BA3	1	Table 3 - Condition 2 Argument 2 Num	Byte	1		N/A	R/W	
402981	2980	0x0BA4	1	Table 3 - Condition 2 Operator	Byte	0 =, Equal	Drop List	N/A	R/W	
402982	2981	0x0BA5	1	Table 3 - Condition 3 Argument 1 Sour	Byte	0	Drop List	N/A	R/W	

402983	2982	0x0BA6	1	Table 3 - Condition 3 Argument 1 Num	Byte	1		N/A	R/W	
402984	2983	0x0BA7	1	Table 3 - Condition 3 Argument 2 Sour	Byte	0	Drop List	N/A	R/W	
402985	2984	0x0BA8	1	Table 3 - Condition 3 Argument 2 Num	Byte	1		N/A	R/W	
402986	2985	0x0BA9	1	Table 3 - Condition 3 Operator	Byte	0 =, Equal	Drop List	N/A	R/W	
402987	2986	0x0BAA	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Programmable Logic 4</b>										
402995	2994	0x0BB2	1	Logic Enabled	Byte	0 - No	No / Yes	N/A	R/W	
402996	2995	0x0BB3	1	Table Number 1	Byte	1 - Lookup Tab	Drop List	N/A	R/W	
402997	2996	0x0BB4	1	Logical Operator 1	Byte	0	Drop List	N/A	R/W	
402998	2997	0x0BB5	1	Table 1 - Condition 1 Argument 1 Sour	Byte	0	Drop List	N/A	R/W	
402999	2998	0x0BB6	1	Table 1 - Condition 1 Argument 1 Num	Byte	1		N/A	R/W	
403000	2999	0x0BB7	1	Table 1 - Condition 1 Argument 2 Sour	Byte	0	Drop List	N/A	R/W	
403001	3000	0x0BB8	1	Table 1 - Condition 1 Argument 2 Num	Byte	1		N/A	R/W	
403002	3001	0x0BB9	1	Table 1 - Condition 1 Operator	Byte	0 =, Equal	Drop List	N/A	R/W	
403003	3002	0x0BBA	1	Table 1 - Condition 2 Argument 1 Sour	Byte	0	Drop List	N/A	R/W	
403004	3003	0x0BBB	1	Table 1 - Condition 2 Argument 1 Num	Byte	1		N/A	R/W	
403005	3004	0x0BBC	1	Table 1 - Condition 2 Argument 2 Sour	Byte	0	Drop List	N/A	R/W	
403006	3005	0x0BBD	1	Table 1 - Condition 2 Argument 2 Num	Byte	1		N/A	R/W	
403007	3006	0x0BBE	1	Table 1 - Condition 2 Operator	Byte	0 =, Equal	Drop List	N/A	R/W	
403008	3007	0x0BBF	1	Table 1 - Condition 3 Argument 1 Sour	Byte	0	Drop List	N/A	R/W	
403009	3008	0x0BC0	1	Table 1 - Condition 3 Argument 1 Num	Byte	1		N/A	R/W	
403010	3009	0x0BC1	1	Table 1 - Condition 3 Argument 2 Sour	Byte	0	Drop List	N/A	R/W	
403011	3010	0x0BC2	1	Table 1 - Condition 3 Argument 2 Num	Byte	1		N/A	R/W	
403012	3011	0x0BC3	1	Table 1 - Condition 3 Operator	Byte	0 =, Equal	Drop List	N/A	R/W	
403013	3012	0x0BC4	1	Table Number 2	Byte	1 - Lookup Tab	Drop List	N/A	R/W	
403014	3013	0x0BC5	1	Logical Operator 2	Byte	1 - Cnd1 & Cnd	Drop List	N/A	R/W	
403015	3014	0x0BC6	1	Table 2 - Condition 1 Argument 1 Sour	Byte	1 - CAN Receive	Drop List	N/A	R/W	
403016	3015	0x0BC7	1	Table 2 - Condition 1 Argument 1 Num	Byte	1		N/A	R/W	
403017	3016	0x0BC8	1	Table 2 - Condition 1 Argument 2 Sour	Byte	3 - Constant Co	Drop List	N/A	R/W	
403018	3017	0x0BC9	1	Table 2 - Condition 1 Argument 2 Num	Byte	1		N/A	R/W	
403019	3018	0x0BCA	1	Table 2 - Condition 1 Operator	Byte	0 =, Equal	Drop List	N/A	R/W	
403020	3019	0x0BCB	1	Table 2 - Condition 2 Argument 1 Sour	Byte	0	Drop List	N/A	R/W	
403021	3020	0x0BCC	1	Table 2 - Condition 2 Argument 1 Num	Byte	1		N/A	R/W	
403022	3021	0x0BCD	1	Table 2 - Condition 2 Argument 2 Sour	Byte	0	Drop List	N/A	R/W	
403023	3022	0x0BCE	1	Table 2 - Condition 2 Argument 2 Num	Byte	1		N/A	R/W	
403024	3023	0x0BCF	1	Table 2 - Condition 2 Operator	Byte	0 =, Equal	Drop List	N/A	R/W	
403025	3024	0x0BD0	1	Table 2 - Condition 3 Argument 1 Sour	Byte	0	Drop List	N/A	R/W	
403026	3025	0x0BD1	1	Table 2 - Condition 3 Argument 1 Num	Byte	1		N/A	R/W	
403027	3026	0x0BD2	1	Table 2 - Condition 3 Argument 2 Sour	Byte	0	Drop List	N/A	R/W	
403028	3027	0x0BD3	1	Table 2 - Condition 3 Argument 2 Num	Byte	1		N/A	R/W	
403029	3028	0x0BD4	1	Table 2 - Condition 3 Operator	Byte	0 =, Equal	Drop List	N/A	R/W	
403030	3029	0x0BD5	1	Table Number 3	Byte	0	Drop List	N/A	R/W	
403031	3030	0x0BD6	1	Logical Operator 3	Byte	0	Drop List	N/A	R/W	
403032	3031	0x0BD7	1	Table 3 - Condition 1 Argument 1 Sour	Byte	0	Drop List	N/A	R/W	
403033	3032	0x0BD8	1	Table 3 - Condition 1 Argument 1 Num	Byte	1		N/A	R/W	
403034	3033	0x0BD9	1	Table 3 - Condition 1 Argument 2 Sour	Byte	0	Drop List	N/A	R/W	
403035	3034	0x0BDA	1	Table 3 - Condition 1 Argument 2 Num	Byte	1		N/A	R/W	
403036	3035	0x0BDB	1	Table 3 - Condition 1 Operator	Byte	0 =, Equal	Drop List	N/A	R/W	
403037	3036	0x0BDC	1	Table 3 - Condition 2 Argument 1 Sour	Byte	0	Drop List	N/A	R/W	
403038	3037	0x0BDD	1	Table 3 - Condition 2 Argument 1 Num	Byte	1		N/A	R/W	
403039	3038	0x0BDE	1	Table 3 - Condition 2 Argument 2 Sour	Byte	0	Drop List	N/A	R/W	

403040	3039	0x0BDF	1	Table 3 - Condition 2 Argument 2 Num	Byte	1		N/A	R/W	
403041	3040	0x0BE0	1	Table 3 - Condition 2 Operator	Byte	0 = =, Equal	Drop List	N/A	R/W	
403042	3041	0x0BE1	1	Table 3 - Condition 3 Argument 1 Sour	Byte	0	Drop List	N/A	R/W	
403043	3042	0x0BE2	1	Table 3 - Condition 3 Argument 1 Num	Byte	1		N/A	R/W	
403044	3043	0x0BE3	1	Table 3 - Condition 3 Argument 2 Sour	Byte	0	Drop List	N/A	R/W	
403045	3044	0x0BE4	1	Table 3 - Condition 3 Argument 2 Num	Byte	1		N/A	R/W	
403046	3045	0x0BE5	1	Table 3 - Condition 3 Operator	Byte	0 = =, Equal	Drop List	N/A	R/W	
403047	3046	0x0BE6	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Math Function Block 1</b>										
403055	3054	0x0BEE	1	Math Enabled	Byte	0 - No	No / Yes	N/A	R/W	
403056	3055	0x0BEF	2	Math Output Minimum Range	Float	0	-32768...32767	N/A	R/W	
403058	3057	0x0BF1	2	Math Output Maximum Range	Float	100	-32768...32767	N/A	R/W	
403060	3059	0x0BF3	1	Input 1 Source	Byte	0 - Control Not	0...12	N/A	R/W	
403061	3060	0x0BF4	1	Input 1 Number	Byte	1	Depends on control source	N/A	R/W	
403062	3061	0x0BF5	2	Input 1 Minimum	Float	0	-10 <sup>6</sup> ...10 <sup>6</sup>	N/A	R/W	
403064	3063	0x0BF7	2	Input 1 Maximum	Float	10000	-10 <sup>6</sup> ...10 <sup>6</sup>	N/A	R/W	
403066	3065	0x0BF9	2	Input 1 Gain	Float	100	-100...100	N/A	R/W	
403068	3067	0x0BFB	1	Input 2 Source	Byte	0 - Control Not	0...12	N/A	R/W	
403069	3068	0x0BFC	1	Input 2 Number	Byte	1	Depends on control source	N/A	R/W	
403070	3069	0x0BFD	2	Input 2 Minimum	Float	0	-10 <sup>6</sup> ...10 <sup>6</sup>	N/A	R/W	
403072	3071	0x0BFF	2	Input 2 Maximum	Float	10000	-10 <sup>6</sup> ...10 <sup>6</sup>	N/A	R/W	
403074	3073	0x0C01	2	Input 2 Gain	Float	100	-100...100	N/A	R/W	
403076	3075	0x0C03	1	Input 3 Source	Byte	0 - Control Not	0...12	N/A	R/W	
403077	3076	0x0C04	1	Input 3 Number	Byte	1	Depends on control source	N/A	R/W	
403078	3077	0x0C05	2	Input 3 Minimum	Float	0	-10 <sup>6</sup> ...10 <sup>6</sup>	N/A	R/W	
403080	3079	0x0C07	2	Input 3 Maximum	Float	10000	-10 <sup>6</sup> ...10 <sup>6</sup>	N/A	R/W	
403082	3081	0x0C09	2	Input 3 Gain	Float	100	-100...100	N/A	R/W	
403084	3083	0x0C0B	1	Input 4 Source	Byte	0 - Control Not	0...12	N/A	R/W	
403085	3084	0x0C0C	1	Input 4 Number	Byte	1	Depends on control source	N/A	R/W	
403086	3085	0x0C0D	2	Input 4 Minimum	Float	0	-10 <sup>6</sup> ...10 <sup>6</sup>	N/A	R/W	
403088	3087	0x0C0F	2	Input 4 Maximum	Float	10000	-10 <sup>6</sup> ...10 <sup>6</sup>	N/A	R/W	
403090	3089	0x0C11	2	Input 4 Gain	Float	100	-100...100	N/A	R/W	
403092	3091	0x0C13	1	Math Function 1	Byte	0	0..14	N/A	R/W	
403093	3092	0x0C14	1	Math Function 2	Byte	0	0..14	N/A	R/W	
403094	3093	0x0C15	1	Math Function 3	Byte	0	0..14	N/A	R/W	
403095	3094	0x0C16	5	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Math Function Block 2</b>										
403100	3099	0x0C1B	1	Math Enabled	Byte	0 - No	No / Yes	N/A	R/W	
403101	3100	0x0C1C	2	Math Output Minimum Range	Float	0	-32768...32767	N/A	R/W	
403103	3102	0x0C1E	2	Math Output Maximum Range	Float	100	-32768...32767	N/A	R/W	
403105	3104	0x0C20	1	Input 1 Source	Byte	0 - Control Not	0...12	N/A	R/W	
403106	3105	0x0C21	1	Input 1 Number	Byte	1	Depends on control source	N/A	R/W	
403107	3106	0x0C22	2	Input 1 Minimum	Float	0	-10 <sup>6</sup> ...10 <sup>6</sup>	N/A	R/W	
403109	3108	0x0C24	2	Input 1 Maximum	Float	10000	-10 <sup>6</sup> ...10 <sup>6</sup>	N/A	R/W	
403111	3110	0x0C26	2	Input 1 Gain	Float	100	-100...100	N/A	R/W	
403113	3112	0x0C28	1	Input 2 Source	Byte	0 - Control Not	0...12	N/A	R/W	
403114	3113	0x0C29	1	Input 2 Number	Byte	1	Depends on control source	N/A	R/W	
403115	3114	0x0C2A	2	Input 2 Minimum	Float	0	-10 <sup>6</sup> ...10 <sup>6</sup>	N/A	R/W	
403117	3116	0x0C2C	2	Input 2 Maximum	Float	10000	-10 <sup>6</sup> ...10 <sup>6</sup>	N/A	R/W	
403119	3118	0x0C2E	2	Input 2 Gain	Float	100	-100...100	N/A	R/W	

403121	3120	0x0C30	1	Input 3 Source	Byte	0 - Control Not	0..12	N/A	R/W	
403122	3121	0x0C31	1	Input 3 Number	Byte	1	Depends on control source	N/A	R/W	
403123	3122	0x0C32	2	Input 3 Minimum	Float	0	-10 <sup>6</sup> ...10 <sup>6</sup>	N/A	R/W	
403125	3124	0x0C34	2	Input 3 Maximum	Float	10000	-10 <sup>6</sup> ...10 <sup>6</sup>	N/A	R/W	
403127	3126	0x0C36	2	Input 3 Gain	Float	100	-100...100	N/A	R/W	
403129	3128	0x0C38	1	Input 4 Source	Byte	0 - Control Not	0..12	N/A	R/W	
403130	3129	0x0C39	1	Input 4 Number	Byte	1	Depends on control source	N/A	R/W	
403131	3130	0x0C3A	2	Input 4 Minimum	Float	0	-10 <sup>6</sup> ...10 <sup>6</sup>	N/A	R/W	
403133	3132	0x0C3C	2	Input 4 Maximum	Float	10000	-10 <sup>6</sup> ...10 <sup>6</sup>	N/A	R/W	
403135	3134	0x0C3E	2	Input 4 Gain	Float	100	-100...100	N/A	R/W	
403137	3136	0x0C40	1	Math Function 1	Byte	0	0..14	N/A	R/W	
403138	3137	0x0C41	1	Math Function 2	Byte	0	0..14	N/A	R/W	
403139	3138	0x0C42	1	Math Function 3	Byte	0	0..14	N/A	R/W	
403140	3139	0x0C43	5	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Math Function Block 3</b>										
403145	3144	0x0C48	1	Math Enabled	Byte	0 - No	No / Yes	N/A	R/W	
403146	3145	0x0C49	2	Math Output Minimum Range	Float	0	-32768...32767	N/A	R/W	
403148	3147	0x0C4B	2	Math Output Maximum Range	Float	100	-32768...32767	N/A	R/W	
403150	3149	0x0C4D	1	Input 1 Source	Byte	0 - Control Not	0..12	N/A	R/W	
403151	3150	0x0C4E	1	Input 1 Number	Byte	1	Depends on control source	N/A	R/W	
403152	3151	0x0C4F	2	Input 1 Minimum	Float	0	-10 <sup>6</sup> ...10 <sup>6</sup>	N/A	R/W	
403154	3153	0x0C51	2	Input 1 Maximum	Float	10000	-10 <sup>6</sup> ...10 <sup>6</sup>	N/A	R/W	
403156	3155	0x0C53	2	Input 1 Gain	Float	100	-100...100	N/A	R/W	
403158	3157	0x0C55	1	Input 2 Source	Byte	0 - Control Not	0..12	N/A	R/W	
403159	3158	0x0C56	1	Input 2 Number	Byte	1	Depends on control source	N/A	R/W	
403160	3159	0x0C57	2	Input 2 Minimum	Float	0	-10 <sup>6</sup> ...10 <sup>6</sup>	N/A	R/W	
403162	3161	0x0C59	2	Input 2 Maximum	Float	10000	-10 <sup>6</sup> ...10 <sup>6</sup>	N/A	R/W	
403164	3163	0x0C5B	2	Input 2 Gain	Float	100	-100...100	N/A	R/W	
403166	3165	0x0C5D	1	Input 3 Source	Byte	0 - Control Not	0..12	N/A	R/W	
403167	3166	0x0C5E	1	Input 3 Number	Byte	1	Depends on control source	N/A	R/W	
403168	3167	0x0C5F	2	Input 3 Minimum	Float	0	-10 <sup>6</sup> ...10 <sup>6</sup>	N/A	R/W	
403170	3169	0x0C61	2	Input 3 Maximum	Float	10000	-10 <sup>6</sup> ...10 <sup>6</sup>	N/A	R/W	
403172	3171	0x0C63	2	Input 3 Gain	Float	100	-100...100	N/A	R/W	
403174	3173	0x0C65	1	Input 4 Source	Byte	0 - Control Not	0..12	N/A	R/W	
403175	3174	0x0C66	1	Input 4 Number	Byte	1	Depends on control source	N/A	R/W	
403176	3175	0x0C67	2	Input 4 Minimum	Float	0	-10 <sup>6</sup> ...10 <sup>6</sup>	N/A	R/W	
403178	3177	0x0C69	2	Input 4 Maximum	Float	10000	-10 <sup>6</sup> ...10 <sup>6</sup>	N/A	R/W	
403180	3179	0x0C6B	2	Input 4 Gain	Float	100	-100...100	N/A	R/W	
403182	3181	0x0C6D	1	Math Function 1	Byte	0	0..14	N/A	R/W	
403183	3182	0x0C6E	1	Math Function 2	Byte	0	0..14	N/A	R/W	
403184	3183	0x0C6F	1	Math Function 3	Byte	0	0..14	N/A	R/W	
403185	3184	0x0C70	5	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Math Function Block 4</b>										
403190	3189	0x0C75	1	Math Enabled	Byte	0 - No	No / Yes	N/A	R/W	
403191	3190	0x0C76	2	Math Output Minimum Range	Float	0	-32768...32767	N/A	R/W	
403193	3192	0x0C78	2	Math Output Maximum Range	Float	100	-32768...32767	N/A	R/W	
403195	3194	0x0C7A	1	Input 1 Source	Byte	0 - Control Not	0..12	N/A	R/W	
403196	3195	0x0C7B	1	Input 1 Number	Byte	1	Depends on control source	N/A	R/W	
403197	3196	0x0C7C	2	Input 1 Minimum	Float	0	-10 <sup>6</sup> ...10 <sup>6</sup>	N/A	R/W	
403199	3198	0x0C7E	2	Input 1 Maximum	Float	10000	-10 <sup>6</sup> ...10 <sup>6</sup>	N/A	R/W	



403201	3200	0x0C80	2	Input 1 Gain	Float	100	-100...100	N/A	R/W	
403203	3202	0x0C82	1	Input 2 Source	Byte	0 - Control Not	0...12	N/A	R/W	
403204	3203	0x0C83	1	Input 2 Number	Byte	1	Depends on control source	N/A	R/W	
403205	3204	0x0C84	2	Input 2 Minimum	Float	0	-10 <sup>6</sup> ...10 <sup>6</sup>	N/A	R/W	
403207	3206	0x0C86	2	Input 2 Maximum	Float	10000	-10 <sup>6</sup> ...10 <sup>6</sup>	N/A	R/W	
403209	3208	0x0C88	2	Input 2 Gain	Float	100	-100...100	N/A	R/W	
403211	3210	0x0C8A	1	Input 3 Source	Byte	0 - Control Not	0...12	N/A	R/W	
403212	3211	0x0C8B	1	Input 3 Number	Byte	1	Depends on control source	N/A	R/W	
403213	3212	0x0C8C	2	Input 3 Minimum	Float	0	-10 <sup>6</sup> ...10 <sup>6</sup>	N/A	R/W	
403215	3214	0x0C8E	2	Input 3 Maximum	Float	10000	-10 <sup>6</sup> ...10 <sup>6</sup>	N/A	R/W	
403217	3216	0x0C90	2	Input 3 Gain	Float	100	-100...100	N/A	R/W	
403219	3218	0x0C92	1	Input 4 Source	Byte	0 - Control Not	0...12	N/A	R/W	
403220	3219	0x0C93	1	Input 4 Number	Byte	1	Depends on control source	N/A	R/W	
403221	3220	0x0C94	2	Input 4 Minimum	Float	0	-10 <sup>6</sup> ...10 <sup>6</sup>	N/A	R/W	
403223	3222	0x0C96	2	Input 4 Maximum	Float	10000	-10 <sup>6</sup> ...10 <sup>6</sup>	N/A	R/W	
403225	3224	0x0C98	2	Input 4 Gain	Float	100	-100...100	N/A	R/W	
403227	3226	0x0C9A	1	Math Function 1	Byte	0	0..14	N/A	R/W	
403228	3227	0x0C9B	1	Math Function 2	Byte	0	0..14	N/A	R/W	
403229	3228	0x0C9C	1	Math Function 3	Byte	0	0..14	N/A	R/W	
403230	3229	0x0C9D	5	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Math Function Block 5</b>										
403235	3234	0x0CA2	1	Math Enabled	Byte	0 - No	No / Yes	N/A	R/W	
403236	3235	0x0CA3	2	Math Output Minimum Range	Float	0	-32768...32767	N/A	R/W	
403238	3237	0x0CA5	2	Math Output Maximum Range	Float	100	-32768...32767	N/A	R/W	
403240	3239	0x0CA7	1	Input 1 Source	Byte	0 - Control Not	0...12	N/A	R/W	
403241	3240	0x0CA8	1	Input 1 Number	Byte	1	Depends on control source	N/A	R/W	
403242	3241	0x0CA9	2	Input 1 Minimum	Float	0	-10 <sup>6</sup> ...10 <sup>6</sup>	N/A	R/W	
403244	3243	0x0CAB	2	Input 1 Maximum	Float	10000	-10 <sup>6</sup> ...10 <sup>6</sup>	N/A	R/W	
403246	3245	0x0CAD	2	Input 1 Gain	Float	100	-100...100	N/A	R/W	
403248	3247	0x0CAF	1	Input 2 Source	Byte	0 - Control Not	0...12	N/A	R/W	
403249	3248	0x0CB0	1	Input 2 Number	Byte	1	Depends on control source	N/A	R/W	
403250	3249	0x0CB1	2	Input 2 Minimum	Float	0	-10 <sup>6</sup> ...10 <sup>6</sup>	N/A	R/W	
403252	3251	0x0CB3	2	Input 2 Maximum	Float	10000	-10 <sup>6</sup> ...10 <sup>6</sup>	N/A	R/W	
403254	3253	0x0CB5	2	Input 2 Gain	Float	100	-100...100	N/A	R/W	
403256	3255	0x0CB7	1	Input 3 Source	Byte	0 - Control Not	0...12	N/A	R/W	
403257	3256	0x0CB8	1	Input 3 Number	Byte	1	Depends on control source	N/A	R/W	
403258	3257	0x0CB9	2	Input 3 Minimum	Float	0	-10 <sup>6</sup> ...10 <sup>6</sup>	N/A	R/W	
403260	3259	0x0CBB	2	Input 3 Maximum	Float	10000	-10 <sup>6</sup> ...10 <sup>6</sup>	N/A	R/W	
403262	3261	0x0CBD	2	Input 3 Gain	Float	100	-100...100	N/A	R/W	
403264	3263	0x0CBF	1	Input 4 Source	Byte	0 - Control Not	0...12	N/A	R/W	
403265	3264	0x0CC0	1	Input 4 Number	Byte	1	Depends on control source	N/A	R/W	
403266	3265	0x0CC1	2	Input 4 Minimum	Float	0	-10 <sup>6</sup> ...10 <sup>6</sup>	N/A	R/W	
403268	3267	0x0CC3	2	Input 4 Maximum	Float	10000	-10 <sup>6</sup> ...10 <sup>6</sup>	N/A	R/W	
403270	3269	0x0CC5	2	Input 4 Gain	Float	100	-100...100	N/A	R/W	
403272	3271	0x0CC7	1	Math Function 1	Byte	0	0..14	N/A	R/W	
403273	3272	0x0CC8	1	Math Function 2	Byte	0	0..14	N/A	R/W	
403274	3273	0x0CC9	1	Math Function 3	Byte	0	0..14	N/A	R/W	
403275	3274	0x0CCA	5	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Conditional Logic Block 1</b>										
403280	3279	0x0CCF	1	Conditional Block Enabled	Byte	0 - Disabled	Disabled/Enabled	N/A	R/W	

403281	3280	0x0CD0	1	Condition 1 Argument 1 Source	Byte	0 - Control Not	0...17	N/A	R/W	
403282	3281	0x0CD1	1	Condition 1 Argument 1 Number	Byte	1	Depends on control source	N/A	R/W	
403283	3282	0x0CD2	1	Condition 1 Argument 2 Source	Byte	0 - Control Not	0...17	N/A	R/W	
403284	3283	0x0CD3	1	Condition 1 Argument 2 Number	Byte	1	Depends on control source	N/A	R/W	
403285	3284	0x0CD4	1	Condition 1 Operator	Byte	0 - ==	0..9	N/A	R/W	
403286	3285	0x0CD5	1	Condition 2 Argument 1 Source	Byte	0 - Control Not	0...17	N/A	R/W	
403287	3286	0x0CD6	1	Condition 2 Argument 1 Number	Byte	1	Depends on control source	N/A	R/W	
403288	3287	0x0CD7	1	Condition 2 Argument 2 Source	Byte	0 - Control Not	0...17	N/A	R/W	
403289	3288	0x0CD8	1	Condition 2 Argument 2 Number	Byte	1	Depends on control source	N/A	R/W	
403290	3289	0x0CD9	1	Condition 2 Operator	Byte	0 - ==	0..9	N/A	R/W	
403291	3290	0x0CDA	1	Conditional Result Operator	Byte	0 - OR	0..2	N/A	R/W	
403292	3291	0x0CDB	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Conditional Logic Block 2</b>										
403300	3299	0x0CE3	1	Conditional Block Enabled	Byte	0 - Disabled	Disabled/Enabled	N/A	R/W	
403301	3300	0x0CE4	1	Condition 1 Argument 1 Source	Byte	0 - Control Not	0...17	N/A	R/W	
403302	3301	0x0CE5	1	Condition 1 Argument 1 Number	Byte	1	Depends on control source	N/A	R/W	
403303	3302	0x0CE6	1	Condition 1 Argument 2 Source	Byte	0 - Control Not	0...17	N/A	R/W	
403304	3303	0x0CE7	1	Condition 1 Argument 2 Number	Byte	1	Depends on control source	N/A	R/W	
403305	3304	0x0CE8	1	Condition 1 Operator	Byte	0 - ==	0..9	N/A	R/W	
403306	3305	0x0CE9	1	Condition 2 Argument 1 Source	Byte	0 - Control Not	0...17	N/A	R/W	
403307	3306	0x0CEA	1	Condition 2 Argument 1 Number	Byte	1	Depends on control source	N/A	R/W	
403308	3307	0x0CEB	1	Condition 2 Argument 2 Source	Byte	0 - Control Not	0...17	N/A	R/W	
403309	3308	0x0CEC	1	Condition 2 Argument 2 Number	Byte	1	Depends on control source	N/A	R/W	
403310	3309	0x0CED	1	Condition 2 Operator	Byte	0 - ==	0..9	N/A	R/W	
403311	3310	0x0CEE	1	Conditional Result Operator	Byte	0 - OR	0..2	N/A	R/W	
403312	3311	0x0CEF	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Conditional Logic Block 3</b>										
403320	3319	0x0CF7	1	Conditional Block Enabled	Byte	0 - Disabled	Disabled/Enabled	N/A	R/W	
403321	3320	0x0CF8	1	Condition 1 Argument 1 Source	Byte	0 - Control Not	0...17	N/A	R/W	
403322	3321	0x0CF9	1	Condition 1 Argument 1 Number	Byte	1	Depends on control source	N/A	R/W	
403323	3322	0x0CFA	1	Condition 1 Argument 2 Source	Byte	0 - Control Not	0...17	N/A	R/W	
403324	3323	0x0CFB	1	Condition 1 Argument 2 Number	Byte	1	Depends on control source	N/A	R/W	
403325	3324	0x0CFC	1	Condition 1 Operator	Byte	0 - ==	0..9	N/A	R/W	
403326	3325	0x0CFD	1	Condition 2 Argument 1 Source	Byte	0 - Control Not	0...17	N/A	R/W	
403327	3326	0x0CFE	1	Condition 2 Argument 1 Number	Byte	1	Depends on control source	N/A	R/W	
403328	3327	0x0CFF	1	Condition 2 Argument 2 Source	Byte	0 - Control Not	0...17	N/A	R/W	
403329	3328	0x0D00	1	Condition 2 Argument 2 Number	Byte	1	Depends on control source	N/A	R/W	
403330	3329	0x0D01	1	Condition 2 Operator	Byte	0 - ==	0..9	N/A	R/W	
403331	3330	0x0D02	1	Conditional Result Operator	Byte	0 - OR	0..2	N/A	R/W	
403332	3331	0x0D03	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Conditional Logic Block 4</b>										
403340	3339	0x0D0B	1	Conditional Block Enabled	Byte	0 - Disabled	Disabled/Enabled	N/A	R/W	
403341	3340	0x0D0C	1	Condition 1 Argument 1 Source	Byte	0 - Control Not	0...17	N/A	R/W	
403342	3341	0x0D0D	1	Condition 1 Argument 1 Number	Byte	1	Depends on control source	N/A	R/W	
403343	3342	0x0D0E	1	Condition 1 Argument 2 Source	Byte	0 - Control Not	0...17	N/A	R/W	
403344	3343	0x0D0F	1	Condition 1 Argument 2 Number	Byte	1	Depends on control source	N/A	R/W	
403345	3344	0x0D10	1	Condition 1 Operator	Byte	0 - ==	0..9	N/A	R/W	
403346	3345	0x0D11	1	Condition 2 Argument 1 Source	Byte	0 - Control Not	0...17	N/A	R/W	
403347	3346	0x0D12	1	Condition 2 Argument 1 Number	Byte	1	Depends on control source	N/A	R/W	
403348	3347	0x0D13	1	Condition 2 Argument 2 Source	Byte	0 - Control Not	0...17	N/A	R/W	
403349	3348	0x0D14	1	Condition 2 Argument 2 Number	Byte	1	Depends on control source	N/A	R/W	

403350	3349	0x0D15	1	Condition 2 Operator	Byte	0 - ==	0...9	N/A	R/W	
403351	3350	0x0D16	1	Conditional Result Operator	Byte	0 - OR	0..2	N/A	R/W	
403352	3351	0x0D17	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Conditional Logic Block 5</b>										
403360	3359	0x0D1F	1	Conditional Block Enabled	Byte	0 - Disabled	Disabled/Enabled	N/A	R/W	
403361	3360	0x0D20	1	Condition 1 Argument 1 Source	Byte	0 - Control Not	0...17	N/A	R/W	
403362	3361	0x0D21	1	Condition 1 Argument 1 Number	Byte	1	Depends on control source	N/A	R/W	
403363	3362	0x0D22	1	Condition 1 Argument 2 Source	Byte	0 - Control Not	0...17	N/A	R/W	
403364	3363	0x0D23	1	Condition 1 Argument 2 Number	Byte	1	Depends on control source	N/A	R/W	
403365	3364	0x0D24	1	Condition 1 Operator	Byte	0 - ==	0..9	N/A	R/W	
403366	3365	0x0D25	1	Condition 2 Argument 1 Source	Byte	0 - Control Not	0...17	N/A	R/W	
403367	3366	0x0D26	1	Condition 2 Argument 1 Number	Byte	1	Depends on control source	N/A	R/W	
403368	3367	0x0D27	1	Condition 2 Argument 2 Source	Byte	0 - Control Not	0...17	N/A	R/W	
403369	3368	0x0D28	1	Condition 2 Argument 2 Number	Byte	1	Depends on control source	N/A	R/W	
403370	3369	0x0D29	1	Condition 2 Operator	Byte	0 - ==	0..9	N/A	R/W	
403371	3370	0x0D2A	1	Conditional Result Operator	Byte	0 - OR	0..2	N/A	R/W	
403372	3371	0x0D2B	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Conditional Logic Block 6</b>										
403380	3379	0x0D33	1	Conditional Block Enabled	Byte	0 - Disabled	Disabled/Enabled	N/A	R/W	
403381	3380	0x0D34	1	Condition 1 Argument 1 Source	Byte	0 - Control Not	0...17	N/A	R/W	
403382	3381	0x0D35	1	Condition 1 Argument 1 Number	Byte	1	Depends on control source	N/A	R/W	
403383	3382	0x0D36	1	Condition 1 Argument 2 Source	Byte	0 - Control Not	0...17	N/A	R/W	
403384	3383	0x0D37	1	Condition 1 Argument 2 Number	Byte	1	Depends on control source	N/A	R/W	
403385	3384	0x0D38	1	Condition 1 Operator	Byte	0 - ==	0..9	N/A	R/W	
403386	3385	0x0D39	1	Condition 2 Argument 1 Source	Byte	0 - Control Not	0...17	N/A	R/W	
403387	3386	0x0D3A	1	Condition 2 Argument 1 Number	Byte	1	Depends on control source	N/A	R/W	
403388	3387	0x0D3B	1	Condition 2 Argument 2 Source	Byte	0 - Control Not	0...17	N/A	R/W	
403389	3388	0x0D3C	1	Condition 2 Argument 2 Number	Byte	1	Depends on control source	N/A	R/W	
403390	3389	0x0D3D	1	Condition 2 Operator	Byte	0 - ==	0..9	N/A	R/W	
403391	3390	0x0D3E	1	Conditional Result Operator	Byte	0 - OR	0..2	N/A	R/W	
403392	3391	0x0D3F	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Conditional Logic Block 7</b>										
403400	3399	0x0D47	1	Conditional Block Enabled	Byte	0 - Disabled	Disabled/Enabled	N/A	R/W	
403401	3400	0x0D48	1	Condition 1 Argument 1 Source	Byte	0 - Control Not	0...17	N/A	R/W	
403402	3401	0x0D49	1	Condition 1 Argument 1 Number	Byte	1	Depends on control source	N/A	R/W	
403403	3402	0x0D4A	1	Condition 1 Argument 2 Source	Byte	0 - Control Not	0...17	N/A	R/W	
403404	3403	0x0D4B	1	Condition 1 Argument 2 Number	Byte	1	Depends on control source	N/A	R/W	
403405	3404	0x0D4C	1	Condition 1 Operator	Byte	0 - ==	0..9	N/A	R/W	
403406	3405	0x0D4D	1	Condition 2 Argument 1 Source	Byte	0 - Control Not	0...17	N/A	R/W	
403407	3406	0x0D4E	1	Condition 2 Argument 1 Number	Byte	1	Depends on control source	N/A	R/W	
403408	3407	0x0D4F	1	Condition 2 Argument 2 Source	Byte	0 - Control Not	0...17	N/A	R/W	
403409	3408	0x0D50	1	Condition 2 Argument 2 Number	Byte	1	Depends on control source	N/A	R/W	
403410	3409	0x0D51	1	Condition 2 Operator	Byte	0 - ==	0..9	N/A	R/W	
403411	3410	0x0D52	1	Conditional Result Operator	Byte	0 - OR	0..2	N/A	R/W	
403412	3411	0x0D53	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Conditional Logic Block 8</b>										
403420	3419	0x0D5B	1	Conditional Block Enabled	Byte	0 - Disabled	Disabled/Enabled	N/A	R/W	
403421	3420	0x0D5C	1	Condition 1 Argument 1 Source	Byte	0 - Control Not	0...17	N/A	R/W	
403422	3421	0x0D5D	1	Condition 1 Argument 1 Number	Byte	1	Depends on control source	N/A	R/W	
403423	3422	0x0D5E	1	Condition 1 Argument 2 Source	Byte	0 - Control Not	0...17	N/A	R/W	
403424	3423	0x0D5F	1	Condition 1 Argument 2 Number	Byte	1	Depends on control source	N/A	R/W	

403425	3424	0x0D60	1	Condition 1 Operator	Byte	0 - ==	0...9	N/A	R/W	
403426	3425	0x0D61	1	Condition 2 Argument 1 Source	Byte	0 - Control Not	0...17	N/A	R/W	
403427	3426	0x0D62	1	Condition 2 Argument 1 Number	Byte	1	Depends on control source	N/A	R/W	
403428	3427	0x0D63	1	Condition 2 Argument 2 Source	Byte	0 - Control Not	0...17	N/A	R/W	
403429	3428	0x0D64	1	Condition 2 Argument 2 Number	Byte	1	Depends on control source	N/A	R/W	
403430	3429	0x0D65	1	Condition 2 Operator	Byte	0 - ==	0...9	N/A	R/W	
403431	3430	0x0D66	1	Conditional Result Operator	Byte	0 - OR	0...2	N/A	R/W	
403432	3431	0x0D67	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Conditional Logic Block 9</b>										
403440	3439	0x0D6F	1	Conditional Block Enabled	Byte	0 - Disabled	Disabled/Enabled	N/A	R/W	
403441	3440	0x0D70	1	Condition 1 Argument 1 Source	Byte	0 - Control Not	0...17	N/A	R/W	
403442	3441	0x0D71	1	Condition 1 Argument 1 Number	Byte	1	Depends on control source	N/A	R/W	
403443	3442	0x0D72	1	Condition 1 Argument 2 Source	Byte	0 - Control Not	0...17	N/A	R/W	
403444	3443	0x0D73	1	Condition 1 Argument 2 Number	Byte	1	Depends on control source	N/A	R/W	
403445	3444	0x0D74	1	Condition 1 Operator	Byte	0 - ==	0...9	N/A	R/W	
403446	3445	0x0D75	1	Condition 2 Argument 1 Source	Byte	0 - Control Not	0...17	N/A	R/W	
403447	3446	0x0D76	1	Condition 2 Argument 1 Number	Byte	1	Depends on control source	N/A	R/W	
403448	3447	0x0D77	1	Condition 2 Argument 2 Source	Byte	0 - Control Not	0...17	N/A	R/W	
403449	3448	0x0D78	1	Condition 2 Argument 2 Number	Byte	1	Depends on control source	N/A	R/W	
403450	3449	0x0D79	1	Condition 2 Operator	Byte	0 - ==	0...9	N/A	R/W	
403451	3450	0x0D7A	1	Conditional Result Operator	Byte	0 - OR	0...2	N/A	R/W	
403452	3451	0x0D7B	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Conditional Logic Block 10</b>										
403460	3459	0x0D83	1	Conditional Block Enabled	Byte	0 - Disabled	Disabled/Enabled	N/A	R/W	
403461	3460	0x0D84	1	Condition 1 Argument 1 Source	Byte	0 - Control Not	0...17	N/A	R/W	
403462	3461	0x0D85	1	Condition 1 Argument 1 Number	Byte	1	Depends on control source	N/A	R/W	
403463	3462	0x0D86	1	Condition 1 Argument 2 Source	Byte	0 - Control Not	0...17	N/A	R/W	
403464	3463	0x0D87	1	Condition 1 Argument 2 Number	Byte	1	Depends on control source	N/A	R/W	
403465	3464	0x0D88	1	Condition 1 Operator	Byte	0 - ==	0...9	N/A	R/W	
403466	3465	0x0D89	1	Condition 2 Argument 1 Source	Byte	0 - Control Not	0...17	N/A	R/W	
403467	3466	0x0D8A	1	Condition 2 Argument 1 Number	Byte	1	Depends on control source	N/A	R/W	
403468	3467	0x0D8B	1	Condition 2 Argument 2 Source	Byte	0 - Control Not	0...17	N/A	R/W	
403469	3468	0x0D8C	1	Condition 2 Argument 2 Number	Byte	1	Depends on control source	N/A	R/W	
403470	3469	0x0D8D	1	Condition 2 Operator	Byte	0 - ==	0...9	N/A	R/W	
403471	3470	0x0D8E	1	Conditional Result Operator	Byte	0 - OR	0...2	N/A	R/W	
403472	3471	0x0D8F	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Set-Reset Latch Function Block #1</b>										
403480	3479	0x0D97	1	Block Enabled	Byte	0 - Disabled	Disabled/Enabled	N/A	R/W	
403481	3480	0x0D98	1	Reset Source	Byte	0 - Control Not	0...17	N/A	R/W	
403482	3481	0x0D99	1	Reset Number	Byte	1	Depends on control source	N/A	R/W	
403483	3482	0x0D9A	2	Reset Minimum Treshold	Float	0	0...100%	N/A	R/W	
403485	3484	0x0D9C	2	Reset Maximum Treshold	Float	100	0...100%	N/A	R/W	
403487	3486	0x0D9E	1	Set Source	Byte	0 - Control Not	0...17	N/A	R/W	
403488	3487	0x0D9F	1	Set number	Byte	1	Depends on control source	N/A	R/W	
403489	3488	0x0DA0	2	Set Maximum Treshold	Byte	0	0...100	N/A	R/W	
403491	3490	0x0DA2	2	Set Minimum Treshold	Byte	100	0...100	N/A	R/W	
403493	3492	0x0DA4	7	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Set-Reset Latch Function Block #2</b>										
403500	3499	0x0DAB	1	Block Enabled	Byte	0 - Disabled	Disabled/Enabled	N/A	R/W	
403501	3500	0x0DAC	1	Reset Source	Byte	0 - Control Not	0...17	N/A	R/W	
403502	3501	0x0DAD	1	Reset Number	Byte	1	Depends on control source	N/A	R/W	

403503	3502	0x0DAE	2	Reset Minimum Treshold	Float	0	0...100%	N/A	R/W	
403505	3504	0x0DB0	2	Reset Maximum Treshold	Float	100	0...100%	N/A	R/W	
403507	3506	0x0DB2	1	Set Source	Byte	0 - Control Not	0...17	N/A	R/W	
403508	3507	0x0DB3	1	Set number	Byte	1	Depends on control source	N/A	R/W	
403509	3508	0x0DB4	2	Set Maximum Treshold	Byte	0	0...100	N/A	R/W	
403511	3510	0x0DB6	2	Set Minimum Treshold	Byte	100	0...100	N/A	R/W	
403513	3512	0x0DB8	7	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Set-Reset Latch Function Block #3</b>										
403520	3519	0x0DBF	1	Block Enabled	Byte	0 - Disabled	Disabled/Enabled	N/A	R/W	
403521	3520	0x0DC0	1	Reset Source	Byte	0 - Control Not	0...17	N/A	R/W	
403522	3521	0x0DC1	1	Reset Number	Byte	1	Depends on control source	N/A	R/W	
403523	3522	0x0DC2	2	Reset Minimum Treshold	Float	0	0...100%	N/A	R/W	
403525	3524	0x0DC4	2	Reset Maximum Treshold	Float	100	0...100%	N/A	R/W	
403527	3526	0x0DC6	1	Set Source	Byte	0 - Control Not	0...17	N/A	R/W	
403528	3527	0x0DC7	1	Set number	Byte	1	Depends on control source	N/A	R/W	
403529	3528	0x0DC8	2	Set Maximum Treshold	Byte	0	0...100	N/A	R/W	
403531	3530	0x0DCA	2	Set Minimum Treshold	Byte	100	0...100	N/A	R/W	
403533	3532	0x0DCC	7	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Set-Reset Latch Function Block #4</b>										
403540	3539	0x0DD3	1	Block Enabled	Byte	0 - Disabled	Disabled/Enabled	N/A	R/W	
403541	3540	0x0DD4	1	Reset Source	Byte	0 - Control Not	0...17	N/A	R/W	
403542	3541	0x0DD5	1	Reset Number	Byte	1	Depends on control source	N/A	R/W	
403543	3542	0x0DD6	2	Reset Minimum Treshold	Float	0	0...100%	N/A	R/W	
403545	3544	0x0DD8	2	Reset Maximum Treshold	Float	100	0...100%	N/A	R/W	
403547	3546	0x0DDA	1	Set Source	Byte	0 - Control Not	0...17	N/A	R/W	
403548	3547	0x0ddb	1	Set number	Byte	1	Depends on control source	N/A	R/W	
403549	3548	0x0DDC	2	Set Maximum Treshold	Byte	0	0...100	N/A	R/W	
403551	3550	0x0DDE	2	Set Minimum Treshold	Byte	100	0...100	N/A	R/W	
403553	3552	0x0DE0	7	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Set-Reset Latch Function Block #5</b>										
403560	3559	0x0DE7	1	Block Enabled	Byte	0 - Disabled	Disabled/Enabled	N/A	R/W	
403561	3560	0x0DE8	1	Reset Source	Byte	0 - Control Not	0...17	N/A	R/W	
403562	3561	0x0DE9	1	Reset Number	Byte	1	Depends on control source	N/A	R/W	
403563	3562	0x0DEA	2	Reset Minimum Treshold	Float	0	0...100%	N/A	R/W	
403565	3564	0x0DEC	2	Reset Maximum Treshold	Float	100	0...100%	N/A	R/W	
403567	3566	0x0DEE	1	Set Source	Byte	0 - Control Not	0...17	N/A	R/W	
403568	3567	0x0DEF	1	Set number	Byte	1	Depends on control source	N/A	R/W	
403569	3568	0x0DF0	2	Set Maximum Treshold	Byte	0	0...100	N/A	R/W	
403571	3570	0x0DF2	2	Set Minimum Treshold	Byte	100	0...100	N/A	R/W	
403573	3572	0x0DF4	7	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Miscellaneous Inputs</b>										
403580	3579	0x0DFB	1	Power Faults Disables Outputs	Byte	FALSE	0-1	N/A	R/W	
403581	3580	0x0DFC	1	Overtemperature Shutdown	Byte	FALSE	0-0	N/A	R/W	
403582	3581	0x0DFD	1	CAN Bus Fault Disables Outputs	Byte	FALSE	0-1	N/A	R/W	
403583	3582	0x0DFE	1	CAN1 Diagnostic Messages Settings	Byte	Diagnostic Mes	0...2	N/A	R/W	
403584	3583	0x0DFF	6	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Diagnostic Block #1</b>										
403590	3589	0x0E05	1	Fault Detection is Enabled	Byte	FALSE		N/A	R/W	
403591	3590	0x0E06	1	Function Type to Monitor	Byte	0 - Control not used		N/A	R/W	
403592	3591	0x0E07	1	Function parameter to Monitor	Byte	0 - No selection		N/A	R/W	
403593	3592	0x0E08	1	Enable Source	Byte	0 - Control not used		N/A	R/W	

403594	3593	0x0E09	1	Enable Number	Byte	0 – No selection	N/A	R/W	
403595	3594	0x0E0A	1	Enable Response	Byte	0 – Enable When ON	N/A	R/W	
403596	3595	0x0E0B	1	Fault Detection Type	Byte	0 – Min and Max Error	N/A	R/W	
403597	3596	0x0E0C	2	Maximum Value for Diagnostic Data	Float	5	N/A	R/W	
403599	3598	0x0E0E	2	Minimum Value for Diagnostic Data	Float	0	N/A	R/W	
403601	3600	0x0E10	1	Use Hysteresis When Defining Thresh	Byte	FALSE	N/A	R/W	
403602	3601	0x0E11	2	Hysteresis	Float	0	N/A	R/W	
403604	3603	0x0E13	1	Event Cleared only by DM11	Byte	FALSE	N/A	R/W	
403605	3604	0x0E14	2	Set Limit for MAXIMUM SHUTDOWN	Float		N/A	R/W	
403607	3606	0x0E16	2	Clear Limit for MAXIMUM SHUTDOWN	Float	4.8	N/A	R/W	
403609	3608	0x0E18	2	Set Limit for MAXIMUM WARNING	Float	4.6	N/A	R/W	
403611	3610	0x0E1A	2	Clear Limit for MAXIMUM WARNING	Float	0	N/A	R/W	
403613	3612	0x0E1C	2	Clear Limit for MINIMUM WARNING	Float	0	N/A	R/W	
403615	3614	0x0E1E	2	Set Limit for MINIMUM WARNING	Float	0	N/A	R/W	
403617	3616	0x0E20	2	Clear Limit for MINIMUM SHUTDOWN	Float	0	N/A	R/W	
403619	3618	0x0E22	2	Set Limit for MINIMUM SHUTDOWN	Float	0.4	N/A	R/W	
403621	3620	0x0E24	1	MAXIMUM SHUTDOWN, Event Genera	Byte	0.2	N/A	R/W	
403622	3621	0x0E25	1	MAXIMUM SHUTDOWN, Lamp Set by	Byte	TRUE	N/A	R/W	
403623	3622	0x0E26	2	MAXIMUM SHUTDOWN, SPN for Even	Double	0 – Protect	N/A	R/W	
403625	3624	0x0E28	1	MAXIMUM SHUTDOWN, FMI for Even	Byte	520448 (\$7F100)	N/A	R/W	
403626	3625	0x0E29	1	MAXIMUM SHUTDOWN, Delay Before	Byte	3, Voltage Above Normal	N/A	R/W	
403627	3626	0x0E2A	1	MAXIMUM WARNING, Event Genera	Byte	1000	N/A	R/W	
403628	3627	0x0E2B	1	MAXIMUM WARNING, Lamp Set by Ev	Byte		N/A	R/W	
403629	3628	0x0E2C	2	MAXIMUM WARNING, SPN for Event	Double	TRUE	N/A	R/W	
403631	3630	0x0E2E	1	MAXIMUM WARNING, FMI for Event	Byte	0 – Protect	N/A	R/W	
403632	3631	0x0E2F	1	MAXIMUM WARNING, Delay Before Ev	Byte		N/A	R/W	
403633	3632	0x0E30	1	MINIMUM WARNING, Event Genera	Byte	520704 (\$7F200)	N/A	R/W	
403634	3633	0x0E31	1	MINIMUM WARNING, Lamp Set by Ev	Byte	3, Voltage Above Normal	N/A	R/W	
403635	3634	0x0E32	2	MAXIMUM WARNING, SPN for Event	Double	1000	N/A	R/W	
403637	3636	0x0E34	1	MINIMUM WARNING, FMI for Event	Byte		N/A	R/W	
403638	3637	0x0E35	1	MINIMUM WARNING, Delay Before Ev	Byte	TRUE	N/A	R/W	
403639	3638	0x0E36	1	MINIMUM SHUTDOWN, Event Genera	Byte	0 – Protect	N/A	R/W	
403640	3639	0x0E37	1	MINIMUM SHUTDOWN, Lamp Set by E	Byte	520960 (\$7F300)	N/A	R/W	
403641	3640	0x0E38	2	MINIMUM SHUTDOWN, SPN for Event	Double	4, Voltage Below Normal	N/A	R/W	
403643	3642	0x0E3A	1	MINIMUM SHUTDOWN, FMI for Event	Byte	1000	N/A	R/W	
403644	3643	0x0E3B	1	MINIMUM SHUTDOWN, Delay Before	Byte		N/A	R/W	
403645	3644	0x0E3C	5	Reserved	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Diagnostic Block #2</b>									
403650	3649	0x0E41	1	Fault Detection is Enabled	Byte	FALSE	N/A	R/W	
403651	3650	0x0E42	1	Function Type to Monitor	Byte	0 – Control not used	N/A	R/W	
403652	3651	0x0E43	1	Function parameter to Monitor	Byte	0 – No selection	N/A	R/W	
403653	3652	0x0E44	1	Enable Source	Byte	0 – Control not used	N/A	R/W	
403654	3653	0x0E45	1	Enable Number	Byte	0 – No selection	N/A	R/W	
403655	3654	0x0E46	1	Enable Response	Byte	0 – Enable When ON	N/A	R/W	
403656	3655	0x0E47	1	Fault Detection Type	Byte	0 – Min and Max Error	N/A	R/W	
403657	3656	0x0E48	2	Maximum Value for Diagnostic Data	Float	5	N/A	R/W	
403659	3658	0x0E4A	2	Minimum Value for Diagnostic Data	Float	0	N/A	R/W	
403661	3660	0x0E4C	1	Use Hysteresis When Defining Thresh	Byte	FALSE	N/A	R/W	
403662	3661	0x0E4D	2	Hysteresis	Float	0	N/A	R/W	
403664	3663	0x0E4F	1	Event Cleared only by DM11	Byte	FALSE	N/A	R/W	
403665	3664	0x0E50	2	Set Limit for MAXIMUM SHUTDOWN	Float		N/A	R/W	

403667	3666	0x0E52	2	Clear Limit for MAXIMUM SHUTDOWN	Float	4.8		N/A	R/W	
403669	3668	0x0E54	2	Set Limit for MAXIMUM WARNING	Float	4.6		N/A	R/W	
403671	3670	0x0E56	2	Clear Limit for MAXIMUM WARNING	Float	0		N/A	R/W	
403673	3672	0x0E58	2	Clear Limit for MINIMUM WARNING	Float	0		N/A	R/W	
403675	3674	0x0E5A	2	Set Limit for MINIMUM WARNING	Float	0		N/A	R/W	
403677	3676	0x0E5C	2	Clear Limit for MINIMUM SHUTDOWN	Float	0		N/A	R/W	
403679	3678	0x0E5E	2	Set Limit for MINIMUM SHUTDOWN	Float	0.4		N/A	R/W	
403681	3680	0x0E60	1	MAXIMUM SHUTDOWN, Event Generate	Byte	0.2		N/A	R/W	
403682	3681	0x0E61	1	MAXIMUM SHUTDOWN, Lamp Set by	Byte	TRUE		N/A	R/W	
403683	3682	0x0E62	2	MAXIMUM SHUTDOWN, SPN for Event	Double	0 – Protect		N/A	R/W	
403685	3684	0x0E64	1	MAXIMUM SHUTDOWN, FMI for Event	Byte	520448 (\$7F100)		N/A	R/W	
403686	3685	0x0E65	1	MAXIMUM SHUTDOWN, Delay Before	Byte	3, Voltage Above Normal		N/A	R/W	
403687	3686	0x0E66	1	MAXIMUM WARNING, Event Generate	Byte	1000		N/A	R/W	
403688	3687	0x0E67	1	MAXIMUM WARNING, Lamp Set by Ev	Byte			N/A	R/W	
403689	3688	0x0E68	2	MAXIMUM WARNING, SPN for Event	Double	TRUE		N/A	R/W	
403691	3690	0x0E6A	1	MAXIMUM WARNING, FMI for Event	Byte	0 – Protect		N/A	R/W	
403692	3691	0x0E6B	1	MAXIMUM WARNING, Delay Before Ev	Byte			N/A	R/W	
403693	3692	0x0E6C	1	MINIMUM WARNING, Event Generate	Byte	520704 (\$7F200)		N/A	R/W	
403694	3693	0x0E6D	1	MINIMUM WARNING, Lamp Set by Ev	Byte	3, Voltage Above Normal		N/A	R/W	
403695	3694	0x0E6E	2	MAXIMUM WARNING, SPN for Event	Double	1000		N/A	R/W	
403697	3696	0x0E70	1	MINIMUM WARNING, FMI for Event	Byte			N/A	R/W	
403698	3697	0x0E71	1	MINIMUM WARNING, Delay Before Ev	Byte	TRUE		N/A	R/W	
403699	3698	0x0E72	1	MINIMUM SHUTDOWN, Event Generate	Byte	0 – Protect		N/A	R/W	
403700	3699	0x0E73	1	MINIMUM SHUTDOWN, Lamp Set by E	Byte	520960 (\$7F300)		N/A	R/W	
403701	3700	0x0E74	2	MINIMUM SHUTDOWN, SPN for Event	Double	4, Voltage Below Normal		N/A	R/W	
403703	3702	0x0E76	1	MINIMUM SHUTDOWN, FMI for Event	Byte	1000		N/A	R/W	
403704	3703	0x0E77	1	MINIMUM SHUTDOWN, Delay Before	Byte			N/A	R/W	
403705	3704	0x0E78	5	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Diagnostic Block #3</b>										
403710	3709	0x0E7D	1	Fault Detection is Enabled	Byte	FALSE		N/A	R/W	
403711	3710	0x0E7E	1	Function Type to Monitor	Byte	0 – Control not used		N/A	R/W	
403712	3711	0x0E7F	1	Function parameter to Monitor	Byte	0 – No selection		N/A	R/W	
403713	3712	0x0E80	1	Enable Source	Byte	0 – Control not used		N/A	R/W	
403714	3713	0x0E81	1	Enable Number	Byte	0 – No selection		N/A	R/W	
403715	3714	0x0E82	1	Enable Response	Byte	0 – Enable When ON		N/A	R/W	
403716	3715	0x0E83	1	Fault Detection Type	Byte	0 – Min and Max Error		N/A	R/W	
403717	3716	0x0E84	2	Maximum Value for Diagnostic Data	Float	5		N/A	R/W	
403719	3718	0x0E86	2	Minimum Value for Diagnostic Data	Float	0		N/A	R/W	
403721	3720	0x0E88	1	Use Hysteresis When Defining Threshd	Byte	FALSE		N/A	R/W	
403722	3721	0x0E89	2	Hysteresis	Float	0		N/A	R/W	
403724	3723	0x0E8B	1	Event Cleared only by DM11	Byte	FALSE		N/A	R/W	
403725	3724	0x0E8C	2	Set Limit for MAXIMUM SHUTDOWN	Float			N/A	R/W	
403727	3726	0x0E8E	2	Clear Limit for MAXIMUM SHUTDOWN	Float	4.8		N/A	R/W	
403729	3728	0x0E90	2	Set Limit for MAXIMUM WARNING	Float	4.6		N/A	R/W	
403731	3730	0x0E92	2	Clear Limit for MAXIMUM WARNING	Float	0		N/A	R/W	
403733	3732	0x0E94	2	Clear Limit for MINIMUM WARNING	Float	0		N/A	R/W	
403735	3734	0x0E96	2	Set Limit for MINIMUM WARNING	Float	0		N/A	R/W	
403737	3736	0x0E98	2	Clear Limit for MINIMUM SHUTDOWN	Float	0		N/A	R/W	
403739	3738	0x0E9A	2	Set Limit for MINIMUM SHUTDOWN	Float	0.4		N/A	R/W	
403741	3740	0x0E9C	1	MAXIMUM SHUTDOWN, Event Generate	Byte	0.2		N/A	R/W	
403742	3741	0x0E9D	1	MAXIMUM SHUTDOWN, Lamp Set by	Byte	TRUE		N/A	R/W	

403743	3742	0x0E9E	2	MAXIMUM SHUTDOWN, SPN for Event	Double	0 – Protect		N/A	R/W	
403745	3744	0x0EA0	1	MAXIMUM SHUTDOWN, FMI for Event	Byte	520448 (\$7F100)		N/A	R/W	
403746	3745	0x0EA1	1	MAXIMUM SHUTDOWN, Delay Before Event	Byte	3, Voltage Above Normal		N/A	R/W	
403747	3746	0x0EA2	1	MAXIMUM WARNING, Event Generated	Byte	1000		N/A	R/W	
403748	3747	0x0EA3	1	MAXIMUM WARNING, Lamp Set by Event	Byte			N/A	R/W	
403749	3748	0x0EA4	2	MAXIMUM WARNING, SPN for Event	Double	TRUE		N/A	R/W	
403751	3750	0x0EA6	1	MAXIMUM WARNING, FMI for Event	Byte	0 – Protect		N/A	R/W	
403752	3751	0x0EA7	1	MAXIMUM WARNING, Delay Before Event	Byte			N/A	R/W	
403753	3752	0x0EA8	1	MINIMUM WARNING, Event Generated	Byte	520704 (\$7F200)		N/A	R/W	
403754	3753	0x0EA9	1	MINIMUM WARNING, Lamp Set by Event	Byte	3, Voltage Above Normal		N/A	R/W	
403755	3754	0x0EAA	2	MAXIMUM WARNING, SPN for Event	Double	1000		N/A	R/W	
403757	3756	0x0EAC	1	MINIMUM WARNING, FMI for Event	Byte			N/A	R/W	
403758	3757	0x0EAD	1	MINIMUM WARNING, Delay Before Event	Byte	TRUE		N/A	R/W	
403759	3758	0x0EAE	1	MINIMUM SHUTDOWN, Event Generated	Byte	0 – Protect		N/A	R/W	
403760	3759	0x0EAF	1	MINIMUM SHUTDOWN, Lamp Set by Event	Byte	520960 (\$7F300)		N/A	R/W	
403761	3760	0x0EB0	2	MINIMUM SHUTDOWN, SPN for Event	Double	4, Voltage Below Normal		N/A	R/W	
403763	3762	0x0EB2	1	MINIMUM SHUTDOWN, FMI for Event	Byte	1000		N/A	R/W	
403764	3763	0x0EB3	1	MINIMUM SHUTDOWN, Delay Before Event	Byte			N/A	R/W	
403765	3764	0x0EB4	5	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Diagnostic Block #4</b>										
403770	3769	0x0EB9	1	Fault Detection is Enabled	Byte	FALSE		N/A	R/W	
403771	3770	0x0EBA	1	Function Type to Monitor	Byte	0 – Control not used		N/A	R/W	
403772	3771	0x0EBB	1	Function parameter to Monitor	Byte	0 – No selection		N/A	R/W	
403773	3772	0x0EBC	1	Enable Source	Byte	0 – Control not used		N/A	R/W	
403774	3773	0x0EBD	1	Enable Number	Byte	0 – No selection		N/A	R/W	
403775	3774	0x0EBE	1	Enable Response	Byte	0 – Enable When ON		N/A	R/W	
403776	3775	0x0EBF	1	Fault Detection Type	Byte	0 – Min and Max Error		N/A	R/W	
403777	3776	0x0EC0	2	Maximum Value for Diagnostic Data	Float	5		N/A	R/W	
403779	3778	0x0EC2	2	Minimum Value for Diagnostic Data	Float	0		N/A	R/W	
403781	3780	0x0EC4	1	Use Hysteresis When Defining Threshold	Byte	FALSE		N/A	R/W	
403782	3781	0x0EC5	2	Hysteresis	Float	0		N/A	R/W	
403784	3783	0x0EC7	1	Event Cleared only by DM11	Byte	FALSE		N/A	R/W	
403785	3784	0x0EC8	2	Set Limit for MAXIMUM SHUTDOWN	Float			N/A	R/W	
403787	3786	0x0ECA	2	Clear Limit for MAXIMUM SHUTDOWN	Float	4.8		N/A	R/W	
403789	3788	0x0ECC	2	Set Limit for MAXIMUM WARNING	Float	4.6		N/A	R/W	
403791	3790	0x0ECE	2	Clear Limit for MAXIMUM WARNING	Float	0		N/A	R/W	
403793	3792	0x0ED0	2	Clear Limit for MINIMUM WARNING	Float	0		N/A	R/W	
403795	3794	0x0ED2	2	Set Limit for MINIMUM WARNING	Float	0		N/A	R/W	
403797	3796	0x0ED4	2	Clear Limit for MINIMUM SHUTDOWN	Float	0		N/A	R/W	
403799	3798	0x0ED6	2	Set Limit for MINIMUM SHUTDOWN	Float	0.4		N/A	R/W	
403801	3800	0x0ED8	1	MAXIMUM SHUTDOWN, Event Generated	Byte	0.2		N/A	R/W	
403802	3801	0x0ED9	1	MAXIMUM SHUTDOWN, Lamp Set by Event	Byte	TRUE		N/A	R/W	
403803	3802	0x0EDA	2	MAXIMUM SHUTDOWN, SPN for Event	Double	0 – Protect		N/A	R/W	
403805	3804	0x0EDC	1	MAXIMUM SHUTDOWN, FMI for Event	Byte	520448 (\$7F100)		N/A	R/W	
403806	3805	0x0EDD	1	MAXIMUM SHUTDOWN, Delay Before Event	Byte	3, Voltage Above Normal		N/A	R/W	
403807	3806	0x0EDE	1	MAXIMUM WARNING, Event Generated	Byte	1000		N/A	R/W	
403808	3807	0x0EDF	1	MAXIMUM WARNING, Lamp Set by Event	Byte			N/A	R/W	
403809	3808	0x0EE0	2	MAXIMUM WARNING, SPN for Event	Double	TRUE		N/A	R/W	
403811	3810	0x0EE2	1	MAXIMUM WARNING, FMI for Event	Byte	0 – Protect		N/A	R/W	
403812	3811	0x0EE3	1	MAXIMUM WARNING, Delay Before Event	Byte			N/A	R/W	
403813	3812	0x0EE4	1	MINIMUM WARNING, Event Generated	Byte	520704 (\$7F200)		N/A	R/W	



403814	3813	0x0EE5	1	MINIMUM WARNING, Lamp Set by Event	Byte	3, Voltage Above Normal	N/A	R/W	
403815	3814	0x0EE6	2	MAXIMUM WARNING, SPN for Event	Double	1000	N/A	R/W	
403817	3816	0x0EE8	1	MINIMUM WARNING, FMI for Event	Byte		N/A	R/W	
403818	3817	0x0EE9	1	MINIMUM WARNING, Delay Before Event	Byte	TRUE	N/A	R/W	
403819	3818	0x0EEA	1	MINIMUM SHUTDOWN, Event Generated	Byte	0 – Protect	N/A	R/W	
403820	3819	0x0EEB	1	MINIMUM SHUTDOWN, Lamp Set by Event	Byte	520960 (\$7F300)	N/A	R/W	
403821	3820	0x0EEC	2	MINIMUM SHUTDOWN, SPN for Event	Double	4, Voltage Below Normal	N/A	R/W	
403823	3822	0x0EEE	1	MINIMUM SHUTDOWN, FMI for Event	Byte	1000	N/A	R/W	
403824	3823	0x0EEF	1	MINIMUM SHUTDOWN, Delay Before Event	Byte		N/A	R/W	
403825	3824	0x0EF0	5	Reserved	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing results 0.
<b>Diagnostic Block #5</b>									
403830	3829	0x0EF5	1	Fault Detection is Enabled	Byte	FALSE	N/A	R/W	
403831	3830	0x0EF6	1	Function Type to Monitor	Byte	0 – Control not used	N/A	R/W	
403832	3831	0x0EF7	1	Function parameter to Monitor	Byte	0 – No selection	N/A	R/W	
403833	3832	0x0EF8	1	Enable Source	Byte	0 – Control not used	N/A	R/W	
403834	3833	0x0EF9	1	Enable Number	Byte	0 – No selection	N/A	R/W	
403835	3834	0x0EFA	1	Enable Response	Byte	0 – Enable When ON	N/A	R/W	
403836	3835	0x0EFB	1	Fault Detection Type	Byte	0 – Min and Max Error	N/A	R/W	
403837	3836	0x0EFC	2	Maximum Value for Diagnostic Data	Float	5	N/A	R/W	
403839	3838	0x0EFE	2	Minimum Value for Diagnostic Data	Float	0	N/A	R/W	
403841	3840	0x0F00	1	Use Hysteresis When Defining Threshold	Byte	FALSE	N/A	R/W	
403842	3841	0x0F01	2	Hysteresis	Float	0	N/A	R/W	
403844	3843	0x0F03	1	Event Cleared only by DM11	Byte	FALSE	N/A	R/W	
403845	3844	0x0F04	2	Set Limit for MAXIMUM SHUTDOWN	Float		N/A	R/W	
403847	3846	0x0F06	2	Clear Limit for MAXIMUM SHUTDOWN	Float	4.8	N/A	R/W	
403849	3848	0x0F08	2	Set Limit for MAXIMUM WARNING	Float	4.6	N/A	R/W	
403851	3850	0x0F0A	2	Clear Limit for MAXIMUM WARNING	Float	0	N/A	R/W	
403853	3852	0x0F0C	2	Clear Limit for MINIMUM WARNING	Float	0	N/A	R/W	
403855	3854	0x0F0E	2	Set Limit for MINIMUM WARNING	Float	0	N/A	R/W	
403857	3856	0x0F10	2	Clear Limit for MINIMUM SHUTDOWN	Float	0	N/A	R/W	
403859	3858	0x0F12	2	Set Limit for MINIMUM SHUTDOWN	Float	0.4	N/A	R/W	
403861	3860	0x0F14	1	MAXIMUM SHUTDOWN, Event Generated	Byte	0.2	N/A	R/W	
403862	3861	0x0F15	1	MAXIMUM SHUTDOWN, Lamp Set by Event	Byte	TRUE	N/A	R/W	
403863	3862	0x0F16	2	MAXIMUM SHUTDOWN, SPN for Event	Double	0 – Protect	N/A	R/W	
403865	3864	0x0F18	1	MAXIMUM SHUTDOWN, FMI for Event	Byte	520448 (\$7F100)	N/A	R/W	
403866	3865	0x0F19	1	MAXIMUM SHUTDOWN, Delay Before Event	Byte	3, Voltage Above Normal	N/A	R/W	
403867	3866	0x0F1A	1	MAXIMUM WARNING, Event Generated	Byte	1000	N/A	R/W	
403868	3867	0x0F1B	1	MAXIMUM WARNING, Lamp Set by Event	Byte		N/A	R/W	
403869	3868	0x0F1C	2	MAXIMUM WARNING, SPN for Event	Double	TRUE	N/A	R/W	
403871	3870	0x0F1E	1	MAXIMUM WARNING, FMI for Event	Byte	0 – Protect	N/A	R/W	
403872	3871	0x0F1F	1	MAXIMUM WARNING, Delay Before Event	Byte		N/A	R/W	
403873	3872	0x0F20	1	MINIMUM WARNING, Event Generated	Byte	520704 (\$7F200)	N/A	R/W	
403874	3873	0x0F21	1	MINIMUM WARNING, Lamp Set by Event	Byte	3, Voltage Above Normal	N/A	R/W	
403875	3874	0x0F22	2	MAXIMUM WARNING, SPN for Event	Double	1000	N/A	R/W	
403877	3876	0x0F24	1	MINIMUM WARNING, FMI for Event	Byte		N/A	R/W	
403878	3877	0x0F25	1	MINIMUM WARNING, Delay Before Event	Byte	TRUE	N/A	R/W	
403879	3878	0x0F26	1	MINIMUM SHUTDOWN, Event Generated	Byte	0 – Protect	N/A	R/W	
403880	3879	0x0F27	1	MINIMUM SHUTDOWN, Lamp Set by Event	Byte	520960 (\$7F300)	N/A	R/W	
403881	3880	0x0F28	2	MINIMUM SHUTDOWN, SPN for Event	Double	4, Voltage Below Normal	N/A	R/W	
403883	3882	0x0F2A	1	MINIMUM SHUTDOWN, FMI for Event	Byte	1000	N/A	R/W	
403884	3883	0x0F2B	1	MINIMUM SHUTDOWN, Delay Before Event	Byte		N/A	R/W	

403885	3884	0x0F2C	5	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Diagnostic Block #6</b>										
403890	3889	0x0F31	1	Fault Detection is Enabled	Byte	FALSE		N/A	R/W	
403891	3890	0x0F32	1	Function Type to Monitor	Byte	0 – Control not used		N/A	R/W	
403892	3891	0x0F33	1	Function parameter to Monitor	Byte	0 – No selection		N/A	R/W	
403893	3892	0x0F34	1	Enable Source	Byte	0 – Control not used		N/A	R/W	
403894	3893	0x0F35	1	Enable Number	Byte	0 – No selection		N/A	R/W	
403895	3894	0x0F36	1	Enable Response	Byte	0 – Enable When ON		N/A	R/W	
403896	3895	0x0F37	1	Fault Detection Type	Byte	0 – Min and Max Error		N/A	R/W	
403897	3896	0x0F38	2	Maximum Value for Diagnostic Data	Float	5		N/A	R/W	
403899	3898	0x0F3A	2	Minimum Value for Diagnostic Data	Float	0		N/A	R/W	
403901	3900	0x0F3C	1	Use Hysteresis When Defining Threshold	Byte	FALSE		N/A	R/W	
403902	3901	0x0F3D	2	Hysteresis	Float	0		N/A	R/W	
403904	3903	0x0F3F	1	Event Cleared only by DM11	Byte	FALSE		N/A	R/W	
403905	3904	0x0F40	2	Set Limit for MAXIMUM SHUTDOWN	Float			N/A	R/W	
403907	3906	0x0F42	2	Clear Limit for MAXIMUM SHUTDOWN	Float	4.8		N/A	R/W	
403909	3908	0x0F44	2	Set Limit for MAXIMUM WARNING	Float	4.6		N/A	R/W	
403911	3910	0x0F46	2	Clear Limit for MAXIMUM WARNING	Float	0		N/A	R/W	
403913	3912	0x0F48	2	Clear Limit for MINIMUM WARNING	Float	0		N/A	R/W	
403915	3914	0x0F4A	2	Set Limit for MINIMUM WARNING	Float	0		N/A	R/W	
403917	3916	0x0F4C	2	Clear Limit for MINIMUM SHUTDOWN	Float	0		N/A	R/W	
403919	3918	0x0F4E	2	Set Limit for MINIMUM SHUTDOWN	Float	0.4		N/A	R/W	
403921	3920	0x0F50	1	MAXIMUM SHUTDOWN, Event Generate	Byte	0.2		N/A	R/W	
403922	3921	0x0F51	1	MAXIMUM SHUTDOWN, Lamp Set by Ev	Byte	TRUE		N/A	R/W	
403923	3922	0x0F52	2	MAXIMUM SHUTDOWN, SPN for Event	Double	0 – Protect		N/A	R/W	
403925	3924	0x0F54	1	MAXIMUM SHUTDOWN, FMI for Event	Byte	520448 (\$7F100)		N/A	R/W	
403926	3925	0x0F55	1	MAXIMUM SHUTDOWN, Delay Before Ev	Byte	3, Voltage Above Normal		N/A	R/W	
403927	3926	0x0F56	1	MAXIMUM WARNING, Event Generate	Byte	1000		N/A	R/W	
403928	3927	0x0F57	1	MAXIMUM WARNING, Lamp Set by Ev	Byte			N/A	R/W	
403929	3928	0x0F58	2	MAXIMUM WARNING, SPN for Event	Double	TRUE		N/A	R/W	
403931	3930	0x0F5A	1	MAXIMUM WARNING, FMI for Event	Byte	0 – Protect		N/A	R/W	
403932	3931	0x0F5B	1	MAXIMUM WARNING, Delay Before Ev	Byte			N/A	R/W	
403933	3932	0x0F5C	1	MINIMUM WARNING, Event Generate	Byte	520704 (\$7F200)		N/A	R/W	
403934	3933	0x0F5D	1	MINIMUM WARNING, Lamp Set by Ev	Byte	3, Voltage Above Normal		N/A	R/W	
403935	3934	0x0F5E	2	MAXIMUM WARNING, SPN for Event	Double	1000		N/A	R/W	
403937	3936	0x0F60	1	MINIMUM WARNING, FMI for Event	Byte			N/A	R/W	
403938	3937	0x0F61	1	MINIMUM WARNING, Delay Before Ev	Byte	TRUE		N/A	R/W	
403939	3938	0x0F62	1	MINIMUM SHUTDOWN, Event Generate	Byte	0 – Protect		N/A	R/W	
403940	3939	0x0F63	1	MINIMUM SHUTDOWN, Lamp Set by Ev	Byte	520960 (\$7F300)		N/A	R/W	
403941	3940	0x0F64	2	MINIMUM SHUTDOWN, SPN for Event	Double	4, Voltage Below Normal		N/A	R/W	
403943	3942	0x0F66	1	MINIMUM SHUTDOWN, FMI for Event	Byte	1000		N/A	R/W	
403944	3943	0x0F67	1	MINIMUM SHUTDOWN, Delay Before Ev	Byte			N/A	R/W	
403945	3944	0x0F68	5	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Diagnostic Block #7</b>										
403950	3949	0x0F6D	1	Fault Detection is Enabled	Byte	FALSE		N/A	R/W	
403951	3950	0x0F6E	1	Function Type to Monitor	Byte	0 – Control not used		N/A	R/W	
403952	3951	0x0F6F	1	Function parameter to Monitor	Byte	0 – No selection		N/A	R/W	
403953	3952	0x0F70	1	Enable Source	Byte	0 – Control not used		N/A	R/W	
403954	3953	0x0F71	1	Enable Number	Byte	0 – No selection		N/A	R/W	
403955	3954	0x0F72	1	Enable Response	Byte	0 – Enable When ON		N/A	R/W	
403956	3955	0x0F73	1	Fault Detection Type	Byte	0 – Min and Max Error		N/A	R/W	

403957	3956	0x0F74	2	Maximum Value for Diagnostic Data	Float	5		N/A	R/W	
403959	3958	0x0F76	2	Minimum Value for Diagnostic Data	Float	0		N/A	R/W	
403961	3960	0x0F78	1	Use Hysteresis When Defining Threshold	Byte	FALSE		N/A	R/W	
403962	3961	0x0F79	2	Hysteresis	Float	0		N/A	R/W	
403964	3963	0x0F7B	1	Event Cleared only by DM11	Byte	FALSE		N/A	R/W	
403965	3964	0x0F7C	2	Set Limit for MAXIMUM SHUTDOWN	Float			N/A	R/W	
403967	3966	0x0F7E	2	Clear Limit for MAXIMUM SHUTDOWN	Float	4.8		N/A	R/W	
403969	3968	0x0F80	2	Set Limit for MAXIMUM WARNING	Float	4.6		N/A	R/W	
403971	3970	0x0F82	2	Clear Limit for MAXIMUM WARNING	Float	0		N/A	R/W	
403973	3972	0x0F84	2	Clear Limit for MINIMUM WARNING	Float	0		N/A	R/W	
403975	3974	0x0F86	2	Set Limit for MINIMUM WARNING	Float	0		N/A	R/W	
403977	3976	0x0F88	2	Clear Limit for MINIMUM SHUTDOWN	Float	0		N/A	R/W	
403979	3978	0x0F8A	2	Set Limit for MINIMUM SHUTDOWN	Float	0.4		N/A	R/W	
403981	3980	0x0F8C	1	MAXIMUM SHUTDOWN, Event Generated	Byte	0.2		N/A	R/W	
403982	3981	0x0F8D	1	MAXIMUM SHUTDOWN, Lamp Set by Event	Byte	TRUE		N/A	R/W	
403983	3982	0x0F8E	2	MAXIMUM SHUTDOWN, SPN for Event	Double	0 – Protect		N/A	R/W	
403985	3984	0x0F90	1	MAXIMUM SHUTDOWN, FMI for Event	Byte	520448 (\$7F100)		N/A	R/W	
403986	3985	0x0F91	1	MAXIMUM SHUTDOWN, Delay Before Event	Byte	3, Voltage Above Normal		N/A	R/W	
403987	3986	0x0F92	1	MAXIMUM WARNING, Event Generated	Byte	1000		N/A	R/W	
403988	3987	0x0F93	1	MAXIMUM WARNING, Lamp Set by Event	Byte			N/A	R/W	
403989	3988	0x0F94	2	MAXIMUM WARNING, SPN for Event	Double	TRUE		N/A	R/W	
403991	3990	0x0F96	1	MAXIMUM WARNING, FMI for Event	Byte	0 – Protect		N/A	R/W	
403992	3991	0x0F97	1	MAXIMUM WARNING, Delay Before Event	Byte			N/A	R/W	
403993	3992	0x0F98	1	MINIMUM WARNING, Event Generated	Byte	520704 (\$7F200)		N/A	R/W	
403994	3993	0x0F99	1	MINIMUM WARNING, Lamp Set by Event	Byte	3, Voltage Above Normal		N/A	R/W	
403995	3994	0x0F9A	2	MAXIMUM WARNING, SPN for Event	Double	1000		N/A	R/W	
403997	3996	0x0F9C	1	MINIMUM WARNING, FMI for Event	Byte			N/A	R/W	
403998	3997	0x0F9D	1	MINIMUM WARNING, Delay Before Event	Byte	TRUE		N/A	R/W	
403999	3998	0x0F9E	1	MINIMUM SHUTDOWN, Event Generated	Byte	0 – Protect		N/A	R/W	
404000	3999	0x0F9F	1	MINIMUM SHUTDOWN, Lamp Set by Event	Byte	520960 (\$7F300)		N/A	R/W	
404001	4000	0x0FA0	2	MINIMUM SHUTDOWN, SPN for Event	Double	4, Voltage Below Normal		N/A	R/W	
404003	4002	0x0FA2	1	MINIMUM SHUTDOWN, FMI for Event	Byte	1000		N/A	R/W	
404004	4003	0x0FA3	1	MINIMUM SHUTDOWN, Delay Before Event	Byte			N/A	R/W	
404005	4004	0x0FA4	5	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Diagnostic Block #8</b>										
404010	4009	0x0FA9	1	Fault Detection is Enabled	Byte	FALSE		N/A	R/W	
404011	4010	0x0FAA	1	Function Type to Monitor	Byte	0 – Control not used		N/A	R/W	
404012	4011	0x0FAB	1	Function parameter to Monitor	Byte	0 – No selection		N/A	R/W	
404013	4012	0x0FAC	1	Enable Source	Byte	0 – Control not used		N/A	R/W	
404014	4013	0x0FAD	1	Enable Number	Byte	0 – No selection		N/A	R/W	
404015	4014	0x0FAE	1	Enable Response	Byte	0 – Enable When ON		N/A	R/W	
404016	4015	0x0FAF	1	Fault Detection Type	Byte	0 – Min and Max Error		N/A	R/W	
404017	4016	0x0FB0	2	Maximum Value for Diagnostic Data	Float	5		N/A	R/W	
404019	4018	0x0FB2	2	Minimum Value for Diagnostic Data	Float	0		N/A	R/W	
404021	4020	0x0FB4	1	Use Hysteresis When Defining Threshold	Byte	FALSE		N/A	R/W	
404022	4021	0x0FB5	2	Hysteresis	Float	0		N/A	R/W	
404024	4023	0x0FB7	1	Event Cleared only by DM11	Byte	FALSE		N/A	R/W	
404025	4024	0x0FB8	2	Set Limit for MAXIMUM SHUTDOWN	Float			N/A	R/W	
404027	4026	0x0FBA	2	Clear Limit for MAXIMUM SHUTDOWN	Float	4.8		N/A	R/W	
404029	4028	0x0FBC	2	Set Limit for MAXIMUM WARNING	Float	4.6		N/A	R/W	
404031	4030	0x0FBE	2	Clear Limit for MAXIMUM WARNING	Float	0		N/A	R/W	

404033	4032	0x0FC0	2	Clear Limit for MINIMUM WARNING	Float	0		N/A	R/W	
404035	4034	0x0FC2	2	Set Limit for MINIMUM WARNING	Float	0		N/A	R/W	
404037	4036	0x0FC4	2	Clear Limit for MINIMUM SHUTDOWN	Float	0		N/A	R/W	
404039	4038	0x0FC6	2	Set Limit for MINIMUM SHUTDOWN	Float	0.4		N/A	R/W	
404041	4040	0x0FC8	1	MAXIMUM SHUTDOWN, Event Generate	Byte	0.2		N/A	R/W	
404042	4041	0x0FC9	1	MAXIMUM SHUTDOWN, Lamp Set by	Byte	TRUE		N/A	R/W	
404043	4042	0x0FCA	2	MAXIMUM SHUTDOWN, SPN for Event	Double	0 – Protect		N/A	R/W	
404045	4044	0x0FCC	1	MAXIMUM SHUTDOWN, FMI for Event	Byte	520448 (\$7F100)		N/A	R/W	
404046	4045	0x0FCD	1	MAXIMUM SHUTDOWN, Delay Before	Byte	3, Voltage Above Normal		N/A	R/W	
404047	4046	0x0FCE	1	MAXIMUM WARNING, Event Generate	Byte	1000		N/A	R/W	
404048	4047	0x0FCF	1	MAXIMUM WARNING, Lamp Set by Ev	Byte			N/A	R/W	
404049	4048	0x0FDD	2	MAXIMUM WARNING, SPN for Event	Double	TRUE		N/A	R/W	
404051	4050	0x0FD2	1	MAXIMUM WARNING, FMI for Event	Byte	0 – Protect		N/A	R/W	
404052	4051	0x0FD3	1	MAXIMUM WARNING, Delay Before Ev	Byte			N/A	R/W	
404053	4052	0x0FD4	1	MINIMUM WARNING, Event Generate	Byte	520704 (\$7F200)		N/A	R/W	
404054	4053	0x0FD5	1	MINIMUM WARNING, Lamp Set by Ev	Byte	3, Voltage Above Normal		N/A	R/W	
404055	4054	0x0FD6	2	MAXIMUM WARNING, SPN for Event	Double	1000		N/A	R/W	
404057	4056	0x0FD8	1	MINIMUM WARNING, FMI for Event	Byte			N/A	R/W	
404058	4057	0x0FD9	1	MINIMUM WARNING, Delay Before Ev	Byte	TRUE		N/A	R/W	
404059	4058	0x0FDA	1	MINIMUM SHUTDOWN, Event Genera	Byte	0 – Protect		N/A	R/W	
404060	4059	0x0FDB	1	MINIMUM SHUTDOWN, Lamp Set by B	Byte	520960 (\$7F300)		N/A	R/W	
404061	4060	0x0FDC	2	MINIMUM SHUTDOWN, SPN for Event	Double	4, Voltage Below Normal		N/A	R/W	
404063	4062	0x0FDE	1	MINIMUM SHUTDOWN, FMI for Event	Byte	1000		N/A	R/W	
404064	4063	0x0FDF	1	MINIMUM SHUTDOWN, Delay Before	Byte			N/A	R/W	
404065	4064	0x0FE0	5	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Diagnostic Block #9</b>										
404070	4069	0x0FE5	1	Fault Detection is Enabled	Byte	FALSE		N/A	R/W	
404071	4070	0x0FE6	1	Function Type to Monitor	Byte	0 – Control not used		N/A	R/W	
404072	4071	0x0FE7	1	Function parameter to Monitor	Byte	0 – No selection		N/A	R/W	
404073	4072	0x0FE8	1	Enable Source	Byte	0 – Control not used		N/A	R/W	
404074	4073	0x0FE9	1	Enable Number	Byte	0 – No selection		N/A	R/W	
404075	4074	0x0FEA	1	Enable Response	Byte	0 – Enable When ON		N/A	R/W	
404076	4075	0x0FEB	1	Fault Detection Type	Byte	0 – Min and Max Error		N/A	R/W	
404077	4076	0x0FEC	2	Maximum Value for Diagnostic Data	Float	5		N/A	R/W	
404079	4078	0x0FEE	2	Minimum Value for Diagnostic Data	Float	0		N/A	R/W	
404081	4080	0x0FF0	1	Use Hysteresis When Defining Threshd	Byte	FALSE		N/A	R/W	
404082	4081	0x0FF1	2	Hysteresis	Float	0		N/A	R/W	
404084	4083	0x0FF3	1	Event Cleared only by DM11	Byte	FALSE		N/A	R/W	
404085	4084	0x0FF4	2	Set Limit for MAXIMUM SHUTDOWN	Float			N/A	R/W	
404087	4086	0x0FF6	2	Clear Limit for MAXIMUM SHUTDOWN	Float	4.8		N/A	R/W	
404089	4088	0x0FF8	2	Set Limit for MAXIMUM WARNING	Float	4.6		N/A	R/W	
404091	4090	0x0FFA	2	Clear Limit for MAXIMUM WARNING	Float	0		N/A	R/W	
404093	4092	0x0FFC	2	Clear Limit for MINIMUM WARNING	Float	0		N/A	R/W	
404095	4094	0x0FFE	2	Set Limit for MINIMUM WARNING	Float	0		N/A	R/W	
404097	4096	0x1000	2	Clear Limit for MINIMUM SHUTDOWN	Float	0		N/A	R/W	
404099	4098	0x1002	2	Set Limit for MINIMUM SHUTDOWN	Float	0.4		N/A	R/W	
404101	4100	0x1004	1	MAXIMUM SHUTDOWN, Event Genera	Byte	0.2		N/A	R/W	
404102	4101	0x1005	1	MAXIMUM SHUTDOWN, Lamp Set by	Byte	TRUE		N/A	R/W	
404103	4102	0x1006	2	MAXIMUM SHUTDOWN, SPN for Event	Double	0 – Protect		N/A	R/W	
404105	4104	0x1008	1	MAXIMUM SHUTDOWN, FMI for Event	Byte	520448 (\$7F100)		N/A	R/W	
404106	4105	0x1009	1	MAXIMUM SHUTDOWN, Delay Before	Byte	3, Voltage Above Normal		N/A	R/W	

404107	4106	0x100A	1	MAXIMUM WARNING, Event Generate	Byte	1000		N/A	R/W	
404108	4107	0x100B	1	MAXIMUM WARNING, Lamp Set by Ev	Byte			N/A	R/W	
404109	4108	0x100C	2	MAXIMUM WARNING, SPN for Event	Double	TRUE		N/A	R/W	
404111	4110	0x100E	1	MAXIMUM WARNING, FMI for Event	Byte	0 – Protect		N/A	R/W	
404112	4111	0x100F	1	MAXIMUM WARNING, Delay Before Ev	Byte			N/A	R/W	
404113	4112	0x1010	1	MINIMUM WARNING, Event Generate	Byte	520704 (\$7F200)		N/A	R/W	
404114	4113	0x1011	1	MINIMUM WARNING, Lamp Set by Ev	Byte	3, Voltage Above Normal		N/A	R/W	
404115	4114	0x1012	2	MAXIMUM WARNING, SPN for Event	Double	1000		N/A	R/W	
404117	4116	0x1014	1	MINIMUM WARNING, FMI for Event	Byte			N/A	R/W	
404118	4117	0x1015	1	MINIMUM WARNING, Delay Before Ev	Byte	TRUE		N/A	R/W	
404119	4118	0x1016	1	MINIMUM SHUTDOWN, Event Genera	Byte	0 – Protect		N/A	R/W	
404120	4119	0x1017	1	MINIMUM SHUTDOWN, Lamp Set by E	Byte	520960 (\$7F300)		N/A	R/W	
404121	4120	0x1018	2	MINIMUM SHUTDOWN, SPN for Event	Double	4, Voltage Below Normal		N/A	R/W	
404123	4122	0x101A	1	MINIMUM SHUTDOWN, FMI for Event	Byte	1000		N/A	R/W	
404124	4123	0x101B	1	MINIMUM SHUTDOWN, Delay Before	Byte			N/A	R/W	
404125	4124	0x101C	5	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Diagnostic Block #10</b>										
404130	4129	0x1021	1	Fault Detection is Enabled	Byte	FALSE		N/A	R/W	
404131	4130	0x1022	1	Function Type to Monitor	Byte	0 – Control not used		N/A	R/W	
404132	4131	0x1023	1	Function parameter to Monitor	Byte	0 – No selection		N/A	R/W	
404133	4132	0x1024	1	Enable Source	Byte	0 – Control not used		N/A	R/W	
404134	4133	0x1025	1	Enable Number	Byte	0 – No selection		N/A	R/W	
404135	4134	0x1026	1	Enable Response	Byte	0 – Enable When ON		N/A	R/W	
404136	4135	0x1027	1	Fault Detection Type	Byte	0 – Min and Max Error		N/A	R/W	
404137	4136	0x1028	2	Maximum Value for Diagnostic Data	Float	5		N/A	R/W	
404139	4138	0x102A	2	Minimum Value for Diagnostic Data	Float	0		N/A	R/W	
404141	4140	0x102C	1	Use Hysteresis When Defining Thresh	Byte	FALSE		N/A	R/W	
404142	4141	0x102D	2	Hysteresis	Float	0		N/A	R/W	
404144	4143	0x102F	1	Event Cleared only by DM11	Byte	FALSE		N/A	R/W	
404145	4144	0x1030	2	Set Limit for MAXIMUM SHUTDOWN	Float			N/A	R/W	
404147	4146	0x1032	2	Clear Limit for MAXIMUM SHUTDOWN	Float	4.8		N/A	R/W	
404149	4148	0x1034	2	Set Limit for MAXIMUM WARNING	Float	4.6		N/A	R/W	
404151	4150	0x1036	2	Clear Limit for MAXIMUM WARNING	Float	0		N/A	R/W	
404153	4152	0x1038	2	Clear Limit for MINIMUM WARNING	Float	0		N/A	R/W	
404155	4154	0x103A	2	Set Limit for MINIMUM WARNING	Float	0		N/A	R/W	
404157	4156	0x103C	2	Clear Limit for MINIMUM SHUTDOWN	Float	0		N/A	R/W	
404159	4158	0x103E	2	Set Limit for MINIMUM SHUTDOWN	Float	0.4		N/A	R/W	
404161	4160	0x1040	1	MAXIMUM SHUTDOWN, Event Genera	Byte	0.2		N/A	R/W	
404162	4161	0x1041	1	MAXIMUM SHUTDOWN, Lamp Set by	Byte	TRUE		N/A	R/W	
404163	4162	0x1042	2	MAXIMUM SHUTDOWN, SPN for Even	Double	0 – Protect		N/A	R/W	
404165	4164	0x1044	1	MAXIMUM SHUTDOWN, FMI for Even	Byte	520448 (\$7F100)		N/A	R/W	
404166	4165	0x1045	1	MAXIMUM SHUTDOWN, Delay Before	Byte	3, Voltage Above Normal		N/A	R/W	
404167	4166	0x1046	1	MAXIMUM WARNING, Event Generate	Byte	1000		N/A	R/W	
404168	4167	0x1047	1	MAXIMUM WARNING, Lamp Set by Ev	Byte			N/A	R/W	
404169	4168	0x1048	2	MAXIMUM WARNING, SPN for Event	Double	TRUE		N/A	R/W	
404171	4170	0x104A	1	MAXIMUM WARNING, FMI for Event	Byte	0 – Protect		N/A	R/W	
404172	4171	0x104B	1	MAXIMUM WARNING, Delay Before Ev	Byte			N/A	R/W	
404173	4172	0x104C	1	MINIMUM WARNING, Event Generate	Byte	520704 (\$7F200)		N/A	R/W	
404174	4173	0x104D	1	MINIMUM WARNING, Lamp Set by Ev	Byte	3, Voltage Above Normal		N/A	R/W	
404175	4174	0x104E	2	MAXIMUM WARNING, SPN for Event	Double	1000		N/A	R/W	
404177	4176	0x1050	1	MINIMUM WARNING, FMI for Event	Byte			N/A	R/W	

404178	4177	0x1051	1	MINIMUM WARNING, Delay Before Ev	Byte	TRUE		N/A	R/W	
404179	4178	0x1052	1	MINIMUM SHUTDOWN, Event Genera	Byte	0 – Protect		N/A	R/W	
404180	4179	0x1053	1	MINIMUM SHUTDOWN, Lamp Set by E	Byte	520960 (\$7F300)		N/A	R/W	
404181	4180	0x1054	2	MINIMUM SHUTDOWN, SPN for Event	Double	4, Voltage Below Normal		N/A	R/W	
404183	4182	0x1056	1	MINIMUM SHUTDOWN, FMI for Event	Byte	1000		N/A	R/W	
404184	4183	0x1057	1	MINIMUM SHUTDOWN, Delay Before	Byte			N/A	R/W	
404185	4184	0x1058	5	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Diagnostic Block #11</b>										
404190	4189	0x105D	1	Fault Detection is Enabled	Byte	FALSE		N/A	R/W	
404191	4190	0x105E	1	Function Type to Monitor	Byte	0 – Control not used		N/A	R/W	
404192	4191	0x105F	1	Function parameter to Monitor	Byte	0 – No selection		N/A	R/W	
404193	4192	0x1060	1	Enable Source	Byte	0 – Control not used		N/A	R/W	
404194	4193	0x1061	1	Enable Number	Byte	0 – No selection		N/A	R/W	
404195	4194	0x1062	1	Enable Response	Byte	0 – Enable When ON		N/A	R/W	
404196	4195	0x1063	1	Fault Detection Type	Byte	0 – Min and Max Error		N/A	R/W	
404197	4196	0x1064	2	Maximum Value for Diagnostic Data	Float	5		N/A	R/W	
404199	4198	0x1066	2	Minimum Value for Diagnostic Data	Float	0		N/A	R/W	
404201	4200	0x1068	1	Use Hysteresis When Defining Threshd	Byte	FALSE		N/A	R/W	
404202	4201	0x1069	2	Hysteresis	Float	0		N/A	R/W	
404204	4203	0x106B	1	Event Cleared only by DM11	Byte	FALSE		N/A	R/W	
404205	4204	0x106C	2	Set Limit for MAXIMUM SHUTDOWN	Float			N/A	R/W	
404207	4206	0x106E	2	Clear Limit for MAXIMUM SHUTDOWN	Float	4.8		N/A	R/W	
404209	4208	0x1070	2	Set Limit for MAXIMUM WARNING	Float	4.6		N/A	R/W	
404211	4210	0x1072	2	Clear Limit for MAXIMUM WARNING	Float	0		N/A	R/W	
404213	4212	0x1074	2	Clear Limit for MINIMUM WARNING	Float	0		N/A	R/W	
404215	4214	0x1076	2	Set Limit for MINIMUM WARNING	Float	0		N/A	R/W	
404217	4216	0x1078	2	Clear Limit for MINIMUM SHUTDOWN	Float	0		N/A	R/W	
404219	4218	0x107A	2	Set Limit for MINIMUM SHUTDOWN	Float	0.4		N/A	R/W	
404221	4220	0x107C	1	MAXIMUM SHUTDOWN, Event Genera	Byte	0.2		N/A	R/W	
404222	4221	0x107D	1	MAXIMUM SHUTDOWN, Lamp Set by	Byte	TRUE		N/A	R/W	
404223	4222	0x107E	2	MAXIMUM SHUTDOWN, SPN for Even	Double	0 – Protect		N/A	R/W	
404225	4224	0x1080	1	MAXIMUM SHUTDOWN, FMI for Even	Byte	520448 (\$7F100)		N/A	R/W	
404226	4225	0x1081	1	MAXIMUM SHUTDOWN, Delay Before	Byte	3, Voltage Above Normal		N/A	R/W	
404227	4226	0x1082	1	MAXIMUM WARNING, Event Generate	Byte	1000		N/A	R/W	
404228	4227	0x1083	1	MAXIMUM WARNING, Lamp Set by Ev	Byte			N/A	R/W	
404229	4228	0x1084	2	MAXIMUM WARNING, SPN for Event	Double	TRUE		N/A	R/W	
404231	4230	0x1086	1	MAXIMUM WARNING, FMI for Event	Byte	0 – Protect		N/A	R/W	
404232	4231	0x1087	1	MAXIMUM WARNING, Delay Before Ev	Byte			N/A	R/W	
404233	4232	0x1088	1	MINIMUM WARNING, Event Generate	Byte	520704 (\$7F200)		N/A	R/W	
404234	4233	0x1089	1	MINIMUM WARNING, Lamp Set by Eve	Byte	3, Voltage Above Normal		N/A	R/W	
404235	4234	0x108A	2	MAXIMUM WARNING, SPN for Event	Double	1000		N/A	R/W	
404237	4236	0x108C	1	MINIMUM WARNING, FMI for Event	Byte			N/A	R/W	
404238	4237	0x108D	1	MINIMUM WARNING, Delay Before Ev	Byte	TRUE		N/A	R/W	
404239	4238	0x108E	1	MINIMUM SHUTDOWN, Event Genera	Byte	0 – Protect		N/A	R/W	
404240	4239	0x108F	1	MINIMUM SHUTDOWN, Lamp Set by E	Byte	520960 (\$7F300)		N/A	R/W	
404241	4240	0x1090	2	MINIMUM SHUTDOWN, SPN for Event	Double	4, Voltage Below Normal		N/A	R/W	
404243	4242	0x1092	1	MINIMUM SHUTDOWN, FMI for Event	Byte	1000		N/A	R/W	
404244	4243	0x1093	1	MINIMUM SHUTDOWN, Delay Before	Byte			N/A	R/W	
404245	4244	0x1094	5	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Diagnostic Block #12</b>										
404250	4249	0x1099	1	Fault Detection is Enabled	Byte	FALSE		N/A	R/W	

404251	4250	0x109A	1	Function Type to Monitor	Byte	0 – Control not used	N/A	R/W	
404252	4251	0x109B	1	Function parameter to Monitor	Byte	0 – No selection	N/A	R/W	
404253	4252	0x109C	1	Enable Source	Byte	0 – Control not used	N/A	R/W	
404254	4253	0x109D	1	Enable Number	Byte	0 – No selection	N/A	R/W	
404255	4254	0x109E	1	Enable Response	Byte	0 – Enable When ON	N/A	R/W	
404256	4255	0x109F	1	Fault Detection Type	Byte	0 – Min and Max Error	N/A	R/W	
404257	4256	0x10A0	2	Maximum Value for Diagnostic Data	Float	5	N/A	R/W	
404259	4258	0x10A2	2	Minimum Value for Diagnostic Data	Float	0	N/A	R/W	
404261	4260	0x10A4	1	Use Hysteresis When Defining Threshc	Byte	FALSE	N/A	R/W	
404262	4261	0x10A5	2	Hysteresis	Float	0	N/A	R/W	
404264	4263	0x10A7	1	Event Cleared only by DM11	Byte	FALSE	N/A	R/W	
404265	4264	0x10A8	2	Set Limit for MAXIMUM SHUTDOWN	Float		N/A	R/W	
404267	4266	0x10AA	2	Clear Limit for MAXIMUM SHUTDOWN	Float	4.8	N/A	R/W	
404269	4268	0x10AC	2	Set Limit for MAXIMUM WARNING	Float	4.6	N/A	R/W	
404271	4270	0x10AE	2	Clear Limit for MAXIMUM WARNING	Float	0	N/A	R/W	
404273	4272	0x10B0	2	Clear Limit for MINIMUM WARNING	Float	0	N/A	R/W	
404275	4274	0x10B2	2	Set Limit for MINIMUM WARNING	Float	0	N/A	R/W	
404277	4276	0x10B4	2	Clear Limit for MINIMUM SHUTDOWN	Float	0	N/A	R/W	
404279	4278	0x10B6	2	Set Limit for MINIMUM SHUTDOWN	Float	0.4	N/A	R/W	
404281	4280	0x10B8	1	MAXIMUM SHUTDOWN, Event Genera	Byte	0.2	N/A	R/W	
404282	4281	0x10B9	1	MAXIMUM SHUTDOWN, Lamp Set by	Byte	TRUE	N/A	R/W	
404283	4282	0x10BA	2	MAXIMUM SHUTDOWN, SPN for Even	Double	0 – Protect	N/A	R/W	
404285	4284	0x10BC	1	MAXIMUM SHUTDOWN, FMI for Even	Byte	520448 (\$7F100)	N/A	R/W	
404286	4285	0x10BD	1	MAXIMUM SHUTDOWN, Delay Before	Byte	3, Voltage Above Normal	N/A	R/W	
404287	4286	0x10BE	1	MAXIMUM WARNING, Event Genera	Byte	1000	N/A	R/W	
404288	4287	0x10BF	1	MAXIMUM WARNING, Lamp Set by Ev	Byte		N/A	R/W	
404289	4288	0x10C0	2	MAXIMUM WARNING, SPN for Event	Double	TRUE	N/A	R/W	
404291	4290	0x10C2	1	MAXIMUM WARNING, FMI for Event	Byte	0 – Protect	N/A	R/W	
404292	4291	0x10C3	1	MAXIMUM WARNING, Delay Before Ev	Byte		N/A	R/W	
404293	4292	0x10C4	1	MINIMUM WARNING, Event Genera	Byte	520704 (\$7F200)	N/A	R/W	
404294	4293	0x10C5	1	MINIMUM WARNING, Lamp Set by Ev	Byte	3, Voltage Above Normal	N/A	R/W	
404295	4294	0x10C6	2	MAXIMUM WARNING, SPN for Event	Double	1000	N/A	R/W	
404297	4296	0x10C8	1	MINIMUM WARNING, FMI for Event	Byte		N/A	R/W	
404298	4297	0x10C9	1	MINIMUM WARNING, Delay Before Ev	Byte	TRUE	N/A	R/W	
404299	4298	0x10CA	1	MINIMUM SHUTDOWN, Event Genera	Byte	0 – Protect	N/A	R/W	
404300	4299	0x10CB	1	MINIMUM SHUTDOWN, Lamp Set by B	Byte	520960 (\$7F300)	N/A	R/W	
404301	4300	0x10CC	2	MINIMUM SHUTDOWN, SPN for Event	Double	4, Voltage Below Normal	N/A	R/W	
404303	4302	0x10CE	1	MINIMUM SHUTDOWN, FMI for Event	Byte	1000	N/A	R/W	
404304	4303	0x10CF	1	MINIMUM SHUTDOWN, Delay Before	Byte		N/A	R/W	
404305	4304	0x10D0	5	Reserved	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Diagnostic Block #13</b>									
404310	4309	0x10D5	1	Fault Detection is Enabled	Byte	FALSE	N/A	R/W	
404311	4310	0x10D6	1	Function Type to Monitor	Byte	0 – Control not used	N/A	R/W	
404312	4311	0x10D7	1	Function parameter to Monitor	Byte	0 – No selection	N/A	R/W	
404313	4312	0x10D8	1	Enable Source	Byte	0 – Control not used	N/A	R/W	
404314	4313	0x10D9	1	Enable Number	Byte	0 – No selection	N/A	R/W	
404315	4314	0x10DA	1	Enable Response	Byte	0 – Enable When ON	N/A	R/W	
404316	4315	0x10DB	1	Fault Detection Type	Byte	0 – Min and Max Error	N/A	R/W	
404317	4316	0x10DC	2	Maximum Value for Diagnostic Data	Float	5	N/A	R/W	
404319	4318	0x10DE	2	Minimum Value for Diagnostic Data	Float	0	N/A	R/W	
404321	4320	0x10E0	1	Use Hysteresis When Defining Threshc	Byte	FALSE	N/A	R/W	

404322	4321	0x10E1	2	Hysteresis	Float	0		N/A	R/W	
404324	4323	0x10E3	1	Event Cleared only by DM11	Byte	FALSE		N/A	R/W	
404325	4324	0x10E4	2	Set Limit for MAXIMUM SHUTDOWN	Float			N/A	R/W	
404327	4326	0x10E6	2	Clear Limit for MAXIMUM SHUTDOWN	Float	4.8		N/A	R/W	
404329	4328	0x10E8	2	Set Limit for MAXIMUM WARNING	Float	4.6		N/A	R/W	
404331	4330	0x10EA	2	Clear Limit for MAXIMUM WARNING	Float	0		N/A	R/W	
404333	4332	0x10EC	2	Clear Limit for MINIMUM WARNING	Float	0		N/A	R/W	
404335	4334	0x10EE	2	Set Limit for MINIMUM WARNING	Float	0		N/A	R/W	
404337	4336	0x10F0	2	Clear Limit for MINIMUM SHUTDOWN	Float	0		N/A	R/W	
404339	4338	0x10F2	2	Set Limit for MINIMUM SHUTDOWN	Float	0.4		N/A	R/W	
404341	4340	0x10F4	1	MAXIMUM SHUTDOWN, Event Generate	Byte	0.2		N/A	R/W	
404342	4341	0x10F5	1	MAXIMUM SHUTDOWN, Lamp Set by	Byte	TRUE		N/A	R/W	
404343	4342	0x10F6	2	MAXIMUM SHUTDOWN, SPN for Event	Double	0 – Protect		N/A	R/W	
404345	4344	0x10F8	1	MAXIMUM SHUTDOWN, FMI for Event	Byte	520448 (\$7F100)		N/A	R/W	
404346	4345	0x10F9	1	MAXIMUM SHUTDOWN, Delay Before	Byte	3, Voltage Above Normal		N/A	R/W	
404347	4346	0x10FA	1	MAXIMUM WARNING, Event Generate	Byte	1000		N/A	R/W	
404348	4347	0x10FB	1	MAXIMUM WARNING, Lamp Set by Ev	Byte			N/A	R/W	
404349	4348	0x10FC	2	MAXIMUM WARNING, SPN for Event	Double	TRUE		N/A	R/W	
404351	4350	0x10FE	1	MAXIMUM WARNING, FMI for Event	Byte	0 – Protect		N/A	R/W	
404352	4351	0x10FF	1	MAXIMUM WARNING, Delay Before Ev	Byte			N/A	R/W	
404353	4352	0x1100	1	MINIMUM WARNING, Event Generate	Byte	520704 (\$7F200)		N/A	R/W	
404354	4353	0x1101	1	MINIMUM WARNING, Lamp Set by Ev	Byte	3, Voltage Above Normal		N/A	R/W	
404355	4354	0x1102	2	MAXIMUM WARNING, SPN for Event	Double	1000		N/A	R/W	
404357	4356	0x1104	1	MINIMUM WARNING, FMI for Event	Byte			N/A	R/W	
404358	4357	0x1105	1	MINIMUM WARNING, Delay Before Ev	Byte	TRUE		N/A	R/W	
404359	4358	0x1106	1	MINIMUM SHUTDOWN, Event Generate	Byte	0 – Protect		N/A	R/W	
404360	4359	0x1107	1	MINIMUM SHUTDOWN, Lamp Set by E	Byte	520960 (\$7F300)		N/A	R/W	
404361	4360	0x1108	2	MINIMUM SHUTDOWN, SPN for Event	Double	4, Voltage Below Normal		N/A	R/W	
404363	4362	0x110A	1	MINIMUM SHUTDOWN, FMI for Event	Byte	1000		N/A	R/W	
404364	4363	0x110B	1	MINIMUM SHUTDOWN, Delay Before	Byte			N/A	R/W	
404365	4364	0x110C	5	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Diagnostic Block #14</b>										
404370	4369	0x1111	1	Fault Detection is Enabled	Byte	FALSE		N/A	R/W	
404371	4370	0x1112	1	Function Type to Monitor	Byte	0 – Control not used		N/A	R/W	
404372	4371	0x1113	1	Function parameter to Monitor	Byte	0 – No selection		N/A	R/W	
404373	4372	0x1114	1	Enable Source	Byte	0 – Control not used		N/A	R/W	
404374	4373	0x1115	1	Enable Number	Byte	0 – No selection		N/A	R/W	
404375	4374	0x1116	1	Enable Response	Byte	0 – Enable When ON		N/A	R/W	
404376	4375	0x1117	1	Fault Detection Type	Byte	0 – Min and Max Error		N/A	R/W	
404377	4376	0x1118	2	Maximum Value for Diagnostic Data	Float	5		N/A	R/W	
404379	4378	0x111A	2	Minimum Value for Diagnostic Data	Float	0		N/A	R/W	
404381	4380	0x111C	1	Use Hysteresis When Defining Thresh	Byte	FALSE		N/A	R/W	
404382	4381	0x111D	2	Hysteresis	Float	0		N/A	R/W	
404384	4383	0x111F	1	Event Cleared only by DM11	Byte	FALSE		N/A	R/W	
404385	4384	0x1120	2	Set Limit for MAXIMUM SHUTDOWN	Float			N/A	R/W	
404387	4386	0x1122	2	Clear Limit for MAXIMUM SHUTDOWN	Float	4.8		N/A	R/W	
404389	4388	0x1124	2	Set Limit for MAXIMUM WARNING	Float	4.6		N/A	R/W	
404391	4390	0x1126	2	Clear Limit for MAXIMUM WARNING	Float	0		N/A	R/W	
404393	4392	0x1128	2	Clear Limit for MINIMUM WARNING	Float	0		N/A	R/W	
404395	4394	0x112A	2	Set Limit for MINIMUM WARNING	Float	0		N/A	R/W	
404397	4396	0x112C	2	Clear Limit for MINIMUM SHUTDOWN	Float	0		N/A	R/W	



404399	4398	0x112E	2	Set Limit for MINIMUM SHUTDOWN	Float	0.4		N/A	R/W	
404401	4400	0x1130	1	MAXIMUM SHUTDOWN, Event Generate	Byte	0.2		N/A	R/W	
404402	4401	0x1131	1	MAXIMUM SHUTDOWN, Lamp Set by	Byte	TRUE		N/A	R/W	
404403	4402	0x1132	2	MAXIMUM SHUTDOWN, SPN for Event	Double	0 – Protect		N/A	R/W	
404405	4404	0x1134	1	MAXIMUM SHUTDOWN, FMI for Event	Byte	520448 (\$7F100)		N/A	R/W	
404406	4405	0x1135	1	MAXIMUM SHUTDOWN, Delay Before	Byte	3, Voltage Above Normal		N/A	R/W	
404407	4406	0x1136	1	MAXIMUM WARNING, Event Generate	Byte	1000		N/A	R/W	
404408	4407	0x1137	1	MAXIMUM WARNING, Lamp Set by Ev	Byte			N/A	R/W	
404409	4408	0x1138	2	MAXIMUM WARNING, SPN for Event	Double	TRUE		N/A	R/W	
404411	4410	0x113A	1	MAXIMUM WARNING, FMI for Event	Byte	0 – Protect		N/A	R/W	
404412	4411	0x113B	1	MAXIMUM WARNING, Delay Before Ev	Byte			N/A	R/W	
404413	4412	0x113C	1	MINIMUM WARNING, Event Generate	Byte	520704 (\$7F200)		N/A	R/W	
404414	4413	0x113D	1	MINIMUM WARNING, Lamp Set by Ev	Byte	3, Voltage Above Normal		N/A	R/W	
404415	4414	0x113E	2	MAXIMUM WARNING, SPN for Event	Double	1000		N/A	R/W	
404417	4416	0x1140	1	MINIMUM WARNING, FMI for Event	Byte			N/A	R/W	
404418	4417	0x1141	1	MINIMUM WARNING, Delay Before Ev	Byte	TRUE		N/A	R/W	
404419	4418	0x1142	1	MINIMUM SHUTDOWN, Event Genera	Byte	0 – Protect		N/A	R/W	
404420	4419	0x1143	1	MINIMUM SHUTDOWN, Lamp Set by E	Byte	520960 (\$7F300)		N/A	R/W	
404421	4420	0x1144	2	MINIMUM SHUTDOWN, SPN for Event	Double	4, Voltage Below Normal		N/A	R/W	
404423	4422	0x1146	1	MINIMUM SHUTDOWN, FMI for Event	Byte	1000		N/A	R/W	
404424	4423	0x1147	1	MINIMUM SHUTDOWN, Delay Before	Byte			N/A	R/W	
404425	4424	0x1148	5	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Diagnostic Block #15</b>										
404430	4429	0x114D	1	Fault Detection is Enabled	Byte	FALSE		N/A	R/W	
404431	4430	0x114E	1	Function Type to Monitor	Byte	0 – Control not used		N/A	R/W	
404432	4431	0x114F	1	Function parameter to Monitor	Byte	0 – No selection		N/A	R/W	
404433	4432	0x1150	1	Enable Source	Byte	0 – Control not used		N/A	R/W	
404434	4433	0x1151	1	Enable Number	Byte	0 – No selection		N/A	R/W	
404435	4434	0x1152	1	Enable Response	Byte	0 – Enable When ON		N/A	R/W	
404436	4435	0x1153	1	Fault Detection Type	Byte	0 – Min and Max Error		N/A	R/W	
404437	4436	0x1154	2	Maximum Value for Diagnostic Data	Float	5		N/A	R/W	
404439	4438	0x1156	2	Minimum Value for Diagnostic Data	Float	0		N/A	R/W	
404441	4440	0x1158	1	Use Hysteresis When Defining Threshd	Byte	FALSE		N/A	R/W	
404442	4441	0x1159	2	Hysteresis	Float	0		N/A	R/W	
404444	4443	0x115B	1	Event Cleared only by DM11	Byte	FALSE		N/A	R/W	
404445	4444	0x115C	2	Set Limit for MAXIMUM SHUTDOWN	Float			N/A	R/W	
404447	4446	0x115E	2	Clear Limit for MAXIMUM SHUTDOWN	Float	4.8		N/A	R/W	
404449	4448	0x1160	2	Set Limit for MAXIMUM WARNING	Float	4.6		N/A	R/W	
404451	4450	0x1162	2	Clear Limit for MAXIMUM WARNING	Float	0		N/A	R/W	
404453	4452	0x1164	2	Clear Limit for MINIMUM WARNING	Float	0		N/A	R/W	
404455	4454	0x1166	2	Set Limit for MINIMUM WARNING	Float	0		N/A	R/W	
404457	4456	0x1168	2	Clear Limit for MINIMUM SHUTDOWN	Float	0		N/A	R/W	
404459	4458	0x116A	2	Set Limit for MINIMUM SHUTDOWN	Float	0.4		N/A	R/W	
404461	4460	0x116C	1	MAXIMUM SHUTDOWN, Event Genera	Byte	0.2		N/A	R/W	
404462	4461	0x116D	1	MAXIMUM SHUTDOWN, Lamp Set by	Byte	TRUE		N/A	R/W	
404463	4462	0x116E	2	MAXIMUM SHUTDOWN, SPN for Even	Double	0 – Protect		N/A	R/W	
404465	4464	0x1170	1	MAXIMUM SHUTDOWN, FMI for Even	Byte	520448 (\$7F100)		N/A	R/W	
404466	4465	0x1171	1	MAXIMUM SHUTDOWN, Delay Before	Byte	3, Voltage Above Normal		N/A	R/W	
404467	4466	0x1172	1	MAXIMUM WARNING, Event Generate	Byte	1000		N/A	R/W	
404468	4467	0x1173	1	MAXIMUM WARNING, Lamp Set by Ev	Byte			N/A	R/W	
404469	4468	0x1174	2	MAXIMUM WARNING, SPN for Event	Double	TRUE		N/A	R/W	

404471	4470	0x1176	1	MAXIMUM WARNING, FMI for Event	Byte	0 – Protect		N/A	R/W	
404472	4471	0x1177	1	MAXIMUM WARNING, Delay Before Ev	Byte			N/A	R/W	
404473	4472	0x1178	1	MINIMUM WARNING, Event Generate	Byte	520704 (\$7F200)		N/A	R/W	
404474	4473	0x1179	1	MINIMUM WARNING, Lamp Set by Ev	Byte	3, Voltage Above Normal		N/A	R/W	
404475	4474	0x117A	2	MAXIMUM WARNING, SPN for Event	Double	1000		N/A	R/W	
404477	4476	0x117C	1	MINIMUM WARNING, FMI for Event	Byte			N/A	R/W	
404478	4477	0x117D	1	MINIMUM WARNING, Delay Before Ev	Byte	TRUE		N/A	R/W	
404479	4478	0x117E	1	MINIMUM SHUTDOWN, Event Genera	Byte	0 – Protect		N/A	R/W	
404480	4479	0x117F	1	MINIMUM SHUTDOWN, Lamp Set by E	Byte	520960 (\$7F300)		N/A	R/W	
404481	4480	0x1180	2	MINIMUM SHUTDOWN, SPN for Event	Double	4, Voltage Below Normal		N/A	R/W	
404483	4482	0x1182	1	MINIMUM SHUTDOWN, FMI for Event	Byte	1000		N/A	R/W	
404484	4483	0x1183	1	MINIMUM SHUTDOWN, Delay Before	Byte			N/A	R/W	
404485	4484	0x1184	5	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Diagnostic Block #16</b>										
404490	4489	0x1189	1	Fault Detection is Enabled	Byte	FALSE		N/A	R/W	
404491	4490	0x118A	1	Function Type to Monitor	Byte	0 – Control not used		N/A	R/W	
404492	4491	0x118B	1	Function parameter to Monitor	Byte	0 – No selection		N/A	R/W	
404493	4492	0x118C	1	Enable Source	Byte	0 – Control not used		N/A	R/W	
404494	4493	0x118D	1	Enable Number	Byte	0 – No selection		N/A	R/W	
404495	4494	0x118E	1	Enable Response	Byte	0 – Enable When ON		N/A	R/W	
404496	4495	0x118F	1	Fault Detection Type	Byte	0 – Min and Max Error		N/A	R/W	
404497	4496	0x1190	2	Maximum Value for Diagnostic Data	Float	5		N/A	R/W	
404499	4498	0x1192	2	Minimum Value for Diagnostic Data	Float	0		N/A	R/W	
404501	4500	0x1194	1	Use Hysteresis When Defining Thresh	Byte	FALSE		N/A	R/W	
404502	4501	0x1195	2	Hysteresis	Float	0		N/A	R/W	
404504	4503	0x1197	1	Event Cleared only by DM11	Byte	FALSE		N/A	R/W	
404505	4504	0x1198	2	Set Limit for MAXIMUM SHUTDOWN	Float			N/A	R/W	
404507	4506	0x119A	2	Clear Limit for MAXIMUM SHUTDOWN	Float	4.8		N/A	R/W	
404509	4508	0x119C	2	Set Limit for MAXIMUM WARNING	Float	4.6		N/A	R/W	
404511	4510	0x119E	2	Clear Limit for MAXIMUM WARNING	Float	0		N/A	R/W	
404513	4512	0x11A0	2	Clear Limit for MINIMUM WARNING	Float	0		N/A	R/W	
404515	4514	0x11A2	2	Set Limit for MINIMUM WARNING	Float	0		N/A	R/W	
404517	4516	0x11A4	2	Clear Limit for MINIMUM SHUTDOWN	Float	0		N/A	R/W	
404519	4518	0x11A6	2	Set Limit for MINIMUM SHUTDOWN	Float	0.4		N/A	R/W	
404521	4520	0x11A8	1	MAXIMUM SHUTDOWN, Event Genera	Byte	0.2		N/A	R/W	
404522	4521	0x11A9	1	MAXIMUM SHUTDOWN, Lamp Set by	Byte	TRUE		N/A	R/W	
404523	4522	0x11AA	2	MAXIMUM SHUTDOWN, SPN for Even	Double	0 – Protect		N/A	R/W	
404525	4524	0x11AC	1	MAXIMUM SHUTDOWN, FMI for Event	Byte	520448 (\$7F100)		N/A	R/W	
404526	4525	0x11AD	1	MAXIMUM SHUTDOWN, Delay Before	Byte	3, Voltage Above Normal		N/A	R/W	
404527	4526	0x11AE	1	MAXIMUM WARNING, Event Generate	Byte	1000		N/A	R/W	
404528	4527	0x11AF	1	MAXIMUM WARNING, Lamp Set by Ev	Byte			N/A	R/W	
404529	4528	0x11B0	2	MAXIMUM WARNING, SPN for Event	Double	TRUE		N/A	R/W	
404531	4530	0x11B2	1	MAXIMUM WARNING, FMI for Event	Byte	0 – Protect		N/A	R/W	
404532	4531	0x11B3	1	MAXIMUM WARNING, Delay Before Ev	Byte			N/A	R/W	
404533	4532	0x11B4	1	MINIMUM WARNING, Event Generate	Byte	520704 (\$7F200)		N/A	R/W	
404534	4533	0x11B5	1	MINIMUM WARNING, Lamp Set by Ev	Byte	3, Voltage Above Normal		N/A	R/W	
404535	4534	0x11B6	2	MAXIMUM WARNING, SPN for Event	Double	1000		N/A	R/W	
404537	4536	0x11B8	1	MINIMUM WARNING, FMI for Event	Byte			N/A	R/W	
404538	4537	0x11B9	1	MINIMUM WARNING, Delay Before Ev	Byte	TRUE		N/A	R/W	
404539	4538	0x11BA	1	MINIMUM SHUTDOWN, Event Genera	Byte	0 – Protect		N/A	R/W	
404540	4539	0x11BB	1	MINIMUM SHUTDOWN, Lamp Set by E	Byte	520960 (\$7F300)		N/A	R/W	

404541	4540	0x11BC	2	MINIMUM SHUTDOWN, SPN for Event	Double	4, Voltage Below Normal	N/A	R/W	
404543	4542	0x11BE	1	MINIMUM SHUTDOWN, FMI for Event	Byte	1000	N/A	R/W	
404544	4543	0x11BF	1	MINIMUM SHUTDOWN, Delay Before	Byte		N/A	R/W	
404545	4544	0x11C0	5	Reserved	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>DTC React #1</b>									
404550	4549	0x11C5	1	DTC React is Enabled	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W
404551	4550	0x11C6	2	SPN to Trigger Reaction	Double	0	0x0-0x3FFFF	N/A	R/W
404553	4552	0x11C8	1	FMI to Trigger Reaction	Byte	0	0-31	N/A	R/W
404554	4553	0x11C9	1	Lamp Used to Trigger Reaction	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W
404555	4554	0x11CA	1	Lamp to Trigger Reaction	Byte	0	0...3	N/A	R/W
404556	4555	0x11CB	1	Source Address Used to Trigger Reaction	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W
404557	4556	0x11CC	1	Source Address to Trigger Reaction	Byte	0	0x0 - 0xFE	N/A	R/W
404558	4557	0x11CD	7	Reserved	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>DTC React #2</b>									
404565	4564	0x11D4	1	DTC React is Enabled	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W
404566	4565	0x11D5	2	SPN to Trigger Reaction	Double	0	0x0-0x3FFFF	N/A	R/W
404568	4567	0x11D7	1	FMI to Trigger Reaction	Byte	0	0-31	N/A	R/W
404569	4568	0x11D8	1	Lamp Used to Trigger Reaction	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W
404570	4569	0x11D9	1	Lamp to Trigger Reaction	Byte	0	0...3	N/A	R/W
404571	4570	0x11DA	1	Source Address Used to Trigger Reaction	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W
404572	4571	0x11DB	1	Source Address to Trigger Reaction	Byte	0	0x0 - 0xFE	N/A	R/W
404573	4572	0x11DC	7	Reserved	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>DTC React #3</b>									
404580	4579	0x11E3	1	DTC React is Enabled	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W
404581	4580	0x11E4	2	SPN to Trigger Reaction	Double	0	0x0-0x3FFFF	N/A	R/W
404583	4582	0x11E6	1	FMI to Trigger Reaction	Byte	0	0-31	N/A	R/W
404584	4583	0x11E7	1	Lamp Used to Trigger Reaction	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W
404585	4584	0x11E8	1	Lamp to Trigger Reaction	Byte	0	0...3	N/A	R/W
404586	4585	0x11E9	1	Source Address Used to Trigger Reaction	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W
404587	4586	0x11EA	1	Source Address to Trigger Reaction	Byte	0	0x0 - 0xFE	N/A	R/W
404588	4587	0x11EB	7	Reserved	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>DTC React #4</b>									
404595	4594	0x11F2	1	DTC React is Enabled	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W
404596	4595	0x11F3	2	SPN to Trigger Reaction	Double	0	0x0-0x3FFFF	N/A	R/W
404598	4597	0x11F5	1	FMI to Trigger Reaction	Byte	0	0-31	N/A	R/W
404599	4598	0x11F6	1	Lamp Used to Trigger Reaction	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W
404600	4599	0x11F7	1	Lamp to Trigger Reaction	Byte	0	0...3	N/A	R/W
404601	4600	0x11F8	1	Source Address Used to Trigger Reaction	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W
404602	4601	0x11F9	1	Source Address to Trigger Reaction	Byte	0	0x0 - 0xFE	N/A	R/W
404603	4602	0x11FA	7	Reserved	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>DTC React #5</b>									
404610	4609	0x1201	1	DTC React is Enabled	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W
404611	4610	0x1202	2	SPN to Trigger Reaction	Double	0	0x0-0x3FFFF	N/A	R/W
404613	4612	0x1204	1	FMI to Trigger Reaction	Byte	0	0-31	N/A	R/W
404614	4613	0x1205	1	Lamp Used to Trigger Reaction	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W
404615	4614	0x1206	1	Lamp to Trigger Reaction	Byte	0	0...3	N/A	R/W
404616	4615	0x1207	1	Source Address Used to Trigger Reaction	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W
404617	4616	0x1208	1	Source Address to Trigger Reaction	Byte	0	0x0 - 0xFE	N/A	R/W
404618	4617	0x1209	7	Reserved	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>DTC React #6</b>									
404625	4624	0x1210	1	DTC React is Enabled	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W

404626	4625	0x1211	2	SPN to Trigger Reaction	Double	0	0x0-0x3FFFF	N/A	R/W	
404628	4627	0x1213	1	FMI to Trigger Reaction	Byte	0	0-31	N/A	R/W	
404629	4628	0x1214	1	Lamp Used to Trigger Reaction	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	
404630	4629	0x1215	1	Lamp to Trigger Reaction	Byte	0	0..3	N/A	R/W	
404631	4630	0x1216	1	Source Address Used to Trigger Reaction	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	
404632	4631	0x1217	1	Source Address to Trigger Reaction	Byte	0	0x0 - 0xFE	N/A	R/W	
404633	4632	0x1218	7	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>DTC React #7</b>										
404640	4639	0x121F	1	DTC React is Enabled	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	
404641	4640	0x1220	2	SPN to Trigger Reaction	Double	0	0x0-0x3FFFF	N/A	R/W	
404643	4642	0x1222	1	FMI to Trigger Reaction	Byte	0	0-31	N/A	R/W	
404644	4643	0x1223	1	Lamp Used to Trigger Reaction	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	
404645	4644	0x1224	1	Lamp to Trigger Reaction	Byte	0	0..3	N/A	R/W	
404646	4645	0x1225	1	Source Address Used to Trigger Reaction	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	
404647	4646	0x1226	1	Source Address to Trigger Reaction	Byte	0	0x0 - 0xFE	N/A	R/W	
404648	4647	0x1227	7	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>DTC React #8</b>										
404655	4654	0x122E	1	DTC React is Enabled	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	
404656	4655	0x122F	2	SPN to Trigger Reaction	Double	0	0x0-0x3FFFF	N/A	R/W	
404658	4657	0x1231	1	FMI to Trigger Reaction	Byte	0	0-31	N/A	R/W	
404659	4658	0x1232	1	Lamp Used to Trigger Reaction	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	
404660	4659	0x1233	1	Lamp to Trigger Reaction	Byte	0	0..3	N/A	R/W	
404661	4660	0x1234	1	Source Address Used to Trigger Reaction	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	
404662	4661	0x1235	1	Source Address to Trigger Reaction	Byte	0	0x0 - 0xFE	N/A	R/W	
404663	4662	0x1236	7	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>DTC React #9</b>										
404670	4669	0x123D	1	DTC React is Enabled	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	
404671	4670	0x123E	2	SPN to Trigger Reaction	Double	0	0x0-0x3FFFF	N/A	R/W	
404673	4672	0x1240	1	FMI to Trigger Reaction	Byte	0	0-31	N/A	R/W	
404674	4673	0x1241	1	Lamp Used to Trigger Reaction	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	
404675	4674	0x1242	1	Lamp to Trigger Reaction	Byte	0	0..3	N/A	R/W	
404676	4675	0x1243	1	Source Address Used to Trigger Reaction	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	
404677	4676	0x1244	1	Source Address to Trigger Reaction	Byte	0	0x0 - 0xFE	N/A	R/W	
404678	4677	0x1245	7	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>DTC React #10</b>										
404685	4684	0x124C	1	DTC React is Enabled	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	
404686	4685	0x124D	2	SPN to Trigger Reaction	Double	0	0x0-0x3FFFF	N/A	R/W	
404688	4687	0x124F	1	FMI to Trigger Reaction	Byte	0	0-31	N/A	R/W	
404689	4688	0x1250	1	Lamp Used to Trigger Reaction	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	
404690	4689	0x1251	1	Lamp to Trigger Reaction	Byte	0	0..3	N/A	R/W	
404691	4690	0x1252	1	Source Address Used to Trigger Reaction	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	
404692	4691	0x1253	1	Source Address to Trigger Reaction	Byte	0	0x0 - 0xFE	N/A	R/W	
404693	4692	0x1254	7	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>DTC React #11</b>										
404700	4699	0x125B	1	DTC React is Enabled	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	
404701	4700	0x125C	2	SPN to Trigger Reaction	Double	0	0x0-0x3FFFF	N/A	R/W	
404703	4702	0x125E	1	FMI to Trigger Reaction	Byte	0	0-31	N/A	R/W	
404704	4703	0x125F	1	Lamp Used to Trigger Reaction	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	
404705	4704	0x1260	1	Lamp to Trigger Reaction	Byte	0	0..3	N/A	R/W	
404706	4705	0x1261	1	Source Address Used to Trigger Reaction	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	
404707	4706	0x1262	1	Source Address to Trigger Reaction	Byte	0	0x0 - 0xFE	N/A	R/W	

404708	4707	0x1263	7	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>DTC React #12</b>										
404715	4714	0x126A	1	DTC React is Enabled	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	
404716	4715	0x126B	2	SPN to Trigger Reaction	Double	0	0x0-0x3FFFF	N/A	R/W	
404718	4717	0x126D	1	FMI to Trigger Reaction	Byte	0	0-31	N/A	R/W	
404719	4718	0x126E	1	Lamp Used to Trigger Reaction	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	
404720	4719	0x126F	1	Lamp to Trigger Reaction	Byte	0	0...3	N/A	R/W	
404721	4720	0x1270	1	Source Address Used to Trigger Reaction	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	
404722	4721	0x1271	1	Source Address to Trigger Reaction	Byte	0	0x0 - 0xFE	N/A	R/W	
404723	4722	0x1272	7	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>DTC React #13</b>										
404730	4729	0x1279	1	DTC React is Enabled	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	
404731	4730	0x127A	2	SPN to Trigger Reaction	Double	0	0x0-0x3FFFF	N/A	R/W	
404733	4732	0x127C	1	FMI to Trigger Reaction	Byte	0	0-31	N/A	R/W	
404734	4733	0x127D	1	Lamp Used to Trigger Reaction	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	
404735	4734	0x127E	1	Lamp to Trigger Reaction	Byte	0	0...3	N/A	R/W	
404736	4735	0x127F	1	Source Address Used to Trigger Reaction	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	
404737	4736	0x1280	1	Source Address to Trigger Reaction	Byte	0	0x0 - 0xFE	N/A	R/W	
404738	4737	0x1281	7	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>DTC React #14</b>										
404745	4744	0x1288	1	DTC React is Enabled	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	
404746	4745	0x1289	2	SPN to Trigger Reaction	Double	0	0x0-0x3FFFF	N/A	R/W	
404748	4747	0x128B	1	FMI to Trigger Reaction	Byte	0	0-31	N/A	R/W	
404749	4748	0x128C	1	Lamp Used to Trigger Reaction	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	
404750	4749	0x128D	1	Lamp to Trigger Reaction	Byte	0	0...3	N/A	R/W	
404751	4750	0x128E	1	Source Address Used to Trigger Reaction	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	
404752	4751	0x128F	1	Source Address to Trigger Reaction	Byte	0	0x0 - 0xFE	N/A	R/W	
404753	4752	0x1290	7	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>DTC React #15</b>										
404760	4759	0x1297	1	DTC React is Enabled	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	
404761	4760	0x1298	2	SPN to Trigger Reaction	Double	0	0x0-0x3FFFF	N/A	R/W	
404763	4762	0x129A	1	FMI to Trigger Reaction	Byte	0	0-31	N/A	R/W	
404764	4763	0x129B	1	Lamp Used to Trigger Reaction	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	
404765	4764	0x129C	1	Lamp to Trigger Reaction	Byte	0	0...3	N/A	R/W	
404766	4765	0x129D	1	Source Address Used to Trigger Reaction	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	
404767	4766	0x129E	1	Source Address to Trigger Reaction	Byte	0	0x0 - 0xFE	N/A	R/W	
404768	4767	0x129F	7	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>DTC React #16</b>										
404775	4774	0x12A6	1	DTC React is Enabled	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	
404776	4775	0x12A7	2	SPN to Trigger Reaction	Double	0	0x0-0x3FFFF	N/A	R/W	
404778	4777	0x12A9	1	FMI to Trigger Reaction	Byte	0	0-31	N/A	R/W	
404779	4778	0x12AA	1	Lamp Used to Trigger Reaction	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	
404780	4779	0x12AB	1	Lamp to Trigger Reaction	Byte	0	0...3	N/A	R/W	
404781	4780	0x12AC	1	Source Address Used to Trigger Reaction	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	
404782	4781	0x12AD	1	Source Address to Trigger Reaction	Byte	0	0x0 - 0xFE	N/A	R/W	
404783	4782	0x12AE	7	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>J1939 Network</b>										
404790	4789	0x0400	1	ECU Instance Number	Byte	0 - Instance #1	0...70 - Instance #1,...7 - Instance #70	N/A	R/W	ECU Instance field of the J1939 ECU Name
404791	4790	0x12B6	1	ECU Address	Byte	128	0...253	N/A	R/W	J1939 ECU address
404792	4791	0x12B7	1	Automatic Baud Rate Detection	Byte	128	0...1	N/A	R/W	If set to 0, the unit will be configured to work on the
404793	4792	0x12B8	7	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0

Ethernet										
404800	4799	0x12BF	3	MAC Address	Byte[6]	Set at the factory	Any valid MAC address	N/A	RO	Ethernet MAC Address. Set at the factory. Writing is
404803	4802	0x12C2	2	IP Address	Byte[4]	192.168.0.34	Any IP address	N/A	R/W	The device IP address
404805	4804	0x12C4	2	Subnet Mask	Byte[4]	255.255.255.0	Any IP address	N/A	R/W	The device subnet mask
404807	4806	0x12C6	2	Gateway	Byte[4]	192.168.0.1	Any IP address	N/A	R/W	The device default gateway
404809	4808	0x12C8	1	Modbus Port	Word	502	Any port value except the Dis	N/A	R/W	The Modbus listening port
404810	4809	0x12C9	10	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0
CAN Receive 1										
404820	4819	0x12D3	1	Signal Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	CAN input signal type
404821	4820	0x12D4	2	PGN	Double	0x3FFFF	Any J1939 PGN value	N/A	R/W	Signal message PGN value
404823	4822	0x12D6	1	PGN From Selected Address	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	Only CAN messages from the selected address will
404824	4823	0x12D7	1	Selected Address	Byte	0	0...253	N/A	R/W	Address of the ECU transmitting CAN messages if P
404825	4824	0x12D8	1	Data Position Byte	Byte	1	0..8	N/A	R/W	Start byte of the CAN input signal in the CAN mess
404826	4825	0x12D9	1	Data Position Bit	Byte	1	0..8	N/A	R/W	Start bit of the CAN input signal in the Data Positio
404827	4826	0x12DA	1	Size	Byte	1	0..32	N/A	R/W	CAN input signal size
404828	4827	0x12DB	2	Resolution	Float	1	Any value	signal units / b	R/W	CAN input signal resolution for continuous input sig
404830	4829	0x12DD	2	Offset	Float	0	Any value	signal units	R/W	CAN input signal offset for continuous input signals
404832	4831	0x12DF	1	Autoreset Time	Word	500	0...10000	ms	R/W	Function block signal output auto-reset time. If Aut
404833	4832	0x12E0	2	Data Minimum	Float	0	Any value	N/A	R/W	
404835	4834	0x12E2	2	DataMaximum	Float	100	Any value	N/A	R/W	
404833	4832	0x12E0	12	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
CAN Receive 2										
404845	4844	0x12EC	1	Signal Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	CAN input signal type
404846	4845	0x12ED	2	PGN	Double	0x3FFFF	Any J1939 PGN value	N/A	R/W	Signal message PGN value
404848	4847	0x12EF	1	PGN From Selected Address	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	Only CAN messages from the selected address will
404849	4848	0x12F0	1	Selected Address	Byte	0	0...253	N/A	R/W	Address of the ECU transmitting CAN messages if P
404850	4849	0x12F1	1	Data Position Byte	Byte	1	0..8	N/A	R/W	Start byte of the CAN input signal in the CAN mess
404851	4850	0x12F2	1	Data Position Bit	Byte	1	0..8	N/A	R/W	Start bit of the CAN input signal in the Data Positio
404852	4851	0x12F3	1	Size	Byte	1	0..32	N/A	R/W	CAN input signal size
404853	4852	0x12F4	2	Resolution	Float	1	Any value	signal units / b	R/W	CAN input signal resolution for continuous input sig
404855	4854	0x12F6	2	Offset	Float	0	Any value	signal units	R/W	CAN input signal offset for continuous input signals
404857	4856	0x12F8	1	Autoreset Time	Word	500	0...10000	ms	R/W	Function block signal output auto-reset time. If Aut
404858	4857	0x12F9	2	Data Minimum	Float	0	Any value	N/A	R/W	
404860	4859	0x12FB	2	DataMaximum	Float	100	Any value	N/A	R/W	
404858	4857	0x12F9	12	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
CAN Receive 3										
404870	4869	0x1305	1	Signal Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	CAN input signal type
404871	4870	0x1306	2	PGN	Double	0x3FFFF	Any J1939 PGN value	N/A	R/W	Signal message PGN value
404873	4872	0x1308	1	PGN From Selected Address	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	Only CAN messages from the selected address will
404874	4873	0x1309	1	Selected Address	Byte	0	0...253	N/A	R/W	Address of the ECU transmitting CAN messages if P
404875	4874	0x130A	1	Data Position Byte	Byte	1	0..8	N/A	R/W	Start byte of the CAN input signal in the CAN mess
404876	4875	0x130B	1	Data Position Bit	Byte	1	0..8	N/A	R/W	Start bit of the CAN input signal in the Data Positio
404877	4876	0x130C	1	Size	Byte	1	0..32	N/A	R/W	CAN input signal size
404878	4877	0x130D	2	Resolution	Float	1	Any value	signal units / b	R/W	CAN input signal resolution for continuous input sig
404880	4879	0x130F	2	Offset	Float	0	Any value	signal units	R/W	CAN input signal offset for continuous input signals
404882	4881	0x1311	1	Autoreset Time	Word	500	0...10000	ms	R/W	Function block signal output auto-reset time. If Aut
404883	4882	0x1312	2	Data Minimum	Float	0	Any value	N/A	R/W	
404885	4884	0x1314	2	DataMaximum	Float	100	Any value	N/A	R/W	
404883	4882	0x1312	12	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
CAN Receive 4										
404895	4894	0x131E	1	Signal Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	CAN input signal type

404896	4895	0x131F	2	PGN	Double	0x3FFFF	Any J1939 PGN value	N/A	R/W	Signal message PGN value
404898	4897	0x1321	1	PGN From Selected Address	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	Only CAN messages from the selected address will
404899	4898	0x1322	1	Selected Address	Byte	0	0...253	N/A	R/W	Address of the ECU transmitting CAN messages if P
404900	4899	0x1323	1	Data Position Byte	Byte	1	0..8	N/A	R/W	Start byte of the CAN input signal in the CAN mess
404901	4900	0x1324	1	Data Position Bit	Byte	1	0..8	N/A	R/W	Start bit of the CAN input signal in the Data Positio
404902	4901	0x1325	1	Size	Byte	1	0..32	N/A	R/W	CAN input signal size
404903	4902	0x1326	2	Resolution	Float	1	Any value	signal units / h	R/W	CAN input signal resolution for continuous input sig
404905	4904	0x1328	2	Offset	Float	0	Any value	signal units	R/W	CAN input signal offset for continuous input signals
404907	4906	0x132A	1	Autoreset Time	Word	500	0...10000	ms	R/W	Function block signal output auto-reset time. If Aut
404908	4907	0x132B	2	Data Minimum	Float	0	Any value	N/A	R/W	
404910	4909	0x132D	2	DataMaximum	Float	100	Any value	N/A	R/W	
404908	4907	0x132B	12	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>CAN Receive 5</b>										
404920	4919	0x1337	1	Signal Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	CAN input signal type
404921	4920	0x1338	2	PGN	Double	0x3FFFF	Any J1939 PGN value	N/A	R/W	Signal message PGN value
404923	4922	0x133A	1	PGN From Selected Address	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	Only CAN messages from the selected address will
404924	4923	0x133B	1	Selected Address	Byte	0	0...253	N/A	R/W	Address of the ECU transmitting CAN messages if P
404925	4924	0x133C	1	Data Position Byte	Byte	1	0..8	N/A	R/W	Start byte of the CAN input signal in the CAN mess
404926	4925	0x133D	1	Data Position Bit	Byte	1	0..8	N/A	R/W	Start bit of the CAN input signal in the Data Positio
404927	4926	0x133E	1	Size	Byte	1	0..32	N/A	R/W	CAN input signal size
404928	4927	0x133F	2	Resolution	Float	1	Any value	signal units / h	R/W	CAN input signal resolution for continuous input sig
404930	4929	0x1341	2	Offset	Float	0	Any value	signal units	R/W	CAN input signal offset for continuous input signals
404932	4931	0x1343	1	Autoreset Time	Word	500	0...10000	ms	R/W	Function block signal output auto-reset time. If Aut
404933	4932	0x1344	2	Data Minimum	Float	0	Any value	N/A	R/W	
404935	4934	0x1346	2	DataMaximum	Float	100	Any value	N/A	R/W	
404933	4932	0x1344	12	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>CAN Receive 6</b>										
404945	4944	0x1350	1	Signal Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	CAN input signal type
404946	4945	0x1351	2	PGN	Double	0x3FFFF	Any J1939 PGN value	N/A	R/W	Signal message PGN value
404948	4947	0x1353	1	PGN From Selected Address	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	Only CAN messages from the selected address will
404949	4948	0x1354	1	Selected Address	Byte	0	0...253	N/A	R/W	Address of the ECU transmitting CAN messages if P
404950	4949	0x1355	1	Data Position Byte	Byte	1	0..8	N/A	R/W	Start byte of the CAN input signal in the CAN mess
404951	4950	0x1356	1	Data Position Bit	Byte	1	0..8	N/A	R/W	Start bit of the CAN input signal in the Data Positio
404952	4951	0x1357	1	Size	Byte	1	0..32	N/A	R/W	CAN input signal size
404953	4952	0x1358	2	Resolution	Float	1	Any value	signal units / h	R/W	CAN input signal resolution for continuous input sig
404955	4954	0x135A	2	Offset	Float	0	Any value	signal units	R/W	CAN input signal offset for continuous input signals
404957	4956	0x135C	1	Autoreset Time	Word	500	0...10000	ms	R/W	Function block signal output auto-reset time. If Aut
404958	4957	0x135D	2	Data Minimum	Float	0	Any value	N/A	R/W	
404960	4959	0x135F	2	DataMaximum	Float	100	Any value	N/A	R/W	
404958	4957	0x135D	12	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>CAN Receive 7</b>										
404970	4969	0x1369	1	Signal Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	CAN input signal type
404971	4970	0x136A	2	PGN	Double	0x3FFFF	Any J1939 PGN value	N/A	R/W	Signal message PGN value
404973	4972	0x136C	1	PGN From Selected Address	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	Only CAN messages from the selected address will
404974	4973	0x136D	1	Selected Address	Byte	0	0...253	N/A	R/W	Address of the ECU transmitting CAN messages if P
404975	4974	0x136E	1	Data Position Byte	Byte	1	0..8	N/A	R/W	Start byte of the CAN input signal in the CAN mess
404976	4975	0x136F	1	Data Position Bit	Byte	1	0..8	N/A	R/W	Start bit of the CAN input signal in the Data Positio
404977	4976	0x1370	1	Size	Byte	1	0..32	N/A	R/W	CAN input signal size
404978	4977	0x1371	2	Resolution	Float	1	Any value	signal units / h	R/W	CAN input signal resolution for continuous input sig
404980	4979	0x1373	2	Offset	Float	0	Any value	signal units	R/W	CAN input signal offset for continuous input signals
404982	4981	0x1375	1	Autoreset Time	Word	500	0...10000	ms	R/W	Function block signal output auto-reset time. If Aut

404983	4982	0x1376	2	Data Minimum	Float	0	Any value	N/A	R/W	
404985	4984	0x1378	2	DataMaximum	Float	100	Any value	N/A	R/W	
404983	4982	0x1376	12	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>CAN Receive 8</b>										
404995	4994	0x1382	1	Signal Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	CAN input signal type
404996	4995	0x1383	2	PGN	Double	0x3FFFF	Any J1939 PGN value	N/A	R/W	Signal message PGN value
404998	4997	0x1385	1	PGN From Selected Address	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	Only CAN messages from the selected address will
404999	4998	0x1386	1	Selected Address	Byte	0	0...253	N/A	R/W	Address of the ECU transmitting CAN messages if P
405000	4999	0x1387	1	Data Position Byte	Byte	1	0...8	N/A	R/W	Start byte of the CAN input signal in the CAN mess
405001	5000	0x1388	1	Data Position Bit	Byte	1	0...8	N/A	R/W	Start bit of the CAN input signal in the Data Positio
405002	5001	0x1389	1	Size	Byte	1	0...32	N/A	R/W	CAN input signal size
405003	5002	0x138A	2	Resolution	Float	1	Any value	signal units / h	R/W	CAN input signal resolution for continuous input sig
405005	5004	0x138C	2	Offset	Float	0	Any value	signal units	R/W	CAN input signal offset for continuous input signals
405007	5006	0x138E	1	Autoreset Time	Word	500	0...10000	ms	R/W	Function block signal output auto-reset time. If Aut
405008	5007	0x138F	2	Data Minimum	Float	0	Any value	N/A	R/W	
405010	5009	0x1391	2	DataMaximum	Float	100	Any value	N/A	R/W	
405008	5007	0x138F	12	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>CAN Receive 9</b>										
405020	5019	0x139B	1	Signal Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	CAN input signal type
405021	5020	0x139C	2	PGN	Double	0x3FFFF	Any J1939 PGN value	N/A	R/W	Signal message PGN value
405023	5022	0x139E	1	PGN From Selected Address	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	Only CAN messages from the selected address will
405024	5023	0x139F	1	Selected Address	Byte	0	0...253	N/A	R/W	Address of the ECU transmitting CAN messages if P
405025	5024	0x13A0	1	Data Position Byte	Byte	1	0...8	N/A	R/W	Start byte of the CAN input signal in the CAN mess
405026	5025	0x13A1	1	Data Position Bit	Byte	1	0...8	N/A	R/W	Start bit of the CAN input signal in the Data Positio
405027	5026	0x13A2	1	Size	Byte	1	0...32	N/A	R/W	CAN input signal size
405028	5027	0x13A3	2	Resolution	Float	1	Any value	signal units / h	R/W	CAN input signal resolution for continuous input sig
405030	5029	0x13A5	2	Offset	Float	0	Any value	signal units	R/W	CAN input signal offset for continuous input signals
405032	5031	0x13A7	1	Autoreset Time	Word	500	0...10000	ms	R/W	Function block signal output auto-reset time. If Aut
405033	5032	0x13A8	2	Data Minimum	Float	0	Any value	N/A	R/W	
405035	5034	0x13AA	2	DataMaximum	Float	100	Any value	N/A	R/W	
405033	5032	0x13A8	12	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>CAN Receive 10</b>										
405045	5044	0x13B4	1	Signal Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	CAN input signal type
405046	5045	0x13B5	2	PGN	Double	0x3FFFF	Any J1939 PGN value	N/A	R/W	Signal message PGN value
405048	5047	0x13B7	1	PGN From Selected Address	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	Only CAN messages from the selected address will
405049	5048	0x13B8	1	Selected Address	Byte	0	0...253	N/A	R/W	Address of the ECU transmitting CAN messages if P
405050	5049	0x13B9	1	Data Position Byte	Byte	1	0...8	N/A	R/W	Start byte of the CAN input signal in the CAN mess
405051	5050	0x13BA	1	Data Position Bit	Byte	1	0...8	N/A	R/W	Start bit of the CAN input signal in the Data Positio
405052	5051	0x13BB	1	Size	Byte	1	0...32	N/A	R/W	CAN input signal size
405053	5052	0x13BC	2	Resolution	Float	1	Any value	signal units / h	R/W	CAN input signal resolution for continuous input sig
405055	5054	0x13BE	2	Offset	Float	0	Any value	signal units	R/W	CAN input signal offset for continuous input signals
405057	5056	0x13C0	1	Autoreset Time	Word	500	0...10000	ms	R/W	Function block signal output auto-reset time. If Aut
405058	5057	0x13C1	2	Data Minimum	Float	0	Any value	N/A	R/W	
405060	5059	0x13C3	2	DataMaximum	Float	100	Any value	N/A	R/W	
405058	5057	0x13C1	12	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>CAN Receive 11</b>										
405070	5069	0x13CD	1	Signal Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	CAN input signal type
405071	5070	0x13CE	2	PGN	Double	0x3FFFF	Any J1939 PGN value	N/A	R/W	Signal message PGN value
405073	5072	0x13D0	1	PGN From Selected Address	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	Only CAN messages from the selected address will
405074	5073	0x13D1	1	Selected Address	Byte	0	0...253	N/A	R/W	Address of the ECU transmitting CAN messages if P
405075	5074	0x13D2	1	Data Position Byte	Byte	1	0...8	N/A	R/W	Start byte of the CAN input signal in the CAN mess



405076	5075	0x13D3	1	Data Position Bit	Byte	1	0..8	N/A	R/W	Start bit of the CAN input signal in the Data Position
405077	5076	0x13D4	1	Size	Byte	1	0..32	N/A	R/W	CAN input signal size
405078	5077	0x13D5	2	Resolution	Float	1	Any value	signal units / bit	R/W	CAN input signal resolution for continuous input signals
405080	5079	0x13D7	2	Offset	Float	0	Any value	signal units	R/W	CAN input signal offset for continuous input signals
405082	5081	0x13D9	1	Autoreset Time	Word	500	0..10000	ms	R/W	Function block signal output auto-reset time. If Autoreset is enabled
405083	5082	0x13DA	2	Data Minimum	Float	0	Any value	N/A	R/W	
405085	5084	0x13DC	2	DataMaximum	Float	100	Any value	N/A	R/W	
405083	5082	0x13DA	12	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing results 0.
<b>CAN Receive 12</b>										
405095	5094	0x13E6	1	Signal Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 - Analog	N/A	R/W	CAN input signal type
405096	5095	0x13E7	2	PGN	Double	0x3FFF	Any J1939 PGN value	N/A	R/W	Signal message PGN value
405098	5097	0x13E9	1	PGN From Selected Address	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	Only CAN messages from the selected address will be received
405099	5098	0x13EA	1	Selected Address	Byte	0	0..253	N/A	R/W	Address of the ECU transmitting CAN messages if PGN From Selected Address is Yes
405100	5099	0x13EB	1	Data Position Byte	Byte	1	0..8	N/A	R/W	Start byte of the CAN input signal in the CAN message
405101	5100	0x13EC	1	Data Position Bit	Byte	1	0..8	N/A	R/W	Start bit of the CAN input signal in the Data Position
405102	5101	0x13ED	1	Size	Byte	1	0..32	N/A	R/W	CAN input signal size
405103	5102	0x13EE	2	Resolution	Float	1	Any value	signal units / bit	R/W	CAN input signal resolution for continuous input signals
405105	5104	0x13F0	2	Offset	Float	0	Any value	signal units	R/W	CAN input signal offset for continuous input signals
405107	5106	0x13F2	1	Autoreset Time	Word	500	0..10000	ms	R/W	Function block signal output auto-reset time. If Autoreset is enabled
405108	5107	0x13F3	2	Data Minimum	Float	0	Any value	N/A	R/W	
405110	5109	0x13F5	2	DataMaximum	Float	100	Any value	N/A	R/W	
405108	5107	0x13F3	#REF!	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing results 0.
<b>CAN Transmit 1</b>										
405120	5119	0x13FF	1	Transmission Enable	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	CAN message PGN
405121	5120	0x1400	2	PGN	Double	0xFFFF	Any J1939 PGN value	N/A	R/W	Transmission Enable. Enables the CAN output message
405123	5122	0x1402	1	Transmission Rate	Word	0	0..10000	ms	R/W	CAN output message transmission rate. If 0 – transmission rate is not set
405124	5123	0x1403	1	Destination Address	Byte	255	0..255	N/A	R/W	Destination address of the PDU1 PGN messages
405125	5124	0x1404	1	Length	Byte	8	0..8	byte	R/W	CAN message data frame length
405126	5125	0x1405	1	Priority	Byte	6	0..7	N/A	R/W	CAN message priority
405127	5126	0x1406	1	Signal #1 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 - Analog	N/A	R/W	Type of the 1-st CAN output signal
405128	5127	0x1407	1	Signal #1 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 1-st CAN output signal
405129	5128	0x1408	1	Signal #1 Source Number	Byte	1	Depends on control source	N/A	R/W	
405132	5131	0x140B	1	Signal #1 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
405130	5129	0x1409	1	Signal #1 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
405131	5130	0x140A	1	Signal #1 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
405133	5132	0x140C	2	Signal #1 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 1-st CAN continuous output signal
405135	5134	0x140E	2	Signal #1 Offset	Float	0	Any value	signal units	R/W	Offset of the 1-st CAN continuous output signal
405137	5136	0x1410	1	Signal #2 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 - Analog	N/A	R/W	Type of the 2-nd CAN output signal
405138	5137	0x1411	1	Signal #2 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 2-nd CAN output signal
405139	5138	0x1412	1	Signal #2 Source Number	Byte	1	Depends on control source	N/A	R/W	
405140	5139	0x1413	1	Signal #2 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
405141	5140	0x1414	1	Signal #2 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
405142	5141	0x1415	1	Signal #2 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
405143	5142	0x1416	2	Signal #2 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 2-nd CAN continuous output signal
405145	5144	0x1418	2	Signal #2 Offset	Float	0	Any value	signal units	R/W	Offset of the 2-nd CAN continuous output signal
405147	5146	0x141A	1	Signal #3 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 - Analog	N/A	R/W	Type of the 3-rd CAN output signal
405148	5147	0x141B	1	Signal #3 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 3-rd CAN output signal
405149	5148	0x141C	1	Signal #3 Source Number	Byte	1	Depends on control source	N/A	R/W	
405150	5149	0x141D	1	Signal #3 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
405151	5150	0x141E	1	Signal #3 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal

405152	5151	0x141F	1	Signal #3 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
405153	5152	0x1420	2	Signal #3 Resolution	Float	1	Any value	signal units / h	R/W	Resolution of the 3-rd CAN continuous output signal
405155	5154	0x1422	2	Signal #3 Offset	Float	0	Any value	signal units	R/W	Offset of the 3-rd CAN continuous output signal
405157	5156	0x1424	1	Signal #4 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
405158	5157	0x1425	1	Signal #4 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
405159	5158	0x1426	1	Signal #4 Source Number	Byte	1	Depends on control source	N/A	R/W	
405160	5159	0x1427	1	Signal #4 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
405161	5160	0x1428	1	Signal #4 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
405162	5161	0x1429	1	Signal #4 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
405163	5162	0x142A	2	Signal #4 Resolution	Float	1	Any value	signal units / h	R/W	Resolution of the 4-th CAN continuous output signal
405165	5164	0x142C	2	Signal #4 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
405167	5166	0x142E	1	Signal #5 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
405168	5167	0x142F	1	Signal #5 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
405169	5168	0x1430	1	Signal #5 Source Number	Byte	1	Depends on control source	N/A	R/W	
405170	5169	0x1431	1	Signal #5 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
405171	5170	0x1432	1	Signal #5 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
405172	5171	0x1433	1	Signal #5 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
405173	5172	0x1434	2	Signal #5 Resolution	Float	1	Any value	signal units / h	R/W	Resolution of the 4-th CAN continuous output signal
405175	5174	0x1436	2	Signal #5 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
405177	5176	0x1438	1	Signal #6 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
405178	5177	0x1439	1	Signal #6 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
405179	5178	0x143A	1	Signal #6 Source Number	Byte	1	Depends on control source	N/A	R/W	
405180	5179	0x143B	1	Signal #6 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
405181	5180	0x143C	1	Signal #6 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
405182	5181	0x143D	1	Signal #6 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
405183	5182	0x143E	2	Signal #6 Resolution	Float	1	Any value	signal units / h	R/W	Resolution of the 4-th CAN continuous output signal
405185	5184	0x1440	2	Signal #6 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
405187	5186	0x1442	1	Signal #7 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
405188	5187	0x1443	1	Signal #7 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
405189	5188	0x1444	1	Signal #7 Source Number	Byte	1	Depends on control source	N/A	R/W	
405190	5189	0x1445	1	Signal #7 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
405191	5190	0x1446	1	Signal #7 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
405192	5191	0x1447	1	Signal #7 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
405193	5192	0x1448	2	Signal #7 Resolution	Float	1	Any value	signal units / h	R/W	Resolution of the 4-th CAN continuous output signal
405195	5194	0x144A	2	Signal #7 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
405197	5196	0x144C	1	Signal #8 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
405198	5197	0x144D	1	Signal #8 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
405199	5198	0x144E	1	Signal #8 Source Number	Byte	1	Depends on control source	N/A	R/W	
405200	5199	0x144F	1	Signal #8 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
405201	5200	0x1450	1	Signal #8 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
405202	5201	0x1451	1	Signal #8 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
405203	5202	0x1452	2	Signal #8 Resolution	Float	1	Any value	signal units / h	R/W	Resolution of the 4-th CAN continuous output signal
405205	5204	0x1454	2	Signal #8 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
405207	5206	0x1456	1	Signal #9 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
405208	5207	0x1457	1	Signal #9 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
405209	5208	0x1458	1	Signal #9 Source Number	Byte	1	Depends on control source	N/A	R/W	
405210	5209	0x1459	1	Signal #9 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
405211	5210	0x145A	1	Signal #9 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
405212	5211	0x145B	1	Signal #9 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
405213	5212	0x145C	2	Signal #9 Resolution	Float	1	Any value	signal units / h	R/W	Resolution of the 4-th CAN continuous output signal
405215	5214	0x145E	2	Signal #9 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal

405217	5216	0x1460	1	Signal #10 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
405218	5217	0x1461	1	Signal #10 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
405219	5218	0x1462	1	Signal #10 Source Number	Byte	1	Depends on control source	N/A	R/W	
405220	5219	0x1463	1	Signal #10 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
405221	5220	0x1464	1	Signal #10 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
405222	5221	0x1465	1	Signal #10 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
405223	5222	0x1466	2	Signal #10 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
405225	5224	0x1468	2	Signal #10 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
405227	5226	0x146A	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>CAN Transmit 2</b>										
405235	5234	0x1472	1	Transmission Enable	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	CAN message PGN
405236	5235	0x1473	2	PGN	Double	0xFFFF	Any J1939 PGN value	N/A	R/W	Transmission Enable. Enables the CAN output mess
405238	5237	0x1475	1	Transmission Rate	Word	0	0..10000	ms	R/W	CAN output message transmission rate. If 0 – trans
405239	5238	0x1476	1	Destination Address	Byte	255	0..255	N/A	R/W	Destination address of the PDU1 PGN messages
405240	5239	0x1477	1	Length	Byte	8	0..8	byte	R/W	CAN message data frame length
405241	5240	0x1478	1	Priority	Byte	6	0..7	N/A	R/W	CAN message priority
405242	5241	0x1479	1	Signal #1 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 1-st CAN output signal
405243	5242	0x147A	1	Signal #1 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 1-st CAN output signal
405244	5243	0x147B	1	Signal #1 Source Number	Byte	1	Depends on control source	N/A	R/W	
405247	5246	0x147E	1	Signal #1 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
405245	5244	0x147C	1	Signal #1 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
405246	5245	0x147D	1	Signal #1 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
405248	5247	0x147F	2	Signal #1 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 1-st CAN continuous output signal
405250	5249	0x1481	2	Signal #1 Offset	Float	0	Any value	signal units	R/W	Offset of the 1-st CAN continuous output signal
405252	5251	0x1483	1	Signal #2 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 2-nd CAN output signal
405253	5252	0x1484	1	Signal #2 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 2-nd CAN output signal
405254	5253	0x1485	1	Signal #2 Source Number	Byte	1	Depends on control source	N/A	R/W	
405255	5254	0x1486	1	Signal #2 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
405256	5255	0x1487	1	Signal #2 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
405257	5256	0x1488	1	Signal #2 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
405258	5257	0x1489	2	Signal #2 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 2-nd CAN continuous output signal
405260	5259	0x148B	2	Signal #2 Offset	Float	0	Any value	signal units	R/W	Offset of the 2-nd CAN continuous output signal
405262	5261	0x148D	1	Signal #3 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 3-rd CAN output signal
405263	5262	0x148E	1	Signal #3 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 3-rd CAN output signal
405264	5263	0x148F	1	Signal #3 Source Number	Byte	1	Depends on control source	N/A	R/W	
405265	5264	0x1490	1	Signal #3 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
405266	5265	0x1491	1	Signal #3 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
405267	5266	0x1492	1	Signal #3 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
405268	5267	0x1493	2	Signal #3 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 3-rd CAN continuous output signal
405270	5269	0x1495	2	Signal #3 Offset	Float	0	Any value	signal units	R/W	Offset of the 3-rd CAN continuous output signal
405272	5271	0x1497	1	Signal #4 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
405273	5272	0x1498	1	Signal #4 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
405274	5273	0x1499	1	Signal #4 Source Number	Byte	1	Depends on control source	N/A	R/W	
405275	5274	0x149A	1	Signal #4 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
405276	5275	0x149B	1	Signal #4 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
405277	5276	0x149C	1	Signal #4 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
405278	5277	0x149D	2	Signal #4 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
405280	5279	0x149F	2	Signal #4 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
405282	5281	0x14A1	1	Signal #5 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
405283	5282	0x14A2	1	Signal #5 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal

405284	5283	0x14A3	1	Signal #5 Source Number	Byte	1	Depends on control source	N/A	R/W	
405285	5284	0x14A4	1	Signal #5 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
405286	5285	0x14A5	1	Signal #5 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
405287	5286	0x14A6	1	Signal #5 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
405288	5287	0x14A7	2	Signal #5 Resolution	Float	1	Any value	signal units / t	R/W	Resolution of the 4-th CAN continuous output signal
405290	5289	0x14A9	2	Signal #5 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
405292	5291	0x14AB	1	Signal #6 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
405293	5292	0x14AC	1	Signal #6 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
405294	5293	0x14AD	1	Signal #6 Source Number	Byte	1	Depends on control source	N/A	R/W	
405295	5294	0x14AE	1	Signal #6 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
405296	5295	0x14AF	1	Signal #6 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
405297	5296	0x14B0	1	Signal #6 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
405298	5297	0x14B1	2	Signal #6 Resolution	Float	1	Any value	signal units / t	R/W	Resolution of the 4-th CAN continuous output signal
405300	5299	0x14B3	2	Signal #6 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
405302	5301	0x14B5	1	Signal #7 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
405303	5302	0x14B6	1	Signal #7 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
405304	5303	0x14B7	1	Signal #7 Source Number	Byte	1	Depends on control source	N/A	R/W	
405305	5304	0x14B8	1	Signal #7 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
405306	5305	0x14B9	1	Signal #7 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
405307	5306	0x14BA	1	Signal #7 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
405308	5307	0x14BB	2	Signal #7 Resolution	Float	1	Any value	signal units / t	R/W	Resolution of the 4-th CAN continuous output signal
405310	5309	0x14BD	2	Signal #7 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
405312	5311	0x14BF	1	Signal #8 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
405313	5312	0x14C0	1	Signal #8 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
405314	5313	0x14C1	1	Signal #8 Source Number	Byte	1	Depends on control source	N/A	R/W	
405315	5314	0x14C2	1	Signal #8 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
405316	5315	0x14C3	1	Signal #8 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
405317	5316	0x14C4	1	Signal #8 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
405318	5317	0x14C5	2	Signal #8 Resolution	Float	1	Any value	signal units / t	R/W	Resolution of the 4-th CAN continuous output signal
405320	5319	0x14C7	2	Signal #8 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
405322	5321	0x14C9	1	Signal #9 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
405323	5322	0x14CA	1	Signal #9 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
405324	5323	0x14CB	1	Signal #9 Source Number	Byte	1	Depends on control source	N/A	R/W	
405325	5324	0x14CC	1	Signal #9 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
405326	5325	0x14CD	1	Signal #9 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
405327	5326	0x14CE	1	Signal #9 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
405328	5327	0x14CF	2	Signal #9 Resolution	Float	1	Any value	signal units / t	R/W	Resolution of the 4-th CAN continuous output signal
405330	5329	0x14D1	2	Signal #9 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
405332	5331	0x14D3	1	Signal #10 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
405333	5332	0x14D4	1	Signal #10 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
405334	5333	0x14D5	1	Signal #10 Source Number	Byte	1	Depends on control source	N/A	R/W	
405335	5334	0x14D6	1	Signal #10 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
405336	5335	0x14D7	1	Signal #10 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
405337	5336	0x14D8	1	Signal #10 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
405338	5337	0x14D9	2	Signal #10 Resolution	Float	1	Any value	signal units / t	R/W	Resolution of the 4-th CAN continuous output signal
405340	5339	0x14DB	2	Signal #10 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
405342	5341	0x14DD	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>CAN Transmit 3</b>										
405350	5349	0x14E5	1	Transmission Enable	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	CAN message PGN
405351	5350	0x14E6	2	PGN	Double	0xFFFF	Any J1939 PGN value	N/A	R/W	Transmission Enable. Enables the CAN output mess
405353	5352	0x14E8	1	Transmission Rate	Word	0	0...10000	ms	R/W	CAN output message transmission rate. If 0 = trans

405354	5353	0x14E9	1	Destination Address	Byte	255	0..255	N/A	R/W	Destination address of the PDU1 PGN messages
405355	5354	0x14EA	1	Length	Byte	8	0..8	byte	R/W	CAN message data frame length
405356	5355	0x14EB	1	Priority	Byte	6	0..7	N/A	R/W	CAN message priority
405357	5356	0x14EC	1	Signal #1 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 1-st CAN output signal
405358	5357	0x14ED	1	Signal #1 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 1-st CAN output signal
405359	5358	0x14EE	1	Signal #1 Source Number	Byte	1	Depends on control source	N/A	R/W	
405362	5361	0x14F1	1	Signal #1 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
405360	5359	0x14EF	1	Signal #1 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
405361	5360	0x14F0	1	Signal #1 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
405363	5362	0x14F2	2	Signal #1 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 1-st CAN continuous output signal
405365	5364	0x14F4	2	Signal #1 Offset	Float	0	Any value	signal units	R/W	Offset of the 1-st CAN continuous output signal
405367	5366	0x14F6	1	Signal #2 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 2-nd CAN output signal
405368	5367	0x14F7	1	Signal #2 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 2-nd CAN output signal
405369	5368	0x14F8	1	Signal #2 Source Number	Byte	1	Depends on control source	N/A	R/W	
405370	5369	0x14F9	1	Signal #2 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
405371	5370	0x14FA	1	Signal #2 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
405372	5371	0x14FB	1	Signal #2 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
405373	5372	0x14FC	2	Signal #2 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 2-nd CAN continuous output signal
405375	5374	0x14FE	2	Signal #2 Offset	Float	0	Any value	signal units	R/W	Offset of the 2-nd CAN continuous output signal
405377	5376	0x1500	1	Signal #3 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 3-rd CAN output signal
405378	5377	0x1501	1	Signal #3 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 3-rd CAN output signal
405379	5378	0x1502	1	Signal #3 Source Number	Byte	1	Depends on control source	N/A	R/W	
405380	5379	0x1503	1	Signal #3 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
405381	5380	0x1504	1	Signal #3 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
405382	5381	0x1505	1	Signal #3 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
405383	5382	0x1506	2	Signal #3 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 3-rd CAN continuous output signal
405385	5384	0x1508	2	Signal #3 Offset	Float	0	Any value	signal units	R/W	Offset of the 3-rd CAN continuous output signal
405387	5386	0x150A	1	Signal #4 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
405388	5387	0x150B	1	Signal #4 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
405389	5388	0x150C	1	Signal #4 Source Number	Byte	1	Depends on control source	N/A	R/W	
405390	5389	0x150D	1	Signal #4 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
405391	5390	0x150E	1	Signal #4 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
405392	5391	0x150F	1	Signal #4 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
405393	5392	0x1510	2	Signal #4 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
405395	5394	0x1512	2	Signal #4 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
405397	5396	0x1514	1	Signal #5 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
405398	5397	0x1515	1	Signal #5 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
405399	5398	0x1516	1	Signal #5 Source Number	Byte	1	Depends on control source	N/A	R/W	
405400	5399	0x1517	1	Signal #5 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
405401	5400	0x1518	1	Signal #5 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
405402	5401	0x1519	1	Signal #5 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
405403	5402	0x151A	2	Signal #5 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
405405	5404	0x151C	2	Signal #5 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
405407	5406	0x151E	1	Signal #6 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
405408	5407	0x151F	1	Signal #6 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
405409	5408	0x1520	1	Signal #6 Source Number	Byte	1	Depends on control source	N/A	R/W	
405410	5409	0x1521	1	Signal #6 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
405411	5410	0x1522	1	Signal #6 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
405412	5411	0x1523	1	Signal #6 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
405413	5412	0x1524	2	Signal #6 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal

405415	5414	0x1526	2	Signal #6 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
405417	5416	0x1528	1	Signal #7 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
405418	5417	0x1529	1	Signal #7 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
405419	5418	0x152A	1	Signal #7 Source Number	Byte	1	Depends on control source	N/A	R/W	
405420	5419	0x152B	1	Signal #7 Size	Byte	1	0...32	bit	R/W	Size of the 1-st CAN output signal
405421	5420	0x152C	1	Signal #7 Byte Position	Byte	1	0...8	N/A	R/W	Byte position of the 1-st CAN output signal
405422	5421	0x152D	1	Signal #7 Bit Position	Byte	1	0...8	N/A	R/W	Bit position of the 1-st CAN output signal
405423	5422	0x152E	2	Signal #7 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
405425	5424	0x1530	2	Signal #7 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
405427	5426	0x1532	1	Signal #8 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
405428	5427	0x1533	1	Signal #8 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
405429	5428	0x1534	1	Signal #8 Source Number	Byte	1	Depends on control source	N/A	R/W	
405430	5429	0x1535	1	Signal #8 Size	Byte	1	0...32	bit	R/W	Size of the 1-st CAN output signal
405431	5430	0x1536	1	Signal #8 Byte Position	Byte	1	0...8	N/A	R/W	Byte position of the 1-st CAN output signal
405432	5431	0x1537	1	Signal #8 Bit Position	Byte	1	0...8	N/A	R/W	Bit position of the 1-st CAN output signal
405433	5432	0x1538	2	Signal #8 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
405435	5434	0x153A	2	Signal #8 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
405437	5436	0x153C	1	Signal #9 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
405438	5437	0x153D	1	Signal #9 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
405439	5438	0x153E	1	Signal #9 Source Number	Byte	1	Depends on control source	N/A	R/W	
405440	5439	0x153F	1	Signal #9 Size	Byte	1	0...32	bit	R/W	Size of the 1-st CAN output signal
405441	5440	0x1540	1	Signal #9 Byte Position	Byte	1	0...8	N/A	R/W	Byte position of the 1-st CAN output signal
405442	5441	0x1541	1	Signal #9 Bit Position	Byte	1	0...8	N/A	R/W	Bit position of the 1-st CAN output signal
405443	5442	0x1542	2	Signal #9 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
405445	5444	0x1544	2	Signal #9 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
405447	5446	0x1546	1	Signal #10 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
405448	5447	0x1547	1	Signal #10 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
405449	5448	0x1548	1	Signal #10 Source Number	Byte	1	Depends on control source	N/A	R/W	
405450	5449	0x1549	1	Signal #10 Size	Byte	1	0...32	bit	R/W	Size of the 1-st CAN output signal
405451	5450	0x154A	1	Signal #10 Byte Position	Byte	1	0...8	N/A	R/W	Byte position of the 1-st CAN output signal
405452	5451	0x154B	1	Signal #10 Bit Position	Byte	1	0...8	N/A	R/W	Bit position of the 1-st CAN output signal
405453	5452	0x154C	2	Signal #10 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
405455	5454	0x154E	2	Signal #10 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
405457	5456	0x1550	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>CAN Transmit 4</b>										
405465	5464	0x1558	1	Transmission Enable	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	CAN message PGN
405466	5465	0x1559	2	PGN	Double	0xFFFF	Any J1939 PGN value	N/A	R/W	Transmission Enable. Enables the CAN output mess
405468	5467	0x155B	1	Transmission Rate	Word	0	0...10000	ms	R/W	CAN output message transmission rate. If 0 – trans
405469	5468	0x155C	1	Destination Address	Byte	255	0...255	N/A	R/W	Destination address of the PDU1 PGN messages
405470	5469	0x155D	1	Length	Byte	8	0...8	byte	R/W	CAN message data frame length
405471	5470	0x155E	1	Priority	Byte	6	0...7	N/A	R/W	CAN message priority
405472	5471	0x155F	1	Signal #1 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 1-st CAN output signal
405473	5472	0x1560	1	Signal #1 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 1-st CAN output signal
405474	5473	0x1561	1	Signal #1 Source Number	Byte	1	Depends on control source	N/A	R/W	
405477	5476	0x1564	1	Signal #1 Size	Byte	1	0...32	bit	R/W	Size of the 1-st CAN output signal
405475	5474	0x1562	1	Signal #1 Byte Position	Byte	1	0...8	N/A	R/W	Byte position of the 1-st CAN output signal
405476	5475	0x1563	1	Signal #1 Bit Position	Byte	1	0...8	N/A	R/W	Bit position of the 1-st CAN output signal
405478	5477	0x1565	2	Signal #1 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 1-st CAN continuous output signal
405480	5479	0x1567	2	Signal #1 Offset	Float	0	Any value	signal units	R/W	Offset of the 1-st CAN continuous output signal
405482	5481	0x1569	1	Signal #2 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 2-nd CAN output signal

405483	5482	0x156A	1	Signal #2 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 2-nd CAN output signal
405484	5483	0x156B	1	Signal #2 Source Number	Byte	1	Depends on control source	N/A	R/W	
405485	5484	0x156C	1	Signal #2 Size	Byte	1	0...32	bit	R/W	Size of the 1-st CAN output signal
405486	5485	0x156D	1	Signal #2 Byte Position	Byte	1	0...8	N/A	R/W	Byte position of the 1-st CAN output signal
405487	5486	0x156E	1	Signal #2 Bit Position	Byte	1	0...8	N/A	R/W	Bit position of the 1-st CAN output signal
405488	5487	0x156F	2	Signal #2 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 2-nd CAN continuous output signal
405490	5489	0x1571	2	Signal #2 Offset	Float	0	Any value	signal units	R/W	Offset of the 2-nd CAN continuous output signal
405492	5491	0x1573	1	Signal #3 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 3-rd CAN output signal
405493	5492	0x1574	1	Signal #3 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 3-rd CAN output signal
405494	5493	0x1575	1	Signal #3 Source Number	Byte	1	Depends on control source	N/A	R/W	
405495	5494	0x1576	1	Signal #3 Size	Byte	1	0...32	bit	R/W	Size of the 1-st CAN output signal
405496	5495	0x1577	1	Signal #3 Byte Position	Byte	1	0...8	N/A	R/W	Byte position of the 1-st CAN output signal
405497	5496	0x1578	1	Signal #3 Bit Position	Byte	1	0...8	N/A	R/W	Bit position of the 1-st CAN output signal
405498	5497	0x1579	2	Signal #3 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 3-rd CAN continuous output signal
405500	5499	0x157B	2	Signal #3 Offset	Float	0	Any value	signal units	R/W	Offset of the 3-rd CAN continuous output signal
405502	5501	0x157D	1	Signal #4 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
405503	5502	0x157E	1	Signal #4 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
405504	5503	0x157F	1	Signal #4 Source Number	Byte	1	Depends on control source	N/A	R/W	
405505	5504	0x1580	1	Signal #4 Size	Byte	1	0...32	bit	R/W	Size of the 1-st CAN output signal
405506	5505	0x1581	1	Signal #4 Byte Position	Byte	1	0...8	N/A	R/W	Byte position of the 1-st CAN output signal
405507	5506	0x1582	1	Signal #4 Bit Position	Byte	1	0...8	N/A	R/W	Bit position of the 1-st CAN output signal
405508	5507	0x1583	2	Signal #4 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
405510	5509	0x1585	2	Signal #4 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
405512	5511	0x1587	1	Signal #5 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
405513	5512	0x1588	1	Signal #5 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
405514	5513	0x1589	1	Signal #5 Source Number	Byte	1	Depends on control source	N/A	R/W	
405515	5514	0x158A	1	Signal #5 Size	Byte	1	0...32	bit	R/W	Size of the 1-st CAN output signal
405516	5515	0x158B	1	Signal #5 Byte Position	Byte	1	0...8	N/A	R/W	Byte position of the 1-st CAN output signal
405517	5516	0x158C	1	Signal #5 Bit Position	Byte	1	0...8	N/A	R/W	Bit position of the 1-st CAN output signal
405518	5517	0x158D	2	Signal #5 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
405520	5519	0x158F	2	Signal #5 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
405522	5521	0x1591	1	Signal #6 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
405523	5522	0x1592	1	Signal #6 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
405524	5523	0x1593	1	Signal #6 Source Number	Byte	1	Depends on control source	N/A	R/W	
405525	5524	0x1594	1	Signal #6 Size	Byte	1	0...32	bit	R/W	Size of the 1-st CAN output signal
405526	5525	0x1595	1	Signal #6 Byte Position	Byte	1	0...8	N/A	R/W	Byte position of the 1-st CAN output signal
405527	5526	0x1596	1	Signal #6 Bit Position	Byte	1	0...8	N/A	R/W	Bit position of the 1-st CAN output signal
405528	5527	0x1597	2	Signal #6 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
405530	5529	0x1599	2	Signal #6 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
405532	5531	0x159B	1	Signal #7 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
405533	5532	0x159C	1	Signal #7 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
405534	5533	0x159D	1	Signal #7 Source Number	Byte	1	Depends on control source	N/A	R/W	
405535	5534	0x159E	1	Signal #7 Size	Byte	1	0...32	bit	R/W	Size of the 1-st CAN output signal
405536	5535	0x159F	1	Signal #7 Byte Position	Byte	1	0...8	N/A	R/W	Byte position of the 1-st CAN output signal
405537	5536	0x15A0	1	Signal #7 Bit Position	Byte	1	0...8	N/A	R/W	Bit position of the 1-st CAN output signal
405538	5537	0x15A1	2	Signal #7 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
405540	5539	0x15A3	2	Signal #7 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
405542	5541	0x15A5	1	Signal #8 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
405543	5542	0x15A6	1	Signal #8 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
405544	5543	0x15A7	1	Signal #8 Source Number	Byte	1	Depends on control source	N/A	R/W	
405545	5544	0x15A8	1	Signal #8 Size	Byte	1	0...32	bit	R/W	Size of the 1-st CAN output signal

405546	5545	0x15A9	1	Signal #8 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
405547	5546	0x15AA	1	Signal #8 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
405548	5547	0x15AB	2	Signal #8 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
405550	5549	0x15AD	2	Signal #8 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
405552	5551	0x15AF	1	Signal #9 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
405553	5552	0x15B0	1	Signal #9 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
405554	5553	0x15B1	1	Signal #9 Source Number	Byte	1	Depends on control source	N/A	R/W	
405555	5554	0x15B2	1	Signal #9 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
405556	5555	0x15B3	1	Signal #9 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
405557	5556	0x15B4	1	Signal #9 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
405558	5557	0x15B5	2	Signal #9 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
405560	5559	0x15B7	2	Signal #9 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
405562	5561	0x15B9	1	Signal #10 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
405563	5562	0x15BA	1	Signal #10 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
405564	5563	0x15BB	1	Signal #10 Source Number	Byte	1	Depends on control source	N/A	R/W	
405565	5564	0x15BC	1	Signal #10 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
405566	5565	0x15BD	1	Signal #10 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
405567	5566	0x15BE	1	Signal #10 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
405568	5567	0x15BF	2	Signal #10 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
405570	5569	0x15C1	2	Signal #10 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
405572	5571	0x15C3	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>CAN Transmit 5</b>										
405580	5579	0x15CB	1	Transmission Enable	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	CAN message PGN
405581	5580	0x15CC	2	PGN	Double	0xFFFF	Any J1939 PGN value	N/A	R/W	Transmission Enable. Enables the CAN output message
405583	5582	0x15CE	1	Transmission Rate	Word	0	0..10000	ms	R/W	CAN output message transmission rate. If 0 – transmission
405584	5583	0x15CF	1	Destination Address	Byte	255	0..255	N/A	R/W	Destination address of the PDU1 PGN messages
405585	5584	0x15D0	1	Length	Byte	8	0..8	byte	R/W	CAN message data frame length
405586	5585	0x15D1	1	Priority	Byte	6	0..7	N/A	R/W	CAN message priority
405587	5586	0x15D2	1	Signal #1 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 1-st CAN output signal
405588	5587	0x15D3	1	Signal #1 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 1-st CAN output signal
405589	5588	0x15D4	1	Signal #1 Source Number	Byte	1	Depends on control source	N/A	R/W	
405590	5589	0x15D5	1	Signal #1 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
405591	5590	0x15D6	1	Signal #1 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
405592	5591	0x15D7	1	Signal #1 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
405593	5592	0x15D8	2	Signal #1 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 1-st CAN continuous output signal
405595	5594	0x15DA	2	Signal #1 Offset	Float	0	Any value	signal units	R/W	Offset of the 1-st CAN continuous output signal
405597	5596	0x15DC	1	Signal #2 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 2-nd CAN output signal
405598	5597	0x15DD	1	Signal #2 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 2-nd CAN output signal
405599	5598	0x15DE	1	Signal #2 Source Number	Byte	1	Depends on control source	N/A	R/W	
405600	5599	0x15DF	1	Signal #2 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
405601	5600	0x15E0	1	Signal #2 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
405602	5601	0x15E1	1	Signal #2 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
405603	5602	0x15E2	2	Signal #2 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 2-nd CAN continuous output signal
405605	5604	0x15E4	2	Signal #2 Offset	Float	0	Any value	signal units	R/W	Offset of the 2-nd CAN continuous output signal
405607	5606	0x15E6	1	Signal #3 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 3-rd CAN output signal
405608	5607	0x15E7	1	Signal #3 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 3-rd CAN output signal
405609	5608	0x15E8	1	Signal #3 Source Number	Byte	1	Depends on control source	N/A	R/W	
405610	5609	0x15E9	1	Signal #3 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
405611	5610	0x15EA	1	Signal #3 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
405612	5611	0x15EB	1	Signal #3 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal



405613	5612	0x15EC	2	Signal #3 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 3-rd CAN continuous output signal
405615	5614	0x15EE	2	Signal #3 Offset	Float	0	Any value	signal units	R/W	Offset of the 3-rd CAN continuous output signal
405617	5616	0x15F0	1	Signal #4 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
405618	5617	0x15F1	1	Signal #4 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
405619	5618	0x15F2	1	Signal #4 Source Number	Byte	1	Depends on control source	N/A	R/W	
405620	5619	0x15F3	1	Signal #4 Size	Byte	1	0...32	bit	R/W	Size of the 1-st CAN output signal
405621	5620	0x15F4	1	Signal #4 Byte Position	Byte	1	0...8	N/A	R/W	Byte position of the 1-st CAN output signal
405622	5621	0x15F5	1	Signal #4 Bit Position	Byte	1	0...8	N/A	R/W	Bit position of the 1-st CAN output signal
405623	5622	0x15F6	2	Signal #4 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
405625	5624	0x15F8	2	Signal #4 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
405627	5626	0x15FA	1	Signal #5 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
405628	5627	0x15FB	1	Signal #5 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
405629	5628	0x15FC	1	Signal #5 Source Number	Byte	1	Depends on control source	N/A	R/W	
405630	5629	0x15FD	1	Signal #5 Size	Byte	1	0...32	bit	R/W	Size of the 1-st CAN output signal
405631	5630	0x15FE	1	Signal #5 Byte Position	Byte	1	0...8	N/A	R/W	Byte position of the 1-st CAN output signal
405632	5631	0x15FF	1	Signal #5 Bit Position	Byte	1	0...8	N/A	R/W	Bit position of the 1-st CAN output signal
405633	5632	0x1600	2	Signal #5 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
405635	5634	0x1602	2	Signal #5 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
405637	5636	0x1604	1	Signal #6 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
405638	5637	0x1605	1	Signal #6 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
405639	5638	0x1606	1	Signal #6 Source Number	Byte	1	Depends on control source	N/A	R/W	
405640	5639	0x1607	1	Signal #6 Size	Byte	1	0...32	bit	R/W	Size of the 1-st CAN output signal
405641	5640	0x1608	1	Signal #6 Byte Position	Byte	1	0...8	N/A	R/W	Byte position of the 1-st CAN output signal
405642	5641	0x1609	1	Signal #6 Bit Position	Byte	1	0...8	N/A	R/W	Bit position of the 1-st CAN output signal
405643	5642	0x160A	2	Signal #6 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
405645	5644	0x160C	2	Signal #6 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
405647	5646	0x160E	1	Signal #7 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
405648	5647	0x160F	1	Signal #7 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
405649	5648	0x1610	1	Signal #7 Source Number	Byte	1	Depends on control source	N/A	R/W	
405650	5649	0x1611	1	Signal #7 Size	Byte	1	0...32	bit	R/W	Size of the 1-st CAN output signal
405651	5650	0x1612	1	Signal #7 Byte Position	Byte	1	0...8	N/A	R/W	Byte position of the 1-st CAN output signal
405652	5651	0x1613	1	Signal #7 Bit Position	Byte	1	0...8	N/A	R/W	Bit position of the 1-st CAN output signal
405653	5652	0x1614	2	Signal #7 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
405655	5654	0x1616	2	Signal #7 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
405657	5656	0x1618	1	Signal #8 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
405658	5657	0x1619	1	Signal #8 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
405659	5658	0x161A	1	Signal #8 Source Number	Byte	1	Depends on control source	N/A	R/W	
405660	5659	0x161B	1	Signal #8 Size	Byte	1	0...32	bit	R/W	Size of the 1-st CAN output signal
405661	5660	0x161C	1	Signal #8 Byte Position	Byte	1	0...8	N/A	R/W	Byte position of the 1-st CAN output signal
405662	5661	0x161D	1	Signal #8 Bit Position	Byte	1	0...8	N/A	R/W	Bit position of the 1-st CAN output signal
405663	5662	0x161E	2	Signal #8 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
405665	5664	0x1620	2	Signal #8 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
405667	5666	0x1622	1	Signal #9 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
405668	5667	0x1623	1	Signal #9 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
405669	5668	0x1624	1	Signal #9 Source Number	Byte	1	Depends on control source	N/A	R/W	
405670	5669	0x1625	1	Signal #9 Size	Byte	1	0...32	bit	R/W	Size of the 1-st CAN output signal
405671	5670	0x1626	1	Signal #9 Byte Position	Byte	1	0...8	N/A	R/W	Byte position of the 1-st CAN output signal
405672	5671	0x1627	1	Signal #9 Bit Position	Byte	1	0...8	N/A	R/W	Bit position of the 1-st CAN output signal
405673	5672	0x1628	2	Signal #9 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
405675	5674	0x162A	2	Signal #9 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
405677	5676	0x162C	1	Signal #10 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal



405745	5744	0x1670	1	Signal #5 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
405746	5745	0x1671	1	Signal #5 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
405747	5746	0x1672	1	Signal #5 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
405748	5747	0x1673	2	Signal #5 Resolution	Float	1	Any value	signal units / Hz	R/W	Resolution of the 4-th CAN continuous output signal
405750	5749	0x1675	2	Signal #5 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
405752	5751	0x1677	1	Signal #6 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
405753	5752	0x1678	1	Signal #6 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
405754	5753	0x1679	1	Signal #6 Source Number	Byte	1	Depends on control source	N/A	R/W	
405755	5754	0x167A	1	Signal #6 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
405756	5755	0x167B	1	Signal #6 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
405757	5756	0x167C	1	Signal #6 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
405758	5757	0x167D	2	Signal #6 Resolution	Float	1	Any value	signal units / Hz	R/W	Resolution of the 4-th CAN continuous output signal
405760	5759	0x167F	2	Signal #6 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
405762	5761	0x1681	1	Signal #7 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
405763	5762	0x1682	1	Signal #7 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
405764	5763	0x1683	1	Signal #7 Source Number	Byte	1	Depends on control source	N/A	R/W	
405765	5764	0x1684	1	Signal #7 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
405766	5765	0x1685	1	Signal #7 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
405767	5766	0x1686	1	Signal #7 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
405768	5767	0x1687	2	Signal #7 Resolution	Float	1	Any value	signal units / Hz	R/W	Resolution of the 4-th CAN continuous output signal
405770	5769	0x1689	2	Signal #7 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
405772	5771	0x168B	1	Signal #8 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
405773	5772	0x168C	1	Signal #8 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
405774	5773	0x168D	1	Signal #8 Source Number	Byte	1	Depends on control source	N/A	R/W	
405775	5774	0x168E	1	Signal #8 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
405776	5775	0x168F	1	Signal #8 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
405777	5776	0x1690	1	Signal #8 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
405778	5777	0x1691	2	Signal #8 Resolution	Float	1	Any value	signal units / Hz	R/W	Resolution of the 4-th CAN continuous output signal
405780	5779	0x1693	2	Signal #8 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
405782	5781	0x1695	1	Signal #9 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
405783	5782	0x1696	1	Signal #9 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
405784	5783	0x1697	1	Signal #9 Source Number	Byte	1	Depends on control source	N/A	R/W	
405785	5784	0x1698	1	Signal #9 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
405786	5785	0x1699	1	Signal #9 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
405787	5786	0x169A	1	Signal #9 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
405788	5787	0x169B	2	Signal #9 Resolution	Float	1	Any value	signal units / Hz	R/W	Resolution of the 4-th CAN continuous output signal
405790	5789	0x169D	2	Signal #9 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
405792	5791	0x169F	1	Signal #10 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
405793	5792	0x16A0	1	Signal #10 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
405794	5793	0x16A1	1	Signal #10 Source Number	Byte	1	Depends on control source	N/A	R/W	
405795	5794	0x16A2	1	Signal #10 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
405796	5795	0x16A3	1	Signal #10 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
405797	5796	0x16A4	1	Signal #10 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
405798	5797	0x16A5	2	Signal #10 Resolution	Float	1	Any value	signal units / Hz	R/W	Resolution of the 4-th CAN continuous output signal
405800	5799	0x16A7	2	Signal #10 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
405802	5801	0x16A9	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>CAN Transmit 7</b>										
405810	5809	0x16B1	1	Transmission Enable	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	CAN message PGN
405811	5810	0x16B2	2	PGN	Double	0xFFFF	Any J1939 PGN value	N/A	R/W	Transmission Enable. Enables the CAN output mess
405813	5812	0x16B4	1	Transmission Rate	Word	0	0..10000	ms	R/W	CAN output message transmission rate. If 0 – trans
405814	5813	0x16B5	1	Destination Address	Byte	255	0..255	N/A	R/W	Destination address of the PDU1 PGN messages

405815	5814	0x16B6	1	Length	Byte	8	0..8	byte	R/W	CAN message data frame length
405816	5815	0x16B7	1	Priority	Byte	6	0..7	N/A	R/W	CAN message priority
405817	5816	0x16B8	1	Signal #1 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 1-st CAN output signal
405818	5817	0x16B9	1	Signal #1 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 1-st CAN output signal
405819	5818	0x16BA	1	Signal #1 Source Number	Byte	1	Depends on control source	N/A	R/W	
405820	5819	0x16BB	1	Signal #1 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
405821	5820	0x16BC	1	Signal #1 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
405822	5821	0x16BD	1	Signal #1 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
405823	5822	0x16BE	2	Signal #1 Resolution	Float	1	Any value	signal units / h	R/W	Resolution of the 1-st CAN continuous output signal
405825	5824	0x16C0	2	Signal #1 Offset	Float	0	Any value	signal units	R/W	Offset of the 1-st CAN continuous output signal
405827	5826	0x16C2	1	Signal #2 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 2-nd CAN output signal
405828	5827	0x16C3	1	Signal #2 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 2-nd CAN output signal
405829	5828	0x16C4	1	Signal #2 Source Number	Byte	1	Depends on control source	N/A	R/W	
405830	5829	0x16C5	1	Signal #2 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
405831	5830	0x16C6	1	Signal #2 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
405832	5831	0x16C7	1	Signal #2 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
405833	5832	0x16C8	2	Signal #2 Resolution	Float	1	Any value	signal units / h	R/W	Resolution of the 2-nd CAN continuous output signal
405835	5834	0x16CA	2	Signal #2 Offset	Float	0	Any value	signal units	R/W	Offset of the 2-nd CAN continuous output signal
405837	5836	0x16CC	1	Signal #3 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 3-rd CAN output signal
405838	5837	0x16CD	1	Signal #3 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 3-rd CAN output signal
405839	5838	0x16CE	1	Signal #3 Source Number	Byte	1	Depends on control source	N/A	R/W	
405840	5839	0x16CF	1	Signal #3 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
405841	5840	0x16D0	1	Signal #3 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
405842	5841	0x16D1	1	Signal #3 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
405843	5842	0x16D2	2	Signal #3 Resolution	Float	1	Any value	signal units / h	R/W	Resolution of the 3-rd CAN continuous output signal
405845	5844	0x16D4	2	Signal #3 Offset	Float	0	Any value	signal units	R/W	Offset of the 3-rd CAN continuous output signal
405847	5846	0x16D6	1	Signal #4 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
405848	5847	0x16D7	1	Signal #4 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
405849	5848	0x16D8	1	Signal #4 Source Number	Byte	1	Depends on control source	N/A	R/W	
405850	5849	0x16D9	1	Signal #4 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
405851	5850	0x16DA	1	Signal #4 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
405852	5851	0x16DB	1	Signal #4 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
405853	5852	0x16DC	2	Signal #4 Resolution	Float	1	Any value	signal units / h	R/W	Resolution of the 4-th CAN continuous output signal
405855	5854	0x16DE	2	Signal #4 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
405857	5856	0x16E0	1	Signal #5 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
405858	5857	0x16E1	1	Signal #5 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
405859	5858	0x16E2	1	Signal #5 Source Number	Byte	1	Depends on control source	N/A	R/W	
405860	5859	0x16E3	1	Signal #5 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
405861	5860	0x16E4	1	Signal #5 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
405862	5861	0x16E5	1	Signal #5 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
405863	5862	0x16E6	2	Signal #5 Resolution	Float	1	Any value	signal units / h	R/W	Resolution of the 4-th CAN continuous output signal
405865	5864	0x16E8	2	Signal #5 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
405867	5866	0x16EA	1	Signal #6 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
405868	5867	0x16EB	1	Signal #6 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
405869	5868	0x16EC	1	Signal #6 Source Number	Byte	1	Depends on control source	N/A	R/W	
405870	5869	0x16ED	1	Signal #6 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
405871	5870	0x16EE	1	Signal #6 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
405872	5871	0x16EF	1	Signal #6 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
405873	5872	0x16F0	2	Signal #6 Resolution	Float	1	Any value	signal units / h	R/W	Resolution of the 4-th CAN continuous output signal
405875	5874	0x16F2	2	Signal #6 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal

405877	5876	0x16F4	1	Signal #7 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
405878	5877	0x16F5	1	Signal #7 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
405879	5878	0x16F6	1	Signal #7 Source Number	Byte	1	Depends on control source	N/A	R/W	
405880	5879	0x16F7	1	Signal #7 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
405881	5880	0x16F8	1	Signal #7 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
405882	5881	0x16F9	1	Signal #7 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
405883	5882	0x16FA	2	Signal #7 Resolution	Float	1	Any value	signal units / Hz	R/W	Resolution of the 4-th CAN continuous output signal
405885	5884	0x16FC	2	Signal #7 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
405887	5886	0x16FE	1	Signal #8 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
405888	5887	0x16FF	1	Signal #8 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
405889	5888	0x1700	1	Signal #8 Source Number	Byte	1	Depends on control source	N/A	R/W	
405890	5889	0x1701	1	Signal #8 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
405891	5890	0x1702	1	Signal #8 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
405892	5891	0x1703	1	Signal #8 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
405893	5892	0x1704	2	Signal #8 Resolution	Float	1	Any value	signal units / Hz	R/W	Resolution of the 4-th CAN continuous output signal
405895	5894	0x1706	2	Signal #8 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
405897	5896	0x1708	1	Signal #9 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
405898	5897	0x1709	1	Signal #9 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
405899	5898	0x170A	1	Signal #9 Source Number	Byte	1	Depends on control source	N/A	R/W	
405900	5899	0x170B	1	Signal #9 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
405901	5900	0x170C	1	Signal #9 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
405902	5901	0x170D	1	Signal #9 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
405903	5902	0x170E	2	Signal #9 Resolution	Float	1	Any value	signal units / Hz	R/W	Resolution of the 4-th CAN continuous output signal
405905	5904	0x1710	2	Signal #9 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
405907	5906	0x1712	1	Signal #10 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
405908	5907	0x1713	1	Signal #10 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
405909	5908	0x1714	1	Signal #10 Source Number	Byte	1	Depends on control source	N/A	R/W	
405910	5909	0x1715	1	Signal #10 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
405911	5910	0x1716	1	Signal #10 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
405912	5911	0x1717	1	Signal #10 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
405913	5912	0x1718	2	Signal #10 Resolution	Float	1	Any value	signal units / Hz	R/W	Resolution of the 4-th CAN continuous output signal
405915	5914	0x171A	2	Signal #10 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
405917	5916	0x171C	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>CAN Transmit 8</b>										
405925	5924	0x1724	1	Transmission Enable	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	CAN message PGN
405926	5925	0x1725	2	PGN	Double	0xFFFF	Any J1939 PGN value	N/A	R/W	Transmission Enable. Enables the CAN output mess
405928	5927	0x1727	1	Transmission Rate	Word	0	0..10000	ms	R/W	CAN output message transmission rate. If 0 – trans
405929	5928	0x1728	1	Destination Address	Byte	255	0..255	N/A	R/W	Destination address of the PDU1 PGN messages
405930	5929	0x1729	1	Length	Byte	8	0..8	byte	R/W	CAN message data frame length
405931	5930	0x172A	1	Priority	Byte	6	0..7	N/A	R/W	CAN message priority
405932	5931	0x172B	1	Signal #1 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 1-st CAN output signal
405933	5932	0x172C	1	Signal #1 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 1-st CAN output signal
405934	5933	0x172D	1	Signal #1 Source Number	Byte	1	Depends on control source	N/A	R/W	
405935	5934	0x172E	1	Signal #1 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
405936	5935	0x172F	1	Signal #1 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
405937	5936	0x1730	1	Signal #1 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
405938	5937	0x1731	2	Signal #1 Resolution	Float	1	Any value	signal units / Hz	R/W	Resolution of the 1-st CAN continuous output signal
405940	5939	0x1733	2	Signal #1 Offset	Float	0	Any value	signal units	R/W	Offset of the 1-st CAN continuous output signal
405942	5941	0x1735	1	Signal #2 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 2-nd CAN output signal
405943	5942	0x1736	1	Signal #2 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 2-nd CAN output signal

405944	5943	0x1737	1	Signal #2 Source Number	Byte	1	Depends on control source	N/A	R/W	
405945	5944	0x1738	1	Signal #2 Size	Byte	1	0...32	bit	R/W	Size of the 1-st CAN output signal
405946	5945	0x1739	1	Signal #2 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
405947	5946	0x173A	1	Signal #2 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
405948	5947	0x173B	2	Signal #2 Resolution	Float	1	Any value	signal units / h	R/W	Resolution of the 2-nd CAN continuous output signal
405950	5949	0x173D	2	Signal #2 Offset	Float	0	Any value	signal units	R/W	Offset of the 2-nd CAN continuous output signal
405952	5951	0x173F	1	Signal #3 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 3-rd CAN output signal
405953	5952	0x1740	1	Signal #3 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 3-rd CAN output signal
405954	5953	0x1741	1	Signal #3 Source Number	Byte	1	Depends on control source	N/A	R/W	
405955	5954	0x1742	1	Signal #3 Size	Byte	1	0...32	bit	R/W	Size of the 1-st CAN output signal
405956	5955	0x1743	1	Signal #3 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
405957	5956	0x1744	1	Signal #3 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
405958	5957	0x1745	2	Signal #3 Resolution	Float	1	Any value	signal units / h	R/W	Resolution of the 3-rd CAN continuous output signal
405960	5959	0x1747	2	Signal #3 Offset	Float	0	Any value	signal units	R/W	Offset of the 3-rd CAN continuous output signal
405962	5961	0x1749	1	Signal #4 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
405963	5962	0x174A	1	Signal #4 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
405964	5963	0x174B	1	Signal #4 Source Number	Byte	1	Depends on control source	N/A	R/W	
405965	5964	0x174C	1	Signal #4 Size	Byte	1	0...32	bit	R/W	Size of the 1-st CAN output signal
405966	5965	0x174D	1	Signal #4 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
405967	5966	0x174E	1	Signal #4 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
405968	5967	0x174F	2	Signal #4 Resolution	Float	1	Any value	signal units / h	R/W	Resolution of the 4-th CAN continuous output signal
405970	5969	0x1751	2	Signal #4 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
405972	5971	0x1753	1	Signal #5 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
405973	5972	0x1754	1	Signal #5 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
405974	5973	0x1755	1	Signal #5 Source Number	Byte	1	Depends on control source	N/A	R/W	
405975	5974	0x1756	1	Signal #5 Size	Byte	1	0...32	bit	R/W	Size of the 1-st CAN output signal
405976	5975	0x1757	1	Signal #5 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
405977	5976	0x1758	1	Signal #5 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
405978	5977	0x1759	2	Signal #5 Resolution	Float	1	Any value	signal units / h	R/W	Resolution of the 4-th CAN continuous output signal
405980	5979	0x175B	2	Signal #5 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
405982	5981	0x175D	1	Signal #6 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
405983	5982	0x175E	1	Signal #6 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
405984	5983	0x175F	1	Signal #6 Source Number	Byte	1	Depends on control source	N/A	R/W	
405985	5984	0x1760	1	Signal #6 Size	Byte	1	0...32	bit	R/W	Size of the 1-st CAN output signal
405986	5985	0x1761	1	Signal #6 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
405987	5986	0x1762	1	Signal #6 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
405988	5987	0x1763	2	Signal #6 Resolution	Float	1	Any value	signal units / h	R/W	Resolution of the 4-th CAN continuous output signal
405990	5989	0x1765	2	Signal #6 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
405992	5991	0x1767	1	Signal #7 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
405993	5992	0x1768	1	Signal #7 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
405994	5993	0x1769	1	Signal #7 Source Number	Byte	1	Depends on control source	N/A	R/W	
405995	5994	0x176A	1	Signal #7 Size	Byte	1	0...32	bit	R/W	Size of the 1-st CAN output signal
405996	5995	0x176B	1	Signal #7 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
405997	5996	0x176C	1	Signal #7 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
405998	5997	0x176D	2	Signal #7 Resolution	Float	1	Any value	signal units / h	R/W	Resolution of the 4-th CAN continuous output signal
406000	5999	0x176F	2	Signal #7 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
406002	6001	0x1771	1	Signal #8 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
406003	6002	0x1772	1	Signal #8 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
406004	6003	0x1773	1	Signal #8 Source Number	Byte	1	Depends on control source	N/A	R/W	
406005	6004	0x1774	1	Signal #8 Size	Byte	1	0...32	bit	R/W	Size of the 1-st CAN output signal
406006	6005	0x1775	1	Signal #8 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal

406007	6006	0x1776	1	Signal #8 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
406008	6007	0x1777	2	Signal #8 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
406010	6009	0x1779	2	Signal #8 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
406012	6011	0x177B	1	Signal #9 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
406013	6012	0x177C	1	Signal #9 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
406014	6013	0x177D	1	Signal #9 Source Number	Byte	1	Depends on control source	N/A	R/W	
406015	6014	0x177E	1	Signal #9 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
406016	6015	0x177F	1	Signal #9 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
406017	6016	0x1780	1	Signal #9 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
406018	6017	0x1781	2	Signal #9 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
406020	6019	0x1783	2	Signal #9 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
406022	6021	0x1785	1	Signal #10 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
406023	6022	0x1786	1	Signal #10 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
406024	6023	0x1787	1	Signal #10 Source Number	Byte	1	Depends on control source	N/A	R/W	
406025	6024	0x1788	1	Signal #10 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
406026	6025	0x1789	1	Signal #10 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
406027	6026	0x178A	1	Signal #10 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
406028	6027	0x178B	2	Signal #10 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
406030	6029	0x178D	2	Signal #10 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
406032	6031	0x178F	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>CAN Transmit 9</b>										
406040	6039	0x1797	1	Transmission Enable	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	CAN message PGN
406041	6040	0x1798	2	PGN	Double	0xFFFF	Any J1939 PGN value	N/A	R/W	Transmission Enable. Enables the CAN output mess
406043	6042	0x179A	1	Transmission Rate	Word	0	0..10000	ms	R/W	CAN output message transmission rate. If 0 – trans
406044	6043	0x179B	1	Destination Address	Byte	255	0..255	N/A	R/W	Destination address of the PDU1 PGN messages
406045	6044	0x179C	1	Length	Byte	8	0..8	byte	R/W	CAN message data frame length
406046	6045	0x179D	1	Priority	Byte	6	0..7	N/A	R/W	CAN message priority
406047	6046	0x179E	1	Signal #1 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 1-st CAN output signal
406048	6047	0x179F	1	Signal #1 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 1-st CAN output signal
406049	6048	0x17A0	1	Signal #1 Source Number	Byte	1	Depends on control source	N/A	R/W	
406050	6049	0x17A1	1	Signal #1 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
406051	6050	0x17A2	1	Signal #1 Bit Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
406052	6051	0x17A3	1	Signal #1 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
406053	6052	0x17A4	2	Signal #1 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 1-st CAN continuous output signal
406055	6054	0x17A6	2	Signal #1 Offset	Float	0	Any value	signal units	R/W	Offset of the 1-st CAN continuous output signal
406057	6056	0x17A8	1	Signal #2 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 2-nd CAN output signal
406058	6057	0x17A9	1	Signal #2 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 2-nd CAN output signal
406059	6058	0x17AA	1	Signal #2 Source Number	Byte	1	Depends on control source	N/A	R/W	
406060	6059	0x17AB	1	Signal #2 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
406061	6060	0x17AC	1	Signal #2 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
406062	6061	0x17AD	1	Signal #2 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
406063	6062	0x17AE	2	Signal #2 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 2-nd CAN continuous output signal
406065	6064	0x17B0	2	Signal #2 Offset	Float	0	Any value	signal units	R/W	Offset of the 2-nd CAN continuous output signal
406067	6066	0x17B2	1	Signal #3 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 3-rd CAN output signal
406068	6067	0x17B3	1	Signal #3 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 3-rd CAN output signal
406069	6068	0x17B4	1	Signal #3 Source Number	Byte	1	Depends on control source	N/A	R/W	
406070	6069	0x17B5	1	Signal #3 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
406071	6070	0x17B6	1	Signal #3 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
406072	6071	0x17B7	1	Signal #3 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
406073	6072	0x17B8	2	Signal #3 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 3-rd CAN continuous output signal

406075	6074	0x17BA	2	Signal #3 Offset	Float	0	Any value	signal units	R/W	Offset of the 3-rd CAN continuous output signal
406077	6076	0x17BC	1	Signal #4 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
406078	6077	0x17BD	1	Signal #4 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
406079	6078	0x17BE	1	Signal #4 Source Number	Byte	1	Depends on control source	N/A	R/W	
406080	6079	0x17BF	1	Signal #4 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
406081	6080	0x17C0	1	Signal #4 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
406082	6081	0x17C1	1	Signal #4 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
406083	6082	0x17C2	2	Signal #4 Resolution	Float	1	Any value	signal units / t	R/W	Resolution of the 4-th CAN continuous output signal
406085	6084	0x17C4	2	Signal #4 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
406087	6086	0x17C6	1	Signal #5 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
406088	6087	0x17C7	1	Signal #5 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
406089	6088	0x17C8	1	Signal #5 Source Number	Byte	1	Depends on control source	N/A	R/W	
406090	6089	0x17C9	1	Signal #5 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
406091	6090	0x17CA	1	Signal #5 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
406092	6091	0x17CB	1	Signal #5 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
406093	6092	0x17CC	2	Signal #5 Resolution	Float	1	Any value	signal units / t	R/W	Resolution of the 4-th CAN continuous output signal
406095	6094	0x17CE	2	Signal #5 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
406097	6096	0x17D0	1	Signal #6 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
406098	6097	0x17D1	1	Signal #6 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
406099	6098	0x17D2	1	Signal #6 Source Number	Byte	1	Depends on control source	N/A	R/W	
406100	6099	0x17D3	1	Signal #6 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
406101	6100	0x17D4	1	Signal #6 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
406102	6101	0x17D5	1	Signal #6 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
406103	6102	0x17D6	2	Signal #6 Resolution	Float	1	Any value	signal units / t	R/W	Resolution of the 4-th CAN continuous output signal
406105	6104	0x17D8	2	Signal #6 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
406107	6106	0x17DA	1	Signal #7 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
406108	6107	0x17DB	1	Signal #7 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
406109	6108	0x17DC	1	Signal #7 Source Number	Byte	1	Depends on control source	N/A	R/W	
406110	6109	0x17DD	1	Signal #7 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
406111	6110	0x17DE	1	Signal #7 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
406112	6111	0x17DF	1	Signal #7 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
406113	6112	0x17E0	2	Signal #7 Resolution	Float	1	Any value	signal units / t	R/W	Resolution of the 4-th CAN continuous output signal
406115	6114	0x17E2	2	Signal #7 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
406117	6116	0x17E4	1	Signal #8 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
406118	6117	0x17E5	1	Signal #8 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
406119	6118	0x17E6	1	Signal #8 Source Number	Byte	1	Depends on control source	N/A	R/W	
406120	6119	0x17E7	1	Signal #8 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
406121	6120	0x17E8	1	Signal #8 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
406122	6121	0x17E9	1	Signal #8 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
406123	6122	0x17EA	2	Signal #8 Resolution	Float	1	Any value	signal units / t	R/W	Resolution of the 4-th CAN continuous output signal
406125	6124	0x17EC	2	Signal #8 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
406127	6126	0x17EE	1	Signal #9 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
406128	6127	0x17EF	1	Signal #9 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
406129	6128	0x17F0	1	Signal #9 Source Number	Byte	1	Depends on control source	N/A	R/W	
406130	6129	0x17F1	1	Signal #9 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
406131	6130	0x17F2	1	Signal #9 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
406132	6131	0x17F3	1	Signal #9 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
406133	6132	0x17F4	2	Signal #9 Resolution	Float	1	Any value	signal units / t	R/W	Resolution of the 4-th CAN continuous output signal
406135	6134	0x17F6	2	Signal #9 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
406137	6136	0x17F8	1	Signal #10 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
406138	6137	0x17F9	1	Signal #10 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal



406139	6138	0x17FA	1	Signal #10 Source Number	Byte	1	Depends on control source	N/A	R/W	
406140	6139	0x17FB	1	Signal #10 Size	Byte	1	0...32	bit	R/W	Size of the 1-st CAN output signal
406141	6140	0x17FC	1	Signal #10 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
406142	6141	0x17FD	1	Signal #10 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
406143	6142	0x17FE	2	Signal #10 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
406145	6144	0x1800	2	Signal #10 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
406147	6146	0x1802	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>CAN Transmit 10</b>										
406155	6154	0x180A	1	Transmission Enable	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	CAN message PGN
406156	6155	0x180B	2	PGN	Double	0xFFFF	Any J1939 PGN value	N/A	R/W	Transmission Enable. Enables the CAN output message
406158	6157	0x180D	1	Transmission Rate	Word	0	0...10000	ms	R/W	CAN output message transmission rate. If 0 – transmission rate is 1000000
406159	6158	0x180E	1	Destination Address	Byte	255	0..255	N/A	R/W	Destination address of the PDU1 PGN messages
406160	6159	0x180F	1	Length	Byte	8	0..8	byte	R/W	CAN message data frame length
406161	6160	0x1810	1	Priority	Byte	6	0..7	N/A	R/W	CAN message priority
406162	6161	0x1811	1	Signal #1 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 - Analog	N/A	R/W	Type of the 1-st CAN output signal
406163	6162	0x1812	1	Signal #1 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 1-st CAN output signal
406164	6163	0x1813	1	Signal #1 Source Number	Byte	1	Depends on control source	N/A	R/W	
406165	6164	0x1814	1	Signal #1 Size	Byte	1	0...32	bit	R/W	Size of the 1-st CAN output signal
406166	6165	0x1815	1	Signal #1 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
406167	6166	0x1816	1	Signal #1 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
406168	6167	0x1817	2	Signal #1 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 1-st CAN continuous output signal
406170	6169	0x1819	2	Signal #1 Offset	Float	0	Any value	signal units	R/W	Offset of the 1-st CAN continuous output signal
406172	6171	0x181B	1	Signal #2 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 - Analog	N/A	R/W	Type of the 2-nd CAN output signal
406173	6172	0x181C	1	Signal #2 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 2-nd CAN output signal
406174	6173	0x181D	1	Signal #2 Source Number	Byte	1	Depends on control source	N/A	R/W	
406175	6174	0x181E	1	Signal #2 Size	Byte	1	0...32	bit	R/W	Size of the 1-st CAN output signal
406176	6175	0x181F	1	Signal #2 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
406177	6176	0x1820	1	Signal #2 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
406178	6177	0x1821	2	Signal #2 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 2-nd CAN continuous output signal
406180	6179	0x1823	2	Signal #2 Offset	Float	0	Any value	signal units	R/W	Offset of the 2-nd CAN continuous output signal
406182	6181	0x1825	1	Signal #3 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 - Analog	N/A	R/W	Type of the 3-rd CAN output signal
406183	6182	0x1826	1	Signal #3 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 3-rd CAN output signal
406184	6183	0x1827	1	Signal #3 Source Number	Byte	1	Depends on control source	N/A	R/W	
406185	6184	0x1828	1	Signal #3 Size	Byte	1	0...32	bit	R/W	Size of the 1-st CAN output signal
406186	6185	0x1829	1	Signal #3 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
406187	6186	0x182A	1	Signal #3 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
406188	6187	0x182B	2	Signal #3 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 3-rd CAN continuous output signal
406190	6189	0x182D	2	Signal #3 Offset	Float	0	Any value	signal units	R/W	Offset of the 3-rd CAN continuous output signal
406192	6191	0x182F	1	Signal #4 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 - Analog	N/A	R/W	Type of the 4-th CAN output signal
406193	6192	0x1830	1	Signal #4 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
406194	6193	0x1831	1	Signal #4 Source Number	Byte	1	Depends on control source	N/A	R/W	
406195	6194	0x1832	1	Signal #4 Size	Byte	1	0...32	bit	R/W	Size of the 1-st CAN output signal
406196	6195	0x1833	1	Signal #4 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
406197	6196	0x1834	1	Signal #4 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
406198	6197	0x1835	2	Signal #4 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
406200	6199	0x1837	2	Signal #4 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
406202	6201	0x1839	1	Signal #5 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 - Analog	N/A	R/W	Type of the 4-th CAN output signal
406203	6202	0x183A	1	Signal #5 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
406204	6203	0x183B	1	Signal #5 Source Number	Byte	1	Depends on control source	N/A	R/W	
406205	6204	0x183C	1	Signal #5 Size	Byte	1	0...32	bit	R/W	Size of the 1-st CAN output signal



406276	6275	0x1883	1	Priority	Byte	6	0..7	N/A	R/W	CAN message priority
406277	6276	0x1884	1	Signal #1 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 1-st CAN output signal
406278	6277	0x1885	1	Signal #1 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 1-st CAN output signal
406279	6278	0x1886	1	Signal #1 Source Number	Byte	1	Depends on control source	N/A	R/W	
406280	6279	0x1887	1	Signal #1 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
406281	6280	0x1888	1	Signal #1 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
406282	6281	0x1889	1	Signal #1 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
406283	6282	0x188A	2	Signal #1 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 1-st CAN continuous output signal
406285	6284	0x188C	2	Signal #1 Offset	Float	0	Any value	signal units	R/W	Offset of the 1-st CAN continuous output signal
406287	6286	0x188E	1	Signal #2 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 2-nd CAN output signal
406288	6287	0x188F	1	Signal #2 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 2-nd CAN output signal
406289	6288	0x1890	1	Signal #2 Source Number	Byte	1	Depends on control source	N/A	R/W	
406290	6289	0x1891	1	Signal #2 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
406291	6290	0x1892	1	Signal #2 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
406292	6291	0x1893	1	Signal #2 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
406293	6292	0x1894	2	Signal #2 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 2-nd CAN continuous output signal
406295	6294	0x1896	2	Signal #2 Offset	Float	0	Any value	signal units	R/W	Offset of the 2-nd CAN continuous output signal
406297	6296	0x1898	1	Signal #3 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 3-rd CAN output signal
406298	6297	0x1899	1	Signal #3 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 3-rd CAN output signal
406299	6298	0x189A	1	Signal #3 Source Number	Byte	1	Depends on control source	N/A	R/W	
406300	6299	0x189B	1	Signal #3 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
406301	6300	0x189C	1	Signal #3 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
406302	6301	0x189D	1	Signal #3 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
406303	6302	0x189E	2	Signal #3 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 3-rd CAN continuous output signal
406305	6304	0x18A0	2	Signal #3 Offset	Float	0	Any value	signal units	R/W	Offset of the 3-rd CAN continuous output signal
406307	6306	0x18A2	1	Signal #4 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
406308	6307	0x18A3	1	Signal #4 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
406309	6308	0x18A4	1	Signal #4 Source Number	Byte	1	Depends on control source	N/A	R/W	
406310	6309	0x18A5	1	Signal #4 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
406311	6310	0x18A6	1	Signal #4 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
406312	6311	0x18A7	1	Signal #4 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
406313	6312	0x18A8	2	Signal #4 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
406315	6314	0x18AA	2	Signal #4 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
406317	6316	0x18AC	1	Signal #5 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
406318	6317	0x18AD	1	Signal #5 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
406319	6318	0x18AE	1	Signal #5 Source Number	Byte	1	Depends on control source	N/A	R/W	
406320	6319	0x18AF	1	Signal #5 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
406321	6320	0x18B0	1	Signal #5 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
406322	6321	0x18B1	1	Signal #5 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
406323	6322	0x18B2	2	Signal #5 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
406325	6324	0x18B4	2	Signal #5 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
406327	6326	0x18B6	1	Signal #6 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
406328	6327	0x18B7	1	Signal #6 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
406329	6328	0x18B8	1	Signal #6 Source Number	Byte	1	Depends on control source	N/A	R/W	
406330	6329	0x18B9	1	Signal #6 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
406331	6330	0x18BA	1	Signal #6 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
406332	6331	0x18BB	1	Signal #6 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
406333	6332	0x18BC	2	Signal #6 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
406335	6334	0x18BE	2	Signal #6 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
406337	6336	0x18C0	1	Signal #7 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal

406338	6337	0x18C1	1	Signal #7 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
406339	6338	0x18C2	1	Signal #7 Source Number	Byte	1	Depends on control source	N/A	R/W	
406340	6339	0x18C3	1	Signal #7 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
406341	6340	0x18C4	1	Signal #7 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
406342	6341	0x18C5	1	Signal #7 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
406343	6342	0x18C6	2	Signal #7 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
406345	6344	0x18C8	2	Signal #7 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
406347	6346	0x18CA	1	Signal #8 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
406348	6347	0x18CB	1	Signal #8 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
406349	6348	0x18CC	1	Signal #8 Source Number	Byte	1	Depends on control source	N/A	R/W	
406350	6349	0x18CD	1	Signal #8 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
406351	6350	0x18CE	1	Signal #8 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
406352	6351	0x18CF	1	Signal #8 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
406353	6352	0x18D0	2	Signal #8 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
406355	6354	0x18D2	2	Signal #8 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
406357	6356	0x18D4	1	Signal #9 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
406358	6357	0x18D5	1	Signal #9 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
406359	6358	0x18D6	1	Signal #9 Source Number	Byte	1	Depends on control source	N/A	R/W	
406360	6359	0x18D7	1	Signal #9 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
406361	6360	0x18D8	1	Signal #9 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
406362	6361	0x18D9	1	Signal #9 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
406363	6362	0x18DA	2	Signal #9 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
406365	6364	0x18DC	2	Signal #9 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
406367	6366	0x18DE	1	Signal #10 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
406368	6367	0x18DF	1	Signal #10 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
406369	6368	0x18E0	1	Signal #10 Source Number	Byte	1	Depends on control source	N/A	R/W	
406370	6369	0x18E1	1	Signal #10 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
406371	6370	0x18E2	1	Signal #10 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
406372	6371	0x18E3	1	Signal #10 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
406373	6372	0x18E4	2	Signal #10 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
406375	6374	0x18E6	2	Signal #10 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
406377	6376	0x18E8	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>CAN Transmit 12</b>										
406385	6384	0x18F0	1	Transmission Enable	Byte	0 - No	0 - No, 1 - Yes	N/A	R/W	CAN message PGN
406386	6385	0x18F1	2	PGN	Double	0xFFFF	Any J1939 PGN value	N/A	R/W	Transmission Enable. Enables the CAN output mess
406388	6387	0x18F3	1	Transmission Rate	Word	0	0..10000	ms	R/W	CAN output message transmission rate. If 0 – trans
406389	6388	0x18F4	1	Destination Address	Byte	255	0..255	N/A	R/W	Destination address of the PDU1 PGN messages
406390	6389	0x18F5	1	Length	Byte	8	0..8	byte	R/W	CAN message data frame length
406391	6390	0x18F6	1	Priority	Byte	6	0..7	N/A	R/W	CAN message priority
406392	6391	0x18F7	1	Signal #1 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 1-st CAN output signal
406393	6392	0x18F8	1	Signal #1 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 1-st CAN output signal
406394	6393	0x18F9	1	Signal #1 Source Number	Byte	1	Depends on control source	N/A	R/W	
406395	6394	0x18FA	1	Signal #1 Size	Byte	1	0..32	bit	R/W	Size of the 1-st CAN output signal
406396	6395	0x18FB	1	Signal #1 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
406397	6396	0x18FC	1	Signal #1 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
406398	6397	0x18FD	2	Signal #1 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 1-st CAN continuous output signal
406400	6399	0x18FF	2	Signal #1 Offset	Float	0	Any value	signal units	R/W	Offset of the 1-st CAN continuous output signal
406402	6401	0x1901	1	Signal #2 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 2-nd CAN output signal
406403	6402	0x1902	1	Signal #2 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 2-nd CAN output signal
406404	6403	0x1903	1	Signal #2 Source Number	Byte	1	Depends on control source	N/A	R/W	

406405	6404	0x1904	1	Signal #2 Size	Byte	1	0...32	bit	R/W	Size of the 1-st CAN output signal
406406	6405	0x1905	1	Signal #2 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
406407	6406	0x1906	1	Signal #2 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
406408	6407	0x1907	2	Signal #2 Resolution	Float	1	Any value	signal units / h	R/W	Resolution of the 2-nd CAN continuous output signal
406410	6409	0x1909	2	Signal #2 Offset	Float	0	Any value	signal units	R/W	Offset of the 2-nd CAN continuous output signal
406412	6411	0x190B	1	Signal #3 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 3-rd CAN output signal
406413	6412	0x190C	1	Signal #3 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 3-rd CAN output signal
406414	6413	0x190D	1	Signal #3 Source Number	Byte	1	Depends on control source	N/A	R/W	
406415	6414	0x190E	1	Signal #3 Size	Byte	1	0...32	bit	R/W	Size of the 1-st CAN output signal
406416	6415	0x190F	1	Signal #3 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
406417	6416	0x1910	1	Signal #3 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
406418	6417	0x1911	2	Signal #3 Resolution	Float	1	Any value	signal units / h	R/W	Resolution of the 3-rd CAN continuous output signal
406420	6419	0x1913	2	Signal #3 Offset	Float	0	Any value	signal units	R/W	Offset of the 3-rd CAN continuous output signal
406422	6421	0x1915	1	Signal #4 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
406423	6422	0x1916	1	Signal #4 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
406424	6423	0x1917	1	Signal #4 Source Number	Byte	1	Depends on control source	N/A	R/W	
406425	6424	0x1918	1	Signal #4 Size	Byte	1	0...32	bit	R/W	Size of the 1-st CAN output signal
406426	6425	0x1919	1	Signal #4 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
406427	6426	0x191A	1	Signal #4 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
406428	6427	0x191B	2	Signal #4 Resolution	Float	1	Any value	signal units / h	R/W	Resolution of the 4-th CAN continuous output signal
406430	6429	0x191D	2	Signal #4 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
406432	6431	0x191F	1	Signal #5 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
406433	6432	0x1920	1	Signal #5 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
406434	6433	0x1921	1	Signal #5 Source Number	Byte	1	Depends on control source	N/A	R/W	
406435	6434	0x1922	1	Signal #5 Size	Byte	1	0...32	bit	R/W	Size of the 1-st CAN output signal
406436	6435	0x1923	1	Signal #5 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
406437	6436	0x1924	1	Signal #5 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
406438	6437	0x1925	2	Signal #5 Resolution	Float	1	Any value	signal units / h	R/W	Resolution of the 4-th CAN continuous output signal
406440	6439	0x1927	2	Signal #5 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
406442	6441	0x1929	1	Signal #6 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
406443	6442	0x192A	1	Signal #6 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
406444	6443	0x192B	1	Signal #6 Source Number	Byte	1	Depends on control source	N/A	R/W	
406445	6444	0x192C	1	Signal #6 Size	Byte	1	0...32	bit	R/W	Size of the 1-st CAN output signal
406446	6445	0x192D	1	Signal #6 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
406447	6446	0x192E	1	Signal #6 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
406448	6447	0x192F	2	Signal #6 Resolution	Float	1	Any value	signal units / h	R/W	Resolution of the 4-th CAN continuous output signal
406450	6449	0x1931	2	Signal #6 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
406452	6451	0x1933	1	Signal #7 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
406453	6452	0x1934	1	Signal #7 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
406454	6453	0x1935	1	Signal #7 Source Number	Byte	1	Depends on control source	N/A	R/W	
406455	6454	0x1936	1	Signal #7 Size	Byte	1	0...32	bit	R/W	Size of the 1-st CAN output signal
406456	6455	0x1937	1	Signal #7 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
406457	6456	0x1938	1	Signal #7 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal
406458	6457	0x1939	2	Signal #7 Resolution	Float	1	Any value	signal units / h	R/W	Resolution of the 4-th CAN continuous output signal
406460	6459	0x193B	2	Signal #7 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
406462	6461	0x193D	1	Signal #8 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
406463	6462	0x193E	1	Signal #8 Source	Byte	0 - Not Connect	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
406464	6463	0x193F	1	Signal #8 Source Number	Byte	1	Depends on control source	N/A	R/W	
406465	6464	0x1940	1	Signal #8 Size	Byte	1	0...32	bit	R/W	Size of the 1-st CAN output signal
406466	6465	0x1941	1	Signal #8 Byte Position	Byte	1	0..8	N/A	R/W	Byte position of the 1-st CAN output signal
406467	6466	0x1942	1	Signal #8 Bit Position	Byte	1	0..8	N/A	R/W	Bit position of the 1-st CAN output signal

406468	6467	0x1943	2	Signal #8 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
406470	6469	0x1945	2	Signal #8 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
406472	6471	0x1947	1	Signal #9 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
406473	6472	0x1948	1	Signal #9 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
406474	6473	0x1949	1	Signal #9 Source Number	Byte	1	Depends on control source	N/A	R/W	
406475	6474	0x194A	1	Signal #9 Size	Byte	1	0...32	bit	R/W	Size of the 1-st CAN output signal
406476	6475	0x194B	1	Signal #9 Byte Position	Byte	1	0...8	N/A	R/W	Byte position of the 1-st CAN output signal
406477	6476	0x194C	1	Signal #9 Bit Position	Byte	1	0...8	N/A	R/W	Bit position of the 1-st CAN output signal
406478	6477	0x194D	2	Signal #9 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
406480	6479	0x194F	2	Signal #9 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
406482	6481	0x1951	1	Signal #10 Type	Byte	0 - Undefined	0 - Undefined,1 - Discrete,2 -	N/A	R/W	Type of the 4-th CAN output signal
406483	6482	0x1952	1	Signal #10 Source	Byte	0 - Not Connected	Drop List	N/A	R/W	Input signal source of the 4-th CAN output signal
406484	6483	0x1953	1	Signal #10 Source Number	Byte	1	Depends on control source	N/A	R/W	
406485	6484	0x1954	1	Signal #10 Size	Byte	1	0...32	bit	R/W	Size of the 1-st CAN output signal
406486	6485	0x1955	1	Signal #10 Byte Position	Byte	1	0...8	N/A	R/W	Byte position of the 1-st CAN output signal
406487	6486	0x1956	1	Signal #10 Bit Position	Byte	1	0...8	N/A	R/W	Bit position of the 1-st CAN output signal
406488	6487	0x1957	2	Signal #10 Resolution	Float	1	Any value	signal units / bit	R/W	Resolution of the 4-th CAN continuous output signal
406490	6489	0x1959	2	Signal #10 Offset	Float	0	Any value	signal units	R/W	Offset of the 4-th CAN continuous output signal
406492	6491	0x195B	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Modbus Receive #1</b>										
406500	6499	0x1963	1	Signal Enabled	Byte	0	0...1	N/A	R/W	Modbus Receive Signal Enabled
406501	6500	0x1964	1	Register Address	Word			N/A	R/W	Input Register
406502	6501	0x1965	2	Data Minimum	Float	0	Any positive float	N/A	R/W	
406504	6503	0x1967	2	Data Maximum	Float	100	Any positive float	N/A	R/W	
406506	6505	0x1969	1	Data Size in Bits	Byte	8	0..16	N/A	R/W	
406507	6506	0x196A	1	Data Position Bit	Byte	0	0..15	N/A	R/W	
406508	6507	0x196B	2	Resolution	Float	1	Any positive float	N/A	R/W	
406510	6509	0x196D	2	Offset	Float	0	Any float value	N/A	R/W	
406512	6511	0x196F	1	Autoreset Time	Word	0	0...60000	ms	R/W	
406513	6512	0x1970	7	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Modbus Receive #2</b>										
406520	6519	0x1977	1	Signal Enabled	Byte	0	0...1	N/A	R/W	Modbus Receive Signal Enabled
406521	6520	0x1978	1	Register Address	Word			N/A	R/W	Input Register
406522	6521	0x1979	2	Data Minimum	Float	0	Any positive float	N/A	R/W	
406524	6523	0x197B	2	Data Maximum	Float	100	Any positive float	N/A	R/W	
406526	6525	0x197D	1	Data Size in Bits	Byte	8	0..16	N/A	R/W	
406527	6526	0x197E	1	Data Position Bit	Byte	0	0..15	N/A	R/W	
406528	6527	0x197F	2	Resolution	Float	1	Any positive float	N/A	R/W	
406530	6529	0x1981	2	Offset	Float	0	Any float value	N/A	R/W	
406532	6531	0x1983	1	Autoreset Time	Word	0	0...60000	ms	R/W	
406533	6532	0x1984	7	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Modbus Receive #3</b>										
406540	6539	0x198B	1	Signal Enabled	Byte	0	0...1	N/A	R/W	Modbus Receive Signal Enabled
406541	6540	0x198C	1	Register Address	Word			N/A	R/W	Input Register
406542	6541	0x198D	2	Data Minimum	Float	0	Any positive float	N/A	R/W	
406544	6543	0x198F	2	Data Maximum	Float	100	Any positive float	N/A	R/W	
406546	6545	0x1991	1	Data Size in Bits	Byte	8	0..16	N/A	R/W	
406547	6546	0x1992	1	Data Position Bit	Byte	0	0..15	N/A	R/W	
406548	6547	0x1993	2	Resolution	Float	1	Any positive float	N/A	R/W	
406550	6549	0x1995	2	Offset	Float	0	Any float value	N/A	R/W	
406552	6551	0x1997	1	Autoreset Time	Word	0	0...60000	ms	R/W	

406553	6552	0x1998	7	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Modbus Receive 4</b>										
406560	6559	0x199F	1	Signal Enabled	Byte	0	0..1	N/A	R/W	Modbus Receive Signal Enabled
406561	6560	0x19A0	1	Register Address	Word			N/A	R/W	Input Register
406562	6561	0x19A1	2	Data Minimum	Float	0	Any positive float	N/A	R/W	
406564	6563	0x19A3	2	Data Maximum	Float	100	Any positive float	N/A	R/W	
406566	6565	0x19A5	1	Data Size in Bits	Byte	8	0..16	N/A	R/W	
406567	6566	0x19A6	1	Data Position Bit	Byte	0	0..15	N/A	R/W	
406568	6567	0x19A7	2	Resolution	Float	1	Any positive float	N/A	R/W	
406570	6569	0x19A9	2	Offset	Float	0	Any float value	N/A	R/W	
406572	6571	0x19AB	1	Autoreset Time	Word	0	0..60000	ms	R/W	
406573	6572	0x19AC	7	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Modbus Receive 5</b>										
406580	6579	0x19B3	1	Signal Enabled	Byte	0	0..1	N/A	R/W	Modbus Receive Signal Enabled
406581	6580	0x19B4	1	Register Address	Word			N/A	R/W	Input Register
406582	6581	0x19B5	2	Data Minimum	Float	0	Any positive float	N/A	R/W	
406584	6583	0x19B7	2	Data Maximum	Float	100	Any positive float	N/A	R/W	
406586	6585	0x19B9	1	Data Size in Bits	Byte	8	0..16	N/A	R/W	
406587	6586	0x19BA	1	Data Position Bit	Byte	0	0..15	N/A	R/W	
406588	6587	0x19BB	2	Resolution	Float	1	Any positive float	N/A	R/W	
406590	6589	0x19BD	2	Offset	Float	0	Any float value	N/A	R/W	
406592	6591	0x19BF	1	Autoreset Time	Word	0	0..60000	ms	R/W	
406593	6592	0x19C0	7	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Modbus Receive 6</b>										
406600	6599	0x19C7	1	Signal Enabled	Byte	0	0..1	N/A	R/W	Modbus Receive Signal Enabled
406601	6600	0x19C8	1	Register Address	Word			N/A	R/W	Input Register
406602	6601	0x19C9	2	Data Minimum	Float	0	Any positive float	N/A	R/W	
406604	6603	0x19CB	2	Data Maximum	Float	100	Any positive float	N/A	R/W	
406606	6605	0x19CD	1	Data Size in Bits	Byte	8	0..16	N/A	R/W	
406607	6606	0x19CE	1	Data Position Bit	Byte	0	0..15	N/A	R/W	
406608	6607	0x19CF	2	Resolution	Float	1	Any positive float	N/A	R/W	
406610	6609	0x19D1	2	Offset	Float	0	Any float value	N/A	R/W	
406612	6611	0x19D3	1	Autoreset Time	Word	0	0..60000	ms	R/W	
406613	6612	0x19D4	7	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Modbus Receive 7</b>										
406620	6619	0x19DB	1	Signal Enabled	Byte	0	0..1	N/A	R/W	Modbus Receive Signal Enabled
406621	6620	0x19DC	1	Register Address	Word			N/A	R/W	Input Register
406622	6621	0x19DD	2	Data Minimum	Float	0	Any positive float	N/A	R/W	
406624	6623	0x19DF	2	Data Maximum	Float	100	Any positive float	N/A	R/W	
406626	6625	0x19E1	1	Data Size in Bits	Byte	8	0..16	N/A	R/W	
406627	6626	0x19E2	1	Data Position Bit	Byte	0	0..15	N/A	R/W	
406628	6627	0x19E3	2	Resolution	Float	1	Any positive float	N/A	R/W	
406630	6629	0x19E5	2	Offset	Float	0	Any float value	N/A	R/W	
406632	6631	0x19E7	1	Autoreset Time	Word	0	0..60000	ms	R/W	
406633	6632	0x19E8	7	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Modbus Receive 8</b>										
406640	6639	0x19EF	1	Signal Enabled	Byte	0	0..1	N/A	R/W	Modbus Receive Signal Enabled
406641	6640	0x19F0	1	Register Address	Word			N/A	R/W	Input Register
406642	6641	0x19F1	2	Data Minimum	Float	0	Any positive float	N/A	R/W	
406644	6643	0x19F3	2	Data Maximum	Float	100	Any positive float	N/A	R/W	
406646	6645	0x19F5	1	Data Size in Bits	Byte	8	0..16	N/A	R/W	

406647	6646	0x19F6	1	Data Position Bit	Byte	0	0..15	N/A	R/W	
406648	6647	0x19F7	2	Resolution	Float	1	Any positive float	N/A	R/W	
406650	6649	0x19F9	2	Offset	Float	0	Any float value	N/A	R/W	
406652	6651	0x19FB	1	Autoreset Time	Word	0	0..60000	ms	R/W	
406653	6652	0x19FC	7	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Modbus Receive 9</b>										
406660	6659	0x1A03	1	Signal Enabled	Byte	0	0..1	N/A	R/W	Modbus Receive Signal Enabled
406661	6660	0x1A04	1	Register Address	Word			N/A	R/W	Input Register
406662	6661	0x1A05	2	Data Minimum	Float	0	Any positive float	N/A	R/W	
406664	6663	0x1A07	2	Data Maximum	Float	100	Any positive float	N/A	R/W	
406666	6665	0x1A09	1	Data Size in Bits	Byte	8	0..16	N/A	R/W	
406667	6666	0x1A0A	1	Data Position Bit	Byte	0	0..15	N/A	R/W	
406668	6667	0x1A0B	2	Resolution	Float	1	Any positive float	N/A	R/W	
406670	6669	0x1A0D	2	Offset	Float	0	Any float value	N/A	R/W	
406672	6671	0x1A0F	1	Autoreset Time	Word	0	0..60000	ms	R/W	
406673	6672	0x1A10	7	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Modbus Receive 10</b>										
406680	6679	0x1A17	1	Signal Enabled	Byte	0	0..1	N/A	R/W	Modbus Receive Signal Enabled
406681	6680	0x1A18	1	Register Address	Word			N/A	R/W	Input Register
406682	6681	0x1A19	2	Data Minimum	Float	0	Any positive float	N/A	R/W	
406684	6683	0x1A1B	2	Data Maximum	Float	100	Any positive float	N/A	R/W	
406686	6685	0x1A1D	1	Data Size in Bits	Byte	8	0..16	N/A	R/W	
406687	6686	0x1A1E	1	Data Position Bit	Byte	0	0..15	N/A	R/W	
406688	6687	0x1A1F	2	Resolution	Float	1	Any positive float	N/A	R/W	
406690	6689	0x1A21	2	Offset	Float	0	Any float value	N/A	R/W	
406692	6691	0x1A23	1	Autoreset Time	Word	0	0..60000	ms	R/W	
406693	6692	0x1A24	8	Reserved	N/A	N/A	N/A	N/A	RO	Reserved for future use. Reading results 0. Writing
<b>Modbus Receive Input Registers Pool</b>										
406701	6700	0x1A2C	100	Modbus Input Register	Word	N/A	0x0 - 0xFFFF	N/A	R/W	Modbus Receive function block can chose any addr